



Title of Change:	MC74VHC1G_GTXX Family Datasheet updates per FPCN22038X	
Effective date:	26 June 2019	
Contact information:	Contact your local ON Semiconductor Sales Office or <david.manley@onsemi.com>	
Type of notification:	This Product Bulletin is for notification purposes only. ON Semiconductor will proceed with implementation of this change upon publication of this Product Bulletin.	
Change Category:	☐ Wafer Fab ☐ Assembly Change ☐ Test Change ☒ Other Datasheet Change	
Change Sub-Category(s):	☐ Manufacturing Site Addition ☐ Material Change ☒ Datasheet/Product Doc change ☐ Manufacturing Site Transfer ☐ Product specific change ☐ Shipping/Packaging/Marking ☐ Manufacturing Process Change ☐ Other: _____	
Sites Affected:	ON Semiconductor Sites: None	External Foundry/Subcon Sites: None
Description and Purpose: This PB is issued to notify customers of datasheet changes for the MC74VHC1G_GT family per FPCN22038X. Provided here are comparison between the new and old datasheets regarding changing specifications and/or specification conditions. • Areas of change are circled red. • Items from the old datasheet that will be changed are highlighted red. • The corresponding value on the new datasheet is highlighted in green. • Areas of change circled in yellow are changes that were found to be needed after the PCN There will be other changes that represent a cleanup and standardization to the datasheet to represent a family oriented specification format. These changes will include forms of the following: • Correction of clerical errors such as spelling. • Formatting to create family standards. • Combined the CMOS level and TTL level devices to the same datasheet. • Addition of new package types and possible removal of packages no longer available. • Standardization of the switching waveforms test circuit figures. • Formatting of the Device ordering information to provide more information to the customer regarding marking and Pin 1 orientation in tape or reel. • Removed references to voltages above recommended operating conditions. Existing datasheet The MC74VHC1GXX input structure provides protection when voltages up to 7.0 V are applied, regardless of the supply voltage. This allows the MC74VHC1GXX to be used to interface 5.0 V circuits to 3.0 V circuits. The internal circuit is composed of multiple stages, including an open drain output which provides the ability to set output switching level. This allows the MC74VHC1GXX to be used to interface 5V circuits to circuits of any voltage between V_{CC} and 7 V using an external resistor and power supply. The MC74VHC1GXX input structure provides protection when voltages up to 7 V are applied, regardless of the supply voltage. New The input structures provide protection when voltages up to 5.5 V are applied, regardless of the supply voltage. This allows the device to be used to interface 5 V circuits to 3 V circuits. The input structures provide protection when voltages up to 5.5 V are applied, regardless of the supply voltage. This allows the device to be used to interface 5 V circuits to 3 V circuits. The output structures also provide protection when V_{CC} = 0 V and when the output voltage exceeds V_{CC}. These input and output structures help prevent device destruction caused by supply voltage – input/output voltage mismatch, battery backup, hot insertion, etc.		



- Maximum voltage rating changed from 7.0 volts to 6.5 volts (Excluding Automotive Devices)
- Thermal resistance and Power Dissipation adjusted to reflect new die.
- Electrostatic Discharge/Latchup adjusted to align with JEDEC Standard.

Existing datasheet

MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CC}	DC Supply Voltage	-0.5 to +7.0	V
V _{IN}	DC Input Voltage	-0.5 to +7.0	V
V _{OUT}	DC Output Voltage	-0.5 to V _{CC} + 0.5	V
I _{IK}	DC Input Diode Current	-20	mA
I _{OK}	DC Output Diode Current	±20	mA
I _{OUT}	DC Output Sink Current	±12.5	mA
I _{CC}	DC Supply Current per Supply Pin	±25	mA
T _{STG}	Storage Temperature Range	-65 to +150	°C
T _L	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
T _J	Junction Temperature Under Bias	+150	°C
θ _{JA}	Thermal Resistance	350 230	°C/W
P _D	Power Dissipation in Still Air at 85°C	150 200	mW
MSL	Moisture Sensitivity	Level 1	
F _R	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in
V _{ESD}	ESD Withstand Voltage	> 2000 > 200	V
I _{LATCHUP}	Latchup Performance	±500	mA

New

MAXIMUM RATINGS

Symbol	Characteristics	Value	Unit
V _{CC}	DC Supply Voltage	-0.5 to +7.0 -0.5 to +6.5	V
V _{IN}	DC Input Voltage	-0.5 to +7.0 -0.5 to +6.5	V
V _{OUT}	DC Output Voltage	-0.5 to V _{CC} + 0.5 -0.5 to +7.0 -0.5 to +6.5	V
I _{IK}	DC Input Diode Current	V _{IN} < GND	-20
I _{OK}	DC Output Diode Current	V _{OUT} < GND	±20
I _{OUT}	DC Output Sink Current		±12.5
I _{CC} or I _{QND}	DC Supply Current per Supply Pin or Ground Pin		±25
T _{STG}	Storage Temperature Range	-65 to +150	°C
T _L	Lead Temperature, 1 mm from Case for 10 secs	260	°C
T _J	Junction Temperature Under Bias	+150	°C
θ _{JA}	Thermal Resistance (Note 2)	659 555 555 562 560 382	°C/W
P _D	Power Dissipation in Still Air	190 225 225 222 223 327	mW
MSL	Moisture Sensitivity	Level 1	
F _R	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in
V _{ESD}	ESD Withstand Voltage (Note 3)	2000 1000	V
I _{LATCHUP}	Latchup Performance (Note 4)	±100	mA

- DC Output Voltage adjusted to clarify over voltage tolerance protection.
- Input Rise and fall time adjusted to reflect new process.

Existing datasheet

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V _{CC}	DC Supply Voltage	2.0	5.5	V
V _{IN}	DC Input Voltage	0.0	5.5	V
V _{OUT}	DC Output Voltage	0.0	V _{CC}	V
T _A	Operating Temperature Range	-55	+125	°C
t _r , t _f	Input Rise and Fall Time	V _{CC} = 3.3 V ± 0.3 V V _{CC} = 5.0 V ± 0.5 V	0 100 20	ns/V

New

RECOMMENDED OPERATING CONDITIONS

Symbol	Characteristics	Min	Max	Unit
V _{CC}	Positive DC Supply Voltage	1.65	5.5	V
V _{IN}	DC Input Voltage	0	5.5	V
V _{OUT}	DC Output Voltage	0	V _{CC}	V
T _A	Operating Temperature Range	-55	+125	°C
t _r , t _f	Input Rise and Fall Time (SC-88A (NLV), UDFN6, SOT-553)	V _{CC} = 3.0 V to 3.6 V V _{CC} = 4.5 V to 5.5 V	0 100 20	ns/V
	Input Rise and Fall Time (SC-74A, SC-88A, SOT-953)	V _{CC} = 1.65 V to 1.95 V V _{CC} = 2.3 V to 2.7 V V _{CC} = 3.0 V to 3.6 V V _{CC} = 4.5 V to 5.5 V	0 20 20 5	ns/V

- Positive Threshold Voltages adjusted to remove minimum limits. Negative Threshold Voltages adjusted to remove maximum limits.
- Maximum Input Leakage Current adjusted to remove crossover with Power Off Leakage Current specification on all datasheets.

Existing datasheet

DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Test Conditions	T _A = 25°C			T _A = 85°C		-55 ≤ T _A ≤ 125°C		Unit	
			Min	Typ	Max	Min	Max	Min	Max		
V _{T+}	Positive Threshold Voltage		3.0	1.2	2.0	2.20	2.20	2.20	2.20	V	
		4.5	1.75	3.0	3.15	3.15	3.15	3.15			
		5.5	2.15	3.6	3.85	3.85	3.85	3.85			
V _{T-}	Negative Threshold Voltage		3.0	0.9	1.5	1.5	0.9	0.9	1.35	V	
			4.5	1.35	2.3	2.75	1.35	1.35	1.35		
			5.5	1.65	2.9	3.35	1.65	1.65	1.65		
V _H	Hysteresis Voltage		3.0	0.50	0.57	1.20	0.50	1.20	0.50	1.20	V
			4.5	0.40	0.47	1.00	0.40	1.00	0.40	1.00	
			5.5	0.50	0.74	1.60	0.50	1.60	0.50	1.60	
V _{OH}	Minimum High-Level Output Voltage	V _{IN} ≤ V _{T+} - Min I _{OH} = -50 μA	2.0	1.9	2.0	1.9	1.9	1.9	1.9	V	
		I _{OH} = -4 mA	3.0	2.9	3.0	2.9	2.9	2.9	2.9		
		I _{OH} = -8 mA	4.5	4.4	4.5	4.4	4.4	4.4	4.4		
V _{OL}	Maximum Low-Level Output Voltage	V _{IN} ≥ V _{T+} + Max I _{OL} = 50 μA	2.0	0.0	0.1	0.1	0.1	0.1	0.1	V	
		I _{OL} = 4 mA	3.0	0.0	0.1	0.1	0.1	0.1	0.1		
		I _{OL} = 8 mA	4.5	0.0	0.1	0.1	0.1	0.1	0.1		
I _{IN}	Maximum Input Leakage Current	V _{IN} = 5.5 V or GND	0 to 5.5		±0.1	±1.0		±1.0	±1.0	μA	
I _{CC}	Maximum Quiescent Supply Current	V _{IN} = V _{CC} or GND	5.5		1.0	20		40	μA		

New

DC ELECTRICAL CHARACTERISTICS (MC74VHC1GT14)

Symbol	Parameter	Test Conditions	V _{CC}	T _A = 25°C			-40°C ≤ T _A ≤ 85°C		-55°C ≤ T _A ≤ 125°C		Unit
			Min	Typ	Max	Min	Max	Min	Max		
V _{T+}	Positive Threshold Voltage		3.0	1.40	1.60	1.6	1.6	1.6	1.6	V	
			4.5	1.74	2.00	2.0	2.0	2.0	2.0		
			5.5	1.94	2.10	2.1	2.1	2.1	2.1		
V _{T-}	Negative Threshold Voltage		3.0	0.35	0.76	0.35	0.35	0.35	0.35	V	
			4.5	0.6	1.01	0.5	0.5	0.5	0.5		
			5.5	0.6	1.13	0.6	0.6	0.6	0.6		
V _H	Hysteresis Voltage		3.0	0.30	0.84	1.20	0.30	1.20	0.30	1.20	V
			4.5	0.40	0.73	1.40	0.40	1.40	0.40	1.40	
			5.5	0.50	0.81	1.60	0.50	1.60	0.50	1.60	
V _{OH}	High-Level Output Voltage	V _{IN} = V _{T+} or V _{IL} I _{OH} = -50 μA I _{OH} = -50 μA I _{OH} = -4 mA I _{OH} = -8 mA	2.0	1.9	2.0	1.9	1.9	1.9	1.9	V	
			3.0	2.9	3.0	2.9	2.9	2.9	2.9		
			4.5	4.4	4.5	4.4	4.4	4.4	4.4		
			5.5	5.4	5.5	5.4	5.4	5.4	5.4		
			4.5	3.94	-	3.80	-	3.66	-		
V _{OL}	Low-Level Output Voltage	V _{IN} = V _{T+} or V _{IL} I _{OL} = 50 μA I _{OL} = 50 μA I _{OL} = 50 μA I _{OL} = 4 mA I _{OL} = 8 mA	2.0	0.0	0.1	0.1	0.1	0.1	0.1	V	
			3.0	0.0	0.1	0.1	0.1	0.1	0.1		
			4.5	0.0	0.1	0.1	0.1	0.1	0.1		
			5.5	0.0	0.1	0.1	0.1	0.1	0.1		
			4.5	0.0	0.36	0.44	0.44	0.52	0.52		
			4.5	0.0	0.36	0.44	0.44	0.52	0.52		
I _{IN}	Input Leakage Current	V _{IN} = 5.5 V or GND	1.65 to 5.5	±0.1*	±1.0	±1.0	±1.0	±1.0	±1.0	μA	
I _{OFF}	Power Off Leakage Current	V _{IN} = 5.5 V or V _{OUT} = 5.5 V	0	-	1.0	-	10	-	10	μA	
I _{CC}	Quiescent Supply Current	V _{IN} = V _{CC} or GND	5.5	-	1.0	-	20	-	40	μA	
I _{OUT}	Increase in Quiescent Supply Current per Input Pin	One Input: V _{IN} = 3.4 V; Other Input at V _{CC} or GND	5.5	-	1.35	-	1.5	-	1.65	mA	



- Minimum limits will be removed on all propagation delay, Output Enable time and Output Disable Time specifications.

Existing datasheet

AC ELECTRICAL CHARACTERISTICS $C_{load} = 50$ pF, Input $t_{f/t_r} = 3.0$ ns

Symbol	Parameter	Test Conditions	$T_A = 25^\circ\text{C}$			$T_A \leq 85^\circ\text{C}$		$-55^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$		Unit
			Min	Typ	Max	Min	Max	Min	Max	
t_{PLH} t_{PLH}	Maximum Propagation Delay, A or B to Y	$V_{CC} = 3.3 \pm 0.3\text{ V}$ $R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	4.6	11.9	15.4	1.0	14.0	1.0	16.1	ns
		$V_{CC} = 5.0 \pm 0.5\text{ V}$ $R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	3.6	7.7	9.7	1.0	9.0	1.0	10.3	

New

AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 3.0$ ns)

Symbol	Parameter	Conditions	V_{CC} (V)	$T_A = 25^\circ\text{C}$			$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$		$-55^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$		Unit
				Min	Typ	Max	Min	Max	Min	Max	
t_{PLH} t_{PLH}	Propagation Delay, A to Y (Figures 3 and 4)	$C_L = 15\text{ pF}$	3.0 to 3.6	—	4.6	11.9	—	14.0	—	16.1	ns
		$C_L = 50\text{ pF}$	—	—	6.1	15.4	—	17.5	—	19.6	
		$C_L = 15\text{ pF}$	4.5 to 5.5	—	3.6	7.7	—	9.0	—	10.3	
		$C_L = 50\text{ pF}$	—	—	4.3	9.7	—	11.0	—	12.3	

AC ELECTRICAL CHARACTERISTICS $C_{load} = 50$ pF, Input $t_{f/t_r} = 3.0$ ns

Symbol	Parameter	Test Conditions	$T_A = 25^\circ\text{C}$			$T_A \leq 85^\circ\text{C}$		$-55^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$		Unit
			Min	Typ	Max	Min	Max	Min	Max	
t_{PZL}	Maximum Output Enable Time, A or B to Y	$V_{CC} = 3.3 \pm 0.3\text{ V}$ $R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	7.6	11.9	15.4	1.0	14.0	1.0	16.1	ns
		$V_{CC} = 5.0 \pm 0.5\text{ V}$ $R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	4.9	7.7	9.7	1.0	9.0	1.0	10.3	
t_{PLZ}	Maximum Output Disable Time	$V_{CC} = 3.3 \pm 0.3\text{ V}$ $R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	10.1	15.4	—	17.5	—	—	—	ns
		$V_{CC} = 5.0 \pm 0.5\text{ V}$ $R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	6.4	9.7	—	11.0	—	—	—	

AC ELECTRICAL CHARACTERISTICS $C_{load} = 50$ pF, Input $t_{f/t_r} = 3.0$ ns

Symbol	Parameter	Test Conditions	$T_A = 25^\circ\text{C}$			$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$		$-55^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$		Unit
			Min	Typ	Max	Min	Max	Min	Max	
t_{PZL}	Maximum Output Enable Time, A or B to Y	$V_{CC} = 3.3 \pm 0.3\text{ V}$ $R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	—	7.6	11.9	—	14.0	—	16.1	ns
		$V_{CC} = 5.0 \pm 0.5\text{ V}$ $R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	—	4.9	7.7	—	9.0	—	10.3	
		$V_{CC} = 3.3 \pm 0.3\text{ V}$ $R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	—	10.1	15.4	—	17.5	—	—	
		$V_{CC} = 5.0 \pm 0.5\text{ V}$ $R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	—	6.4	9.7	—	11.0	—	12.3	

List of Affected Parts:

Note: Only the standard (off the shelf) part numbers are listed in the parts list. Any custom parts affected by this PCN are shown in the customer specific PCN addendum in the PCN email notification, or on the [PCN Customized Portal](#).

M74VHC1G125DFT1G
M74VHC1G125DFT2G
M74VHC1G126DFT1G
M74VHC1G126DFT2G
M74VHC1G132DFT1G
M74VHC1G132DFT2G
M74VHC1G135DFT1G
M74VHC1G135DFT2G
M74VHC1GT00DFT1G
M74VHC1GT00DFT2G
M74VHC1GT02DFT1G
M74VHC1GT02DFT2G
M74VHC1GT04DFT1G
M74VHC1GT04DFT2G
M74VHC1GT04DFT3G
M74VHC1GT08DFT1G
M74VHC1GT08DFT2G
M74VHC1GT125DF1G
M74VHC1GT125DF2G
M74VHC1GT126DF1G

M74VHC1GT126DF2G
M74VHC1GT14DFT1G
M74VHC1GT14DFT2G
M74VHC1GT32DFT1G
M74VHC1GT32DFT2G
M74VHC1GT50DFT1G
M74VHC1GT50DFT2G
M74VHC1GT86DFT1G
M74VHC1GT86DFT2G
M74VHC1GU04DFT1G
M74VHC1GU04DFT2G
MC74VHC1G00DFT1G
MC74VHC1G00DFT2G
MC74VHC1G01DFT1G
MC74VHC1G01DFT2G
MC74VHC1G02DFT1G
MC74VHC1G02DFT2G
MC74VHC1G03DFT1G
MC74VHC1G03DFT2G
MC74VHC1G04DFT1G

MC74VHC1G04DFT2G
MC74VHC1G05DFT1G
MC74VHC1G05DFT2G
MC74VHC1G07DFT1G
MC74VHC1G07DFT2G
MC74VHC1G08DFT1G
MC74VHC1G08DFT2G
MC74VHC1G09DFT1G
MC74VHC1G09DFT2G
MC74VHC1G125DFT1G
MC74VHC1G14DFT1G
MC74VHC1G14DFT2G
MC74VHC1G32DFT1G
MC74VHC1G32DFT2G
MC74VHC1G50DFT1G
MC74VHC1G50DFT2G
MC74VHC1G86DFT1G
MC74VHC1G86DFT2G
MC74VHC1GU04DF1G
NL17VHC1GT50DF1G

Japanese translation of the notification starts here.
通知の日本語訳はここから始まります。

Note: *The Japanese version is for reference only. In case of any differences between the English and Japanese version, the English version shall control.*

注：日本語版は参照用です。英語版と日本語版の違いがある場合は、英語版が優先されます。



変更件名:	MC74VHC1G_GTXX ファミリー FPCN22038X についてのデータシートの更新	
発効日:	26 June 2019	
連絡先情報:	現地のオン・セミコンダクター営業所または <david.manley@onsemi.com>にお問い合わせください。	
通知種別:	本製品速報は通知目的のみのものです。オン・セミコンダクターは本製品速報の発行により本変更を実行します。	
変更カテゴリ:	☐ ウェハファブの変更 ☐ アセンブリの変更 ☐ 試験の変更 ☒ その他 <u>データシートの変更</u>	
変更サブカテゴリ:	☐ 製造拠点の追加 ☐ 材料の変更 ☒ データシート/製品資料の変更 ☐ 製造拠点の移転 ☐ 製品仕様の変更 ☐ 出荷/パッケージング/表記 ☐ 製造プロセスの変更 ☐ その他: _____	
影響を受ける拠点:	オン・セミコンダクター拠点: なし	外部製造工場 / 下請業者拠点: なし
説明および目的: 本 PB は、MC74VHC1G_GT ファミリー FPCN22038X についてのデータシートの変更をお客様にお知らせするものです。 仕様および/または仕様条件の変更に関する新旧データシート間での比較を以下に示します。 - 変更箇所は赤色の丸で囲まれています。 - 旧データシートから変更される項目は赤色でハイライトされています。 - 新データシートで対応する値は緑色でハイライトされています。 - 黄色の丸で囲んだ変更箇所は PCN の後に変更が必要であることがわかったものです 他にも、ファミリーに合わせて仕様フォーマットを表現するためにデータシートの整理と標準化をしたことによる変更があります。これらの変更は、以下のよう形で行われます。 - スペルなどの事務的なミスの訂正。 - ファミリーの標準を作成するための書式設定。 - CMOS レベルと TTL レベルのデバイスを同じデータシートに統合。 - 新しいパッケージタイプの追加、および入手できなくなったパッケージの削除見込み。 - 波形切り替え試験回路図の標準化。 - マーキング、およびテープまたはリールでのピン 1 の向きに関して、お客様にさらに情報を提供するための、デバイス注文情報の書式設定。 推奨動作条件を超える電圧へのリファレンスを削除。		
Existing datasheet The MC74VHC1GXX input structure provides protection when voltages up to 7.0 V are applied, regardless of the supply voltage. This allows the MC74VHC1GXX to be used to interface 5.0 V circuits to 3.0 V circuits. The internal circuit is composed of multiple stages, including an open drain output which provides the ability to set output switching level. This allows the MC74VHC1GXX to be used to interface 5V circuits to circuits of any voltage between V_{CC} and 7 V using an external resistor and power supply. The MC74VHC1GXX input structure provides protection when voltages up to 7 V are applied, regardless of the supply voltage. New The input structures provide protection when voltages up to 5.5 V are applied, regardless of the supply voltage. This allows the device to be used to interface 5 V circuits to 3 V circuits. The input structures provide protection when voltages up to 5.5 V are applied, regardless of the supply voltage. This allows the device to be used to interface 5 V circuits to 3 V circuits. The output structures also provide protection when $V_{CC} = 0$ V and when the output voltage exceeds V_{CC}. These input and output structures help prevent device destruction caused by supply voltage – input/output voltage mismatch, battery backup, hot insertion, etc.		



- 最大定格電圧が 7.0V から 6.5V に変更 (車載デバイスを除く)
- 新規のダイを反映するために熱抵抗とワット損を調整。
- JEDEC 規格に合わせるために静電放電/ラッチアップを調整。

Existing datasheet

MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V_{CC}	DC Supply Voltage	-0.5 to +7.0	V
V_{IN}	DC Input Voltage	-0.5 to +7.0	V
V_{OUT}	DC Output Voltage	-0.5 to $V_{CC} + 0.5$	V
I_{IK}	DC Input Diode Current	-20	mA
I_{OK}	DC Output Diode Current	±20	mA
I_{OUT}	DC Output Sink Current	±12.5	mA
I_{CC}	DC Supply Current per Supply Pin	±25	mA
T_{STG}	Storage Temperature Range	-65 to +150	°C
T_L	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
T_J	Junction Temperature Under Bias	+150	°C
θ_{JA}	Thermal Resistance	350 230	°C/W
P_D	Power Dissipation in Still Air at 85°C	150 200	mW
MSL	Moisture Sensitivity	Level 1	
F_R	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in
V_{ESD}	ESD Withstand Voltage	>2000 >200 N/A	V
$I_{LATCHUP}$	Latchup Performance	±500	mA

New

MAXIMUM RATINGS

Symbol	Characteristics	Value	Unit
V_{CC}	DC Supply Voltage	TSOP-5, SC-88A (NLV) SC-74A, SC-88A, UDFN6, SOT-553, SOT-953	-0.5 to +7.0 -0.5 to +6.5
V_{IN}	DC Input Voltage	TSOP-5, SC-88A (NLV) SC-74A, SC-88A, UDFN6, SOT-553, SOT-953	-0.5 to +7.0 -0.5 to +6.5
V_{OUT}	DC Output Voltage	Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode ($V_{CC} = 0$ V)	-0.5 to $V_{CC} + 0.5$ -0.5 to +7.0 -0.5 to +6.5
	DC Output Voltage	Active-Mode (High or Low State) SC-74A, SC-88A, UDFN6, SOT-553, SOT-953 Tri-State Mode (Note 1) Power-Down Mode ($V_{CC} = 0$ V)	-0.5 to $V_{CC} + 0.5$ -0.5 to +6.5 -0.5 to +6.5
I_{IK}	DC Input Diode Current	$V_{IN} < GND$	-20
I_{OK}	DC Output Diode Current	$V_{OUT} < GND$	±20
I_{OUT}	DC Output Source/Sink Current		±12.5
I_{CC} or I_{QDD}	DC Supply Current per Supply Pin or Ground Pin		±25
T_{STG}	Storage Temperature Range		-65 to +150
T_L	Lead Temperature, 1 mm from Case for 10 secs		260
T_J	Junction Temperature Under Bias		+150
θ_{JA}	Thermal Resistance (Note 2)	SC-88A SC-74A TSOP-5 SOT-553 SOT-953 UDFN6	659 555 555 562 560 382
P_D	Power Dissipation in Still Air	SC-88A SC-74A TSOP-5 SOT-553 SOT-953 UDFN6	190 225 225 222 223 327
MSL	Moisture Sensitivity		Level 1
F_R	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in
V_{ESD}	ESD Withstand Voltage (Note 3)	Human Body Model Charged Device Model	2000 1000
$I_{LATCHUP}$	Latchup Performance (Note 4)		±100

- OVT 保護について明確化するために DC 出力電圧を調整。
- 新規プロセスを反映するために入力立ち上がり/立ち下がり時間を調整。

Existing datasheet

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V_{CC}	DC Supply Voltage	2.0	5.5	V
V_{IN}	DC Input Voltage	0.0	5.5	V
V_{OUT}	DC Output Voltage	0.0	V_{CC}	V
T_A	Operating Temperature Range	-55	+125	°C
t_r, t_f	Input Rise and Fall Time	$V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$ $V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$	100 20	ns/V

New

RECOMMENDED OPERATING CONDITIONS

Symbol	Characteristics	Min	Max	Unit
V_{CC}	Positive DC Supply Voltage	1.65	5.5	V
V_{IN}	DC Input Voltage	0	5.5	V
V_{OUT}	DC Output Voltage	Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode ($V_{CC} = 0$ V)	0 V_{CC} 0	0 5.5 5.5
T_A	Operating Temperature Range	-55	+125	°C
t_r, t_f	Input Rise and Fall Time	$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$ SC-88A (NLV), UDFN6, SOT-553	0	100
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$	0	29
	Input Rise and Fall Time	$V_{CC} = 1.65 \text{ V to } 1.95 \text{ V}$	0	20
		$V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$	0	20
		$V_{CC} = 3.0 \text{ V to } 3.6 \text{ V}$	0	10
		$V_{CC} = 4.5 \text{ V to } 5.5 \text{ V}$	0	5

- Positive Threshold voltage の下限を削除。Negative Threshold voltage の上限を削除。
- すべてのデータシートの電源オフリーク電流仕様でクロスオーバーを除去したのに合わせて最大入力リーク電流を調整。

Existing datasheet

DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Test Conditions	V _{CC} (V)	T _A = 25 °C			T _A = 85 °C		-55 ≤ T _A ≤ 125 °C		Unit
				Min	Typ	Max	Min	Max	Min	Max	
V _{TH}	Positive Threshold Voltage		3.0	1.2	2.0	2.20	2.20	3.15	3.15	2.20	V
			4.5	1.75	3.0	3.15					
			5.5	2.15	3.8	3.85					
V _{TL}	Negative Threshold Voltage		3.0	0.9	1.5	1.5	0.9	1.35	1.35		V
			4.5	1.35	2.3	2.75					
			5.5	1.65	2.9	3.35					
V _{HI}	Hysteresis Voltage		3.0	0.30	0.57	1.20	0.30	1.20	0.30	1.20	V
			4.5	0.40	0.87	1.40					
			5.5	0.50	0.74	1.60					
V _{OH}	Minimum High-Level Output Voltage	V _{IN} ≥ V _{TH} - Min I _O H = -50 μA	2.0	1.9	2.0	1.9	1.9	2.0	1.9	2.0	V
			3.0	2.9	3.0	2.9					
			4.5	4.4	4.5	4.4					
V _{OL}	Maximum Low-Level Output Voltage	V _{IN} ≥ V _{TH} + Max I _O L = 50 μA	2.0	0.0	0.1	0.1	0.1	0.1	0.1	0.1	V
			3.0	0.0	0.1	0.1					
			4.5	0.0	0.1	0.1					
		I _O L = -4 mA I _O L = -8 mA	3.0	0.36	0.38	0.44	0.44	0.52	0.52	V	
			4.5		0.36						
I _{IN}	Maximum Input Leakage Current	V _{IN} = 5.5 V or GND	0 to 5.5			±0.1	±1.0	±1.0	±1.0	μA	
I _{CC}	Maximum Quiescent Supply Current	V _{IN} = V _{CC} or GND	5.5		1.0	20	40	40	40	μA	

New

DC ELECTRICAL CHARACTERISTICS (MC74VHC1G14T14)

Symbol	Parameter	Test Conditions	Vcc (V)	Ta = 25°C			-40°C ≤ Ta ≤ 85°C		-55°C ≤ Ta ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	Min	Max	
VTH	Positive Threshold Voltage		3.0	—	1.40	1.60	—	1.6	—	1.6	V
		4.5	—	1.74	2.00	—	2.0	—	2.0		
		5.5	—	1.94	2.10	—	2.1	—	2.1		
VTL	Negative Threshold Voltage		3.0	0.35	0.76	—	0.35	—	0.35	—	V
		4.5	0.5	1.01	—	0.5	—	0.5	—		
		5.5	0.6	1.13	—	0.6	—	0.6	—		
VH	Hysteresis Voltage		3.0	0.30	0.64	1.20	0.30	1.20	0.30	1.20	V
		4.5	0.40	0.73	1.40	0.40	1.40	0.40	1.40		
		5.5	0.50	0.81	1.60	0.50	1.60	0.50	1.60		
VOH	High-Level Output Voltage VIN = VIH or VIL IOH = -50 μA IOH = -50 μA IOH = -4 mA IOH = -8 mA	2.0	1.9	2.0	—	1.9	—	1.9	—	V	
		3.0	2.9	3.0	—	2.9	—	2.9	—		
		4.5	4.4	4.5	—	4.4	—	4.4	—		
		3.0	2.58	—	—	2.48	—	2.34	—		
		4.5	3.94	—	—	3.80	—	3.66	—		
VOL	Low-Level Output Voltage VIN = VIH or VIL IOL = 50 μA IOL = 50 μA IOL = 4 mA IOL = 8 mA	2.0	—	0.0	0.1	—	0.1	—	0.1	V	
		3.0	—	0.0	0.1	—	0.1	—	0.1		
		4.5	—	0.0	0.1	—	0.1	—	0.1		
		3.0	—	0.36	—	0.44	—	0.52	—		
		4.5	—	0.36	—	0.44	—	0.52	—		
IIN	Input Leakage Current VIN = 5.5 V or GND	1.65 to 5.5	—	±0.1*	—	±1.0	—	±1.0	—	μA	
IOFF	Power Off Leakage Current VIN = 5.5 V or VOUT = 5.5 V	0	—	1.0	—	10	—	10	—	μA	
ICC	Quiescent Supply Current VIN = VCC or GND	5.5	—	1.0	—	20	—	40	—	μA	
IOCT	Increase in Quiescent Supply Current per Input Pin One Input: VIN = 3.4 V; Other Input at VCC or GND	5.5	—	1.35	—	1.5	—	1.65	—	mA	



- すべての伝播遅延と出カインーブル時間と出力ディスエーブル時間の仕様から下限を削除。

Existing datasheet

AC ELECTRICAL CHARACTERISTICS $C_{load} = 50$ pF, Input $t_{f/t_r} = 3.0$ ns

Symbol	Parameter	Test Conditions	$T_A = 25^\circ\text{C}$			$T_A \leq 85^\circ\text{C}$			$-55^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$			Unit
			Min	Typ	Max	Min	Max	Min	Max	Min	Max	
t_{PLH} , t_{PHL}	Maximum Propagation Delay, A or B to Y	$V_{CC} = 3.3 \pm 0.3\text{ V}$ $C_L = 15\text{ pF}$ $R_L = 500\ \Omega$	4.6	11.9	1.0	14.0	1.0	16.1	1.0	16.1	19.6	ns
		$V_{CC} = 5.0 \pm 0.5\text{ V}$ $C_L = 15\text{ pF}$ $R_L = 500\ \Omega$	3.6	7.7	1.0	9.0	1.0	10.3	1.0	10.3	12.3	
			4.3	9.7	1.0	11.0	1.0	12.3	1.0	12.3	15.6	

New

AC ELECTRICAL CHARACTERISTICS (Input $t_{f/t_r} = 3.0$ ns)

Symbol	Parameter	Conditions	V_{CC} (V)	$T_A = 25^\circ\text{C}$			$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$			$-55^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$			Unit
				Min	Typ	Max	Min	Max	Min	Max	Min	Max	
t_{PLH} , t_{PHL}	Propagation Delay, A to Y (Figures 3 and 4)	$C_L = 15\text{ pF}$	3.0 to 3.6	–	4.6	11.9	–	14.0	–	16.1	–	19.6	ns
		$C_L = 50\text{ pF}$	–	6.1	15.4	–	17.5	–	19.6	–	19.6	–	
		$C_L = 15\text{ pF}$	4.5 to 5.5	–	3.6	7.7	–	9.0	–	10.3	–	10.3	
		$C_L = 50\text{ pF}$	–	4.3	9.7	–	11.0	–	12.3	–	12.3	–	

AC ELECTRICAL CHARACTERISTICS $C_{load} = 50$ pF, Input $t_{f/t_r} = 3.0$ ns

Symbol	Parameter	Test Conditions	$T_A = 25^\circ\text{C}$			$T_A \leq 85^\circ\text{C}$			$-55^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$			Unit
			Min	Typ	Max	Min	Max	Min	Max	Min	Max	
t_{ZL}	Maximum Output Enable Time, A or B to Y	$V_{CC} = 3.3 \pm 0.3\text{ V}$ $R_L = R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	7.6	11.9	1.0	14.0	1.0	16.1	1.0	16.1	19.6	ns
		$V_{CC} = 5.0 \pm 0.5\text{ V}$ $R_L = R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	4.9	7.7	1.0	9.0	1.0	10.3	1.0	10.3	12.3	
			6.4	9.7	1.0	11.0	1.0	12.3	1.0	12.3	15.6	
t_{PLZ}	Maximum Output Disable Time	$V_{CC} = 3.3 \pm 0.3\text{ V}$ $R_L = R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	10.1	15.4	–	17.5	–	19.6	–	19.6	–	ns
		$V_{CC} = 5.0 \pm 0.5\text{ V}$ $R_L = R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	6.4	9.7	–	11.0	–	12.3	–	12.3	–	
			–	–	–	–	–	–	–	–	–	

AC ELECTRICAL CHARACTERISTICS $C_{load} = 50$ pF, Input $t_{f/t_r} = 3.0$ ns

Symbol	Parameter	Test Conditions	$T_A = 25^\circ\text{C}$			$-40^\circ\text{C} \leq T_A \leq 85^\circ\text{C}$			$-55^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$			Unit
			Min	Typ	Max	Min	Max	Min	Max	Min	Max	
t_{ZL}	Maximum Output Enable Time, A or B to Y	$V_{CC} = 3.3 \pm 0.3\text{ V}$ $R_L = R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	–	7.6	11.9	–	14.0	–	16.1	–	19.6	ns
		$V_{CC} = 5.0 \pm 0.5\text{ V}$ $R_L = R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	–	4.9	7.7	–	9.0	–	10.3	–	12.3	
			–	6.4	9.7	–	11.0	–	12.3	–	15.6	
t_{PLZ}	Maximum Output Disable Time	$V_{CC} = 3.3 \pm 0.3\text{ V}$ $R_L = R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	–	10.1	15.4	–	17.5	–	19.6	–	19.6	ns
		$V_{CC} = 5.0 \pm 0.5\text{ V}$ $R_L = R_L = 500\ \Omega$ $C_L = 15\text{ pF}$ $C_L = 50\text{ pF}$	–	6.4	9.7	–	11.0	–	12.3	–	12.3	
			–	–	–	–	–	–	–	–	–	

影響を受ける部品の一覧:

注: 標準の部品番号(既製品)のみが部品一覧に記載されます。本 PCN に影響を受けるカスタム 部品は、PCN メールの顧客の特定の PCN の付属文書、または PCN カスタマイズポータルに記載されています。

M74VHC1G125DFT1G

M74VHC1G125DFT2G

M74VHC1G126DFT1G

M74VHC1G126DFT2G

M74VHC1G132DFT1G

M74VHC1G132DFT2G

M74VHC1G135DFT1G

M74VHC1G135DFT2G

M74VHC1GT00DFT1G

M74VHC1GT00DFT2G

M74VHC1GT02DFT1G

M74VHC1GT02DFT2G

M74VHC1GT04DFT1G

M74VHC1GT04DFT2G

M74VHC1GT04DFT3G

M74VHC1GT08DFT1G

M74VHC1GT08DFT2G

M74VHC1GT125DF1G

M74VHC1GT125DF2G

M74VHC1GT126DF1G

M74VHC1GT126DF2G

M74VHC1GT14DFT1G

M74VHC1GT14DFT2G

M74VHC1GT32DFT1G

M74VHC1GT32DFT2G

M74VHC1GT50DFT1G

M74VHC1GT50DFT2G

M74VHC1GT86DFT1G

M74VHC1GT86DFT2G

M74VHC1GU04DFT1G

M74VHC1GU04DFT2G

MC74VHC1G00DFT1G

MC74VHC1G00DFT2G

MC74VHC1G01DFT1G

MC74VHC1G01DFT2G

MC74VHC1G02DFT1G

MC74VHC1G02DFT2G

MC74VHC1G03DFT1G

MC74VHC1G03DFT2G

MC74VHC1G04DFT1G

MC74VHC1G04DFT2G

MC74VHC1G05DFT1G

MC74VHC1G05DFT2G

MC74VHC1G07DFT1G

MC74VHC1G07DFT2G

MC74VHC1G08DFT1G

MC74VHC1G08DFT2G

MC74VHC1G09DFT1G

MC74VHC1G09DFT2G

MC74VHC1G125DFT1G

MC74VHC1G14DFT1G

MC74VHC1G14DFT2G

MC74VHC1G32DFT1G

MC74VHC1G32DFT2G

MC74VHC1G50DFT1G

MC74VHC1G50DFT2G

MC74VHC1G86DFT1G

MC74VHC1G86DFT2G

MC74VHC1GU04DF1G

NL17VHC1GT50DF1G