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**PRODUCT BULLETIN # 20128**Generic Copy

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**Issue Date:** 01-Aug-2013**TITLE:** NCV7462 Data Sheet Update**PROPOSED FIRST SHIP DATE:** 01-Aug-2013**AFFECTED CHANGE CATEGORY(S):** Data Sheet only**FOR ANY QUESTIONS CONCERNING THIS NOTIFICATION:**

Contact your local ON Semiconductor sales office or Ondrej Kupcik &lt;Ondrej.kupcik@onsemi.com&gt;

**NOTIFICATION TYPE:**

ON Semiconductor considers this change approved unless specific conditions of acceptance are provided in writing. To do so, contact &lt;quality@onsemi.com&gt;.

**DESCRIPTION AND PURPOSE:**

Several corrections are made with the publication of the 1st revision of the NCV7462 data sheet:

- Figures updated:
  - Page 1: Marking Diagram
  - Figure 1: Block Diagram
  - Figure 3: Application Diagram
  - Figure 12: Principal Operating Modes
- Electrical characteristics updated:
  - Vop\_LS1/2 (recommended operating conditions) updated from "40 V" to "VS"
  - Parameters T\_wd\_TO and T\_wd\_trig (Window Watchdog) updated – limits added
  - Parameters LIN\_off\_dom, ILIN\_off\_dom\_slp, ILIN\_off\_rec, ILIN\_no\_GND, ILIN\_no\_VS moved from LIN Transmitter to LIN Receiver table updated; conditions updated
  - Vbus\_dom\_LIN, Vbus\_rec\_LIN (LIN Receiver DC Characteristics) parameters added
- Tape & Reel parameters updated:
  - Specified NCV7462DQ5R2G is delivered in 24mm Tape
- Typos corrected:
  - Pin 34,35 description corrected
  - D1-D4 conditions (LIN Transmitter Dynamic Characteristics)
  - Rpullup\_pinx, VinL\_FLASH conditions (TxDx/FLASH, SDI, SCLK, CSN Inputs)
  - ICMP\_STBY (CONTROL\_0), WU\_CAN\_DIS, WU\_LIN\_DIS (CONTROL\_1), WUx (STATUS\_1) SPI bits description

**List of affected General Parts:**

NCV7462DQ0R2G