



<b>Title of Change:</b>	LV52207NXB-VH datasheet change										
<b>Effective date:</b>	2 October 2018										
<b>Contact information:</b>	Contact your local ON Semiconductor Sales Office or Tsutomu Tanaka <Tsutomu.Tanaka@onsemi.com>, or Nobuyuki Otaka <Nobuyuki.Otaka@onsemi.com>										
<b>Type of notification:</b>	This Product Bulletin is for notification purposes only. ON Semiconductor will proceed with implementation of this change upon publication of this Product Bulletin.										
<b>Change Category:</b>	<input type="checkbox"/> Wafer Fab <input type="checkbox"/> Assembly Change <input type="checkbox"/> Test Change <input checked="" type="checkbox"/> Other <u>Datasheet</u>										
<b>Change Sub-Category(s):</b> <input type="checkbox"/> Manufacturing Site Addition <input type="checkbox"/> Material Change <input checked="" type="checkbox"/> Datasheet/Product Doc change <input type="checkbox"/> Manufacturing Site Transfer <input type="checkbox"/> Product specific change <input type="checkbox"/> Shipping/Packaging/Marking <input type="checkbox"/> Manufacturing Process Change <input type="checkbox"/> Other: _____											
<b>Sites Affected:</b>	ON Semiconductor Sites: None	External Foundry/Subcon Sites: None									
<b>Description and Purpose:</b> This PB announces the following datasheet changes for LV52207NXB-VH: <table border="1" data-bbox="121 1087 1510 1556"> <thead> <tr> <th></th> <th>Current</th> <th>New</th> </tr> </thead> <tbody> <tr> <td>Min. Duty% on PWM pin</td> <td>0.9%</td> <td>0.5%</td> </tr> <tr> <td>PWM DIMMING CONTROL Explanation</td> <td>None</td> <td> <b>PWM DIMMING CONTROL</b>  To avoid LED light off, 75uA offset per channel for PWL duty was designed.   <math display="block">I_{LED} = I_{LED\_FULL} * 0.00375 + (I_{LED\_FULL} - I_{LED\_FULL} * 0.00375) * PWMDUTY</math>   PWMDUTY: PWM pin DUTY  <math display="block">I_{LED\_FULL} * 0.00375 \cong 75\mu A</math>  *This formula is applicable at PWM=10KHz. When used the higher frequency, a gap gradually occurs. </td> </tr> </tbody> </table>				Current	New	Min. Duty% on PWM pin	0.9%	0.5%	PWM DIMMING CONTROL Explanation	None	<b>PWM DIMMING CONTROL</b> To avoid LED light off, 75uA offset per channel for PWL duty was designed.  $I_{LED} = I_{LED\_FULL} * 0.00375 + (I_{LED\_FULL} - I_{LED\_FULL} * 0.00375) * PWMDUTY$  PWMDUTY: PWM pin DUTY $I_{LED\_FULL} * 0.00375 \cong 75\mu A$ *This formula is applicable at PWM=10KHz. When used the higher frequency, a gap gradually occurs.
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The change will not impact form, fit, or function of product(s)											
<b>List of Affected Parts:</b>  LV52207NXB-VH											