

Issue Date: 15 July 2015

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Title of Change:	Copper wire conversion and mold compound change for LV5710V			
Proposed first ship date:	11 December 2015			
Contact information:	Contact your local ON Semiconductor Sales Office or <hidekazu.inoue@onsemi.com> <hiroshi.kojima@onsemi.com><takeshi2.hoshino@onsemi.com> <kazumi.onda@onsemi.com><kenya.liguchi@onsemi.com></kenya.liguchi@onsemi.com></kazumi.onda@onsemi.com></takeshi2.hoshino@onsemi.com></hiroshi.kojima@onsemi.com></hidekazu.inoue@onsemi.com>			
Samples:	Samples should be available after completion of qualification. Contact your local ON Semiconductor Sales Office or <jun.hasunuma@onsemi.com></jun.hasunuma@onsemi.com>			
Type of notification:	This is an Initial Product/Process Change Notification (IPCN) sent to customers. IPCNs are issued at least 120 days prior to implementation of the change. An IPCN is advance notification about an upcoming change and contains general information regarding the change details and devices affected. It also contains the preliminary reliability qualification plan. The completed qualification and characterization data will be included in the Final Product/Process Change Notification (FPCN). This IPCN notification will be followed by a Final Product/Process Change Notification (FPCN) at least 90 days prior to implementation of the change. In case of questions, contact <pcn.support@onsemi.com>.</pcn.support@onsemi.com>			
Change Part Identification:	Affected products will be identified with date code.			
Change category:	Wafer Fab Change Assembly Change Test Change Other			
Change Sub-Category(s):				
Sites Affected: All site(s) not applicable ON Semiconductor site(s) : External Foundry/Subcon site(s) ON Tarlac City, Philippines				
 Description and Purpose: This is an Initial Process Change Notification for below contents: Gold wire connecting chip and Lead will be changed to Copper wire. Mold resin will be changed to suitable for Copper wire. There is no change of the electrical characteristic specifications with this changing. 				
Qualification Plan: Estimated date for qualification completion: 31 July 2015 Package name :SSOP016 (225mil)				
Test Item		Test Condition	Test Time	
High Temperature Operating Life		Tj=Tjmax, Vcc=Operatingmax	1000hrs	
Temperature Humidity Bias *		Ta=85degC,RH=85%, Vcc=Recommended T	1000hrs	
Temperature Cycle *		Ta=-65degC(30min) ⇔ Ta=150degC (30min)	100cycles	
Pressure Cooker *		Ta=121degC,RH=100% ,205kPa	50hrs	
High Temperature Storage		Ta=150degC	1000hrs	
Resistance to Soldering heat (Reflow Soldering)		255degC,10s (Peak260degC)	2times	
Notes: The test items with * mark are put into operation after the reflow soldering (at 255degC for 10seconds) -> SMD Temperature Humidity Bias Test: PD>=0.1W -> Intermittent power application consists of 1h ON and 3h OFF.				

Judgment Criteria :

Judgment Criteria are due to the limits of the electrical characteristics in the detail specification



List of affected Standard Parts:				
Part Number	Qualification Vehicle			
LV5710V-TLM-E	LV5710V-TLM-E			