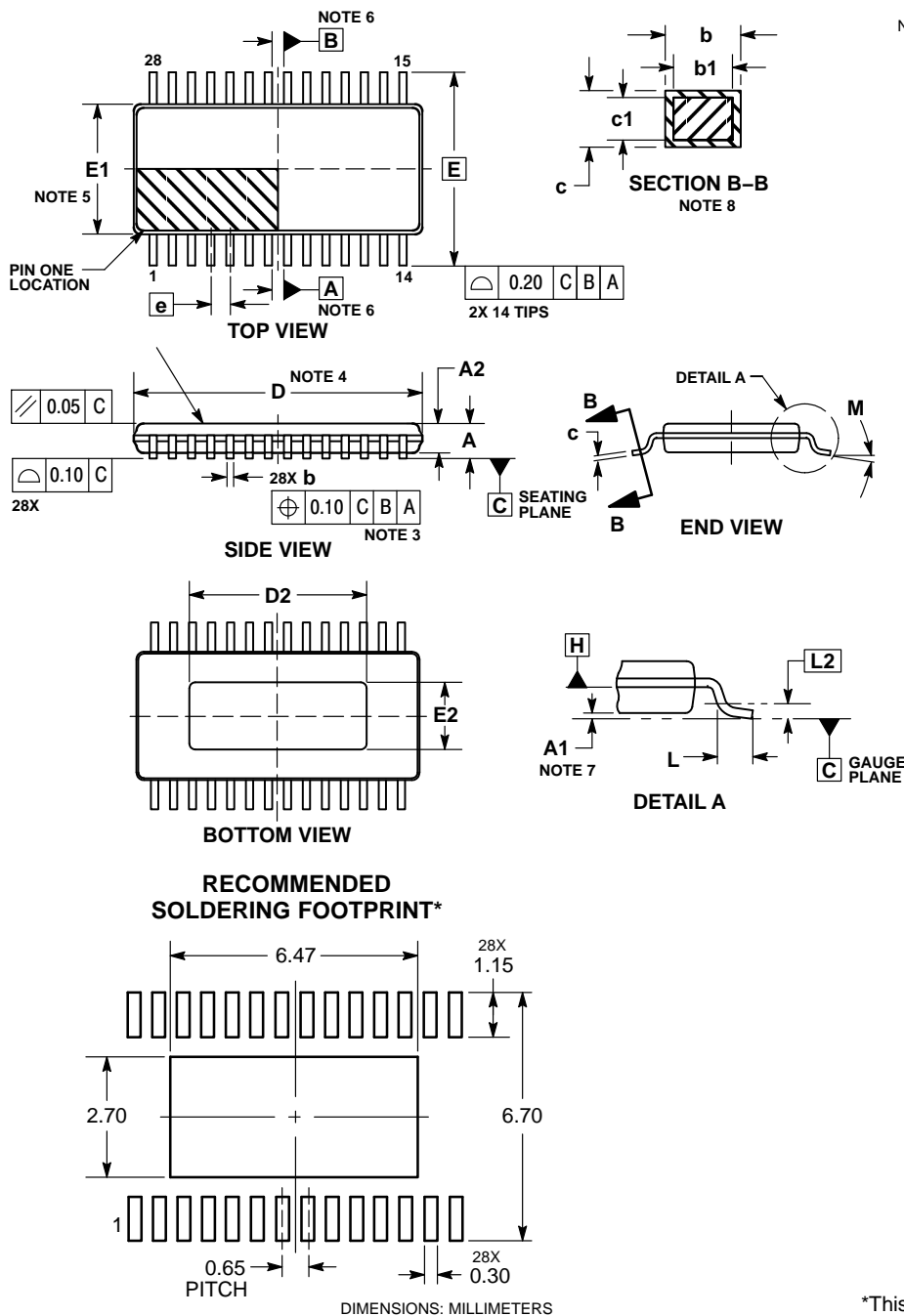




SCALE 1:1

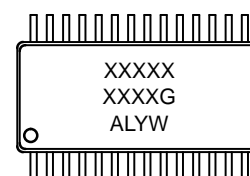
**TSSOP28 9.7x4.4 EP**  
**CASE 948BG**  
**ISSUE O**

DATE 08 FEB 2012


**NOTES:**

1. DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.
2. DIMENSIONS IN MILLIMETERS.
3. DIMENSION **b** DOES NOT INCLUDE DAMBAR PROTRUSION. DAMBAR PROTRUSION SHALL BE 0.07 MAX AT MAXIMUM MATERIAL CONDITION. DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT. MINIMUM SPACE BETWEEN PROTRUSION AND ADJACENT LEAD IS 0.07.
4. DIMENSION **D** DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.15 PER SIDE. DIMENSION **D** IS DETERMINED AT DATUM PLANE **H**.
5. DIMENSION **E1** DOES NOT INCLUDE INTER-LEAD FLASH OR PROTRUSIONS. INTER-LEAD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.25 PER SIDE. DIMENSION **E1** IS DETERMINED AT DATUM PLANE **H**.
6. DATUMS **A** AND **B** TO BE DETERMINED AT DATUM PLANE **H**.
7. **A1** IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY.
8. SECTION **B-B** TO BE DETERMINED AT 0.10 TO 0.25 FROM THE LEAD TIP.

MILLIMETERS		
DIM	MIN	MAX
<b>A</b>	—	1.20
<b>A1</b>	0.00	0.15
<b>A2</b>	0.80	1.05
<b>b</b>	0.19	0.30
<b>b1</b>	0.19	0.25
<b>c</b>	0.09	0.20
<b>c1</b>	0.09	0.16
<b>D</b>	9.60	9.80
<b>D2</b>	5.21	6.17
<b>E</b>	6.40	BSC
<b>E1</b>	4.30	4.50
<b>E2</b>	1.44	2.40
<b>e</b>	0.65	BSC
<b>L</b>	0.45	0.75
<b>L2</b>	0.25	BSC
<b>M</b>	0°	8°

**GENERIC MARKING DIAGRAM\***


**A** = Assembly Location  
**L** = Wafer Lot  
**Y** = Year  
**W** = Work Week  
**G** = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

\*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

<b>DOCUMENT NUMBER:</b>	<b>98AON67255E</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>TSSOP28 EP, 9.7X4.4MM, 0.65P</b>	<b>PAGE 1 OF 1</b>

**onsemi** and **onsemi** are trademarks of Semiconductor Components Industries, LLC dba **onsemi** or its subsidiaries in the United States and/or other countries. **onsemi** reserves the right to make changes without further notice to any products herein. **onsemi** makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. **onsemi** does not convey any license under its patent rights nor the rights of others.