

ILCC48 10x10 CASE 847AC **ISSUE B**

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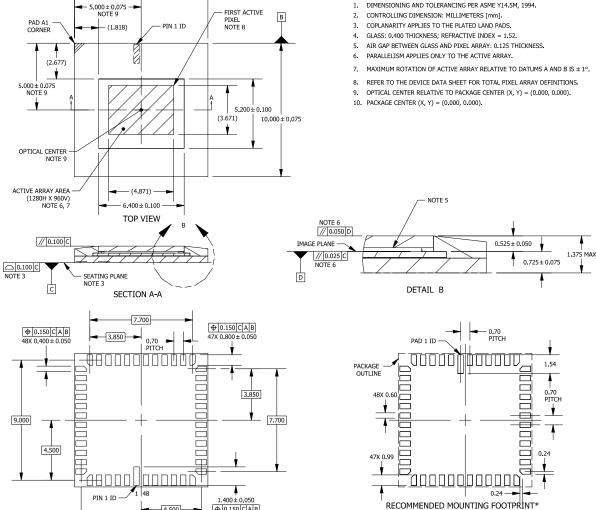
DATE 13 FEB 2020

NOTES

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

*FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD
THE ON SEMICONDUCTOR SOLDERING AND MOUNTING
TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

CONTROLLING DIMENSION: MILLIMETERS [mm]. 2.



GENERIC MARKING DIAGRAM*

9.000 **BOTTOM VIEW**

 10.000 ± 0.075



XXXX = Specific Device Code

= Year

Ф 0.150 C A В

ZZZ = Lot Traceability

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

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