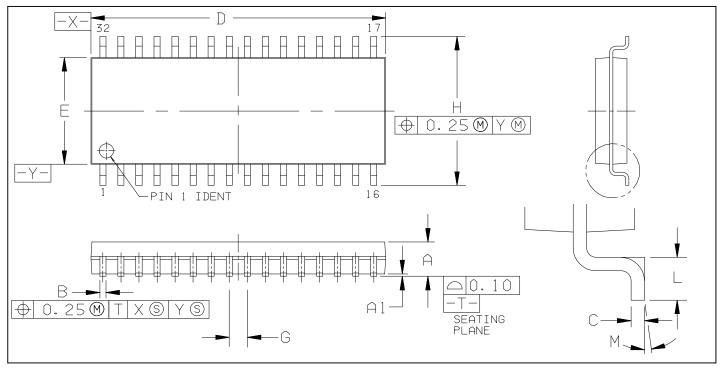
ON Semiconductor	MECHANICAL OUTLINES DICTIONARY	98A0N03783D	
ON SUMEORALE IS		PAGE 751P	
DO NOT SCALE THIS DWG	ALL APPROVAL SIGNATURES ON FILE IN DOCUMENT CENTRAL	ISSUE () SHEET 1 OF 2	



	MILLIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
A	2. 29	2.54		
A1	0.10	0.25		
В	0.36	0.51		
С	0.15	0.32		
D	20.57	20.88		
Е	7.42	7.60		
G	1.27 BSC			
Н	10.29	10.64		
L	0.53	1.04		
М	0°	8°		

CASE NO.	751P-01
STATUS	ON SEMICONDUCTOR STANDARD
NEW STD	
USED ON	SOIC WIDE BODY, 32 LEAD

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: MM
- 3. DIMENSIONS "D" AND "E" DO NOT INCLUDE MOLD PROTRUSION.
- 4. MAXIMUM MOLD PROTRUSION 0.15 PER SIDE.
- 5. DIMENSION "B" DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.13 TOTAL IN EXCESS OF "B" DIMENSION AT MAXIMUM MATERIAL CONDITION.



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SHEET 2 OF 2

ISSUE	REVISION	COORD/ DATE
		DATE
0	RELEASED FOR PRODUCTION. REQ. BY S. MOHAMMED	FB 02 NOV 00