

# MECHANICAL CASE OUTLINE

## PACKAGE DIMENSIONS

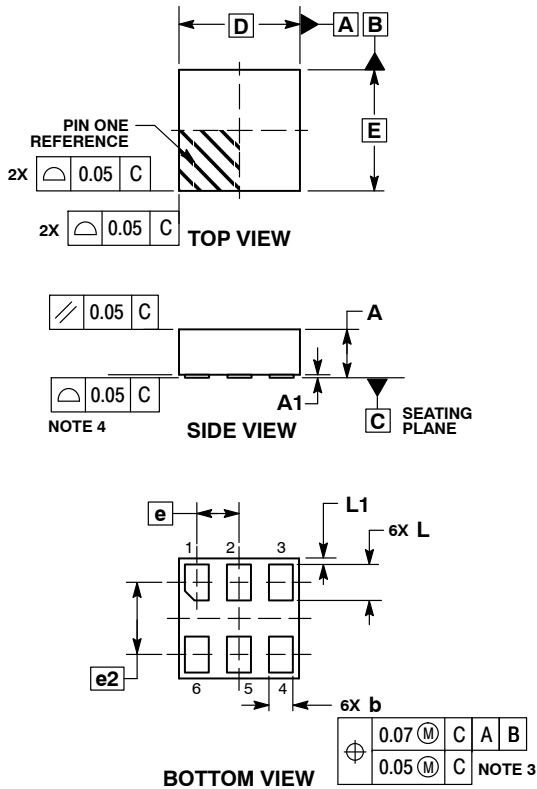
ON Semiconductor®



SCALE 4:1

**XLLGA6 1.00x1.00, 0.35P**  
**CASE 713AD**  
**ISSUE 0**

DATE 21 OCT 2014



**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO THE PLATED TERMINALS AND IS MEASURED BETWEEN 0.15 AND 0.25 MM FROM THE TERMINAL TIPS.
4. COPLANARITY APPLIES TO ALL OF THE TERMINALS.

MILLIMETERS		
DIM	MIN	MAX
A	---	0.40
A1	0.00	0.05
b	0.17	0.23
D	1.00 BSC	
E	1.00 BSC	
e	0.35 BSC	
e2	0.60 BSC	
L	0.27	0.33
L1	0.05 REF	

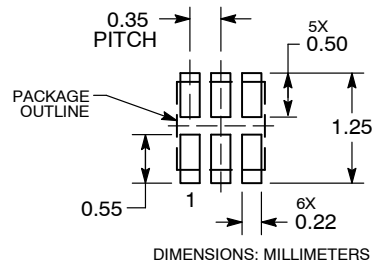
**GENERIC MARKING DIAGRAM\***



XX = Specific Device Code  
M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

**RECOMMENDED SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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<b>DESCRIPTION:</b>	<b>XLLGA6 1.00X1.00, 0.35P</b>	<b>PAGE 1 OF 1</b>

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