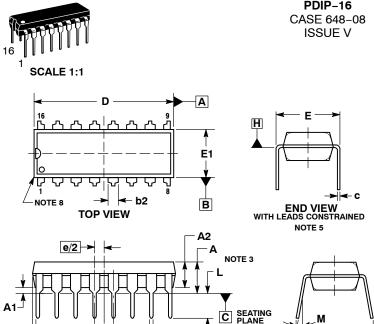


D1

е

SIDE VIEW



16X b

| + | 0.010 M | C | A M | B M

PDIP-16

eВ

END VIEW

NOTE 6

DATE 22 APR 2015

NOTES

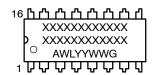
- DTES:
 DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: INCHES.
 DIMENSIONS A, A1 AND L ARE MEASURED WITH THE PACKAGE SEATED IN JEDEC SEATING PLANE GAUGE GS-3.
 DIMENSIONS D, D1 AND E1 DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS ARE NOT TO EXCEED 0.10 INCH.
 DIMENSION E1 SMEASURED AT A POINT 0.015 BELOW DATUM.
- DIMENSION E IS MEASURED AT A POINT 0.015 BELOW DATUM PLANE H WITH THE LEADS CONSTRAINED PERPENDICULAR TO DATUM C.
 DIMENSION eB IS MEASURED AT THE LEAD TIPS WITH THE
- DIMENSION BETS MEASURED AT THE LEAD TIPS WITH THE LEADS UNCONSTRAINED.

 DATUM PLANE H IS COINCIDENT WITH THE BOTTOM OF THE LEADS, WHERE THE LEADS EXIT THE BODY.

 PACKAGE CONTOUR IS OPTIONAL (ROUNDED OR SQUARE CONTOUR IS OPTIONAL)

	INCHES		MILLIM	ETERS
DIM	MIN	MAX	MIN	MAX
Α		0.210		5.33
A1	0.015		0.38	
A2	0.115	0.195	2.92	4.95
b	0.014	0.022	0.35	0.56
b2	0.060 TYP		1.52 TYP	
С	0.008	0.014	0.20	0.36
D	0.735	0.775	18.67	19.69
D1	0.005		0.13	
E	0.300	0.325	7.62	8.26
E1	0.240	0.280	6.10	7.11
е	0.100 BSC		2.54 BSC	
eB		0.430		10.92
L	0.115	0.150	2.92	3.81
M		10°		10°

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code = Assembly Location

WL = Wafer Lot

YY = Year WW = Work Week = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1:		STYLE 2	STYLE 2:		
PIN 1.	CATHODE	PIN 1.	COMMON DRAIN		
2.	CATHODE	2.	COMMON DRAIN		
3.	CATHODE	3.	COMMON DRAIN		
4.	CATHODE	4.	COMMON DRAIN		
5.	CATHODE	5.	COMMON DRAIN		
6.	CATHODE	6.	COMMON DRAIN		
7.	CATHODE	7.	COMMON DRAIN		
8.	CATHODE	8.	COMMON DRAIN		
9.	ANODE	9.	GATE		
10.	ANODE	10.	SOURCE		
11.	ANODE	11.	GATE		
12.	ANODE	12.	SOURCE		
13.	ANODE	13.	GATE		
14.	ANODE	14.	SOURCE		
15.	ANODE	15.	GATE		
16.	ANODE	16.	SOURCE		

DOCUMENT NUMBER:	98ASB42431B	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	PDIP-16		PAGE 1 OF 1	

onsemi and Onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries, onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.