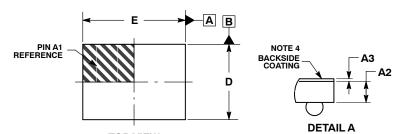
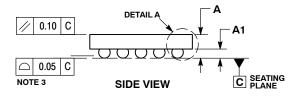




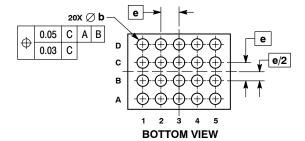
WLCSP20, 1.66x2.26x0.6 CASE 567PM ISSUE O

DATE 09 AUG 2016

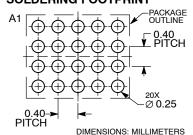




TOP VIEW



RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTES:

- DIMENSIONING AND TOLERANCING PER
 ASME Y14.5M. 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
- COPLANARITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS.
- 4. BACKSIDE COATING IS OPTIONAL.

	MILLIMETERS				
DIM	MIN	NOM	MAX		
Α			0.60		
A1	0.179	0.199	0.219		
A2	0.323	0.338	0.353		
АЗ	0.017	0.022	0.027		
b	0.241	0.271	0.301		
D	1.61	1.66	1.71		
E	2.21	2.26	2.31		
е	0.40 BSC				

GENERIC MARKING DIAGRAM*



A = Assembly Location

L = Wafer Lot Y = Year WW = Work Week

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

DOCUMENT NUMBER:	98AON14685G	Electronic versions are uncontrolled except when accessed directly from the Document Repositor Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
DESCRIPTION:	WLCSP20, 1.66X2.26X0.6		PAGE 1 OF 1

onsemi and ONSemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.