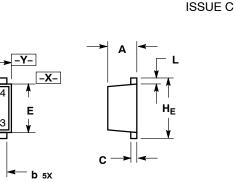
semi

SCALE 4:1

D

5

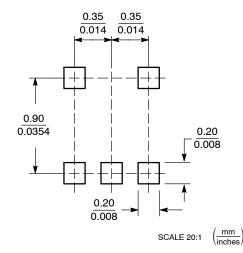
 \sim 2



SOLDERING FOOTPRINT*

0.08 X Y

 \oplus



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

DATE 30 JUL 2008

NOTES:

SOT-953 CASE 527AB

DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

2. CONTROLLING DIMENSION: MILLIMETERS 3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.34	0.42	0.50	0.013	0.017	0.020
b	0.10	0.15	0.20	0.004	0.006	0.008
С	0.05	0.10	0.15	0.002	0.004	0.006
D	0.95	1.00	1.05	0.037	0.039	0.041
Е	0.75	0.80	0.85	0.03	0.032	0.034
е	0.35 BSC		0.014 BSC			
L	0.05	0.10	0.15	0.002	0.004	0.006
ΗE	0.95	1.00	1.05	0.037	0.039	0.041

GENERIC **MARKING DIAGRAM***

Х = Specific Device Code

М = Month Code

*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot " .", may or may not be present.

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