0.10 C

8

9

PIN 1 INDICATOR





A

5

В

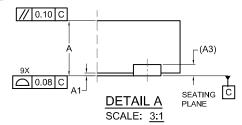
0.10 C

SEE DETAIL A

DATE 24 NOV 2022

NOTES:

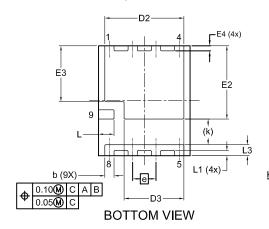
- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE TERMINALS.
- DIMENSIONS D1, D2, E1 AND E2 DO NOT INCLUDE MOLD FLASH.
- SEATING PLANE IS DEFINED BY THE TERMINALS.
 "A1" IS DEFINED AS THE DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY.

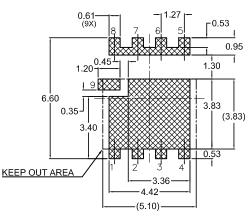


DIM	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	0.95	1.00	1.05	
A1	0.00	0.02	0.05	
A3	0.20 REF			
b	0.45	0.50	0.55	
D	4.90	5.00	5.10	
D2	4.10	4.30	4.50	
D3	3.16	3.26	3.36	
Е	5.90	6.00	6.10	
E2	3.90	4.00	4.10	
E3	2.95	3.05	3.15	
E4	0.18	0.28	0.38	
е	1.27 BSC			
k	1.40 REF			
L	0.75	0.85	0.95	
L1	0.18	0.28	0.38	
L3	0.50	0.60	0.70	

FRONT VIEW

TOP VIEW





LAND PATTERN RECOMMENDATION

*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

GENERIC MARKING DIAGRAM*

XXXXXX XXXXXX AWLYWW XXXX = Specific Device Code

A = Assembly Location

WL = Wafer Lot

Y = Year Code

WW = Work Week Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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