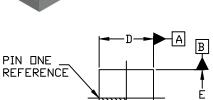
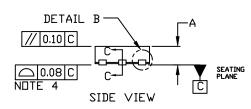
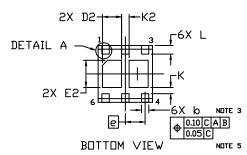
## **MECHANICAL CASE OUTLINE**







TOP VIEW



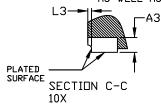


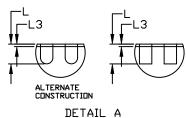
CASE 515AS **ISSUE 0** 

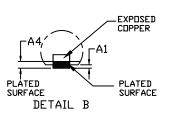
**DATE 21 JUL 2021** 

## NOTES:

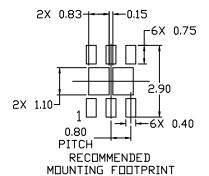
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009
- CONTROLLING DIMENSION: MILLIMETERS
  DIMENSION 6 APPLIES TO PLATED TERMINALS AND IS
  MEASURED BETWEEN 0.15 AND 0.30MM FROM THE
  TERMINAL TIP.
- COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
- POSITIONAL TOLERANCE APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.







	MILLIMETERS			
DIM	MIN.	N□M.	MAX.	
Α	0.70	0.75	0.80	
A1	0.00		0.05	
A3	0.20 REF			
Α4	0.10			
b	0.25	0.30	0.35	
D	2.10	2.20	2.30	
D2	0.72	0.77	0.82	
E	2.20	2.30	2.40	
E2	1.05	1.10	1.15	
е	0.80 B2C			
K	0.25 REF			
K2	0.30 REF			
L	0.30	0.35	0.40	
L3			0.09	



For additional information on our Pb-Free strategy and soldering details, please download the IIN Semiconductor Soldering and Mounting Techniques Reference Manual, SILDERRM/10.

GENERIC				
MARKING	DIAGRAM*			



XXXX = Specific Device Code

= Assembly Location

= Wafer Lot Υ = Year W = Work Week

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

DOCUMENT NUMBER:	98AON36028H	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	WDFNW6 2.2x2.3, 0.8P		PAGE 1 OF 1	

ON Semiconductor and un are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.