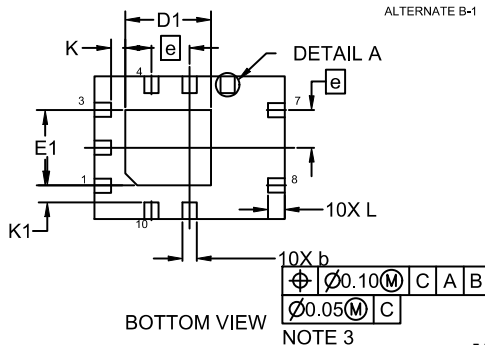
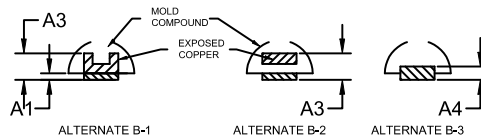
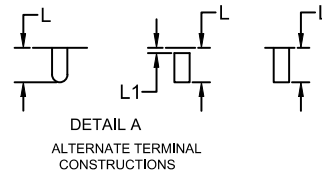
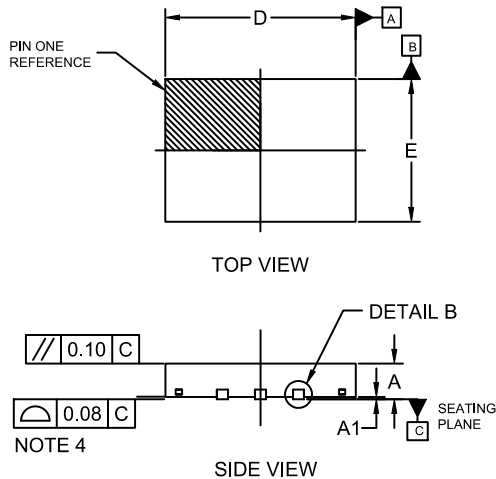
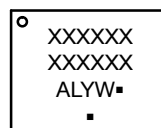


SCALE 2:1

**WQFN10 4x3, 0.8P**  
**CASE 511DV**  
**ISSUE C**

DATE 15 JUL 2019


**GENERIC**  
**MARKING DIAGRAM\***

XXXXXX = Specific Device Code  
A = Assembly Location  
L = Wafer Lot  
Y = Year  
W = Work Week  
▪ = Pb-Free Package

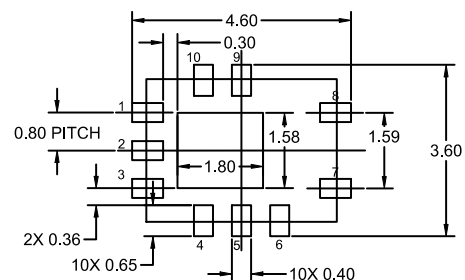
(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

## NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION b APPLIES TO THE PLATED TERMINALS AND IS MEASURED BETWEEN 0.20 AND 0.25 FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE PLATED TERMINALS.

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.70	0.75	0.80
A1	0.00	---	0.05
A3	0.20 REF		
A4	0.10 REF		
b	0.25	0.30	0.35
D	3.90	4.00	4.10
D1	1.75	1.80	1.85
E	2.90	3.00	3.10
E1	1.53	1.58	1.63
e	0.80 BSC		
K	0.30 REF		
K1	0.36 REF		
L	0.30	0.35	0.40
L1	0.05 REF		


**RECOMMENDED**  
**MOUNTING FOOTPRINT**

\* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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<b>DESCRIPTION:</b>	<b>WQFN10 4x3, 0.8P</b>	<b>PAGE 1 OF 1</b>

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