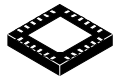


MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS

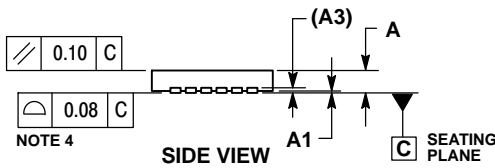
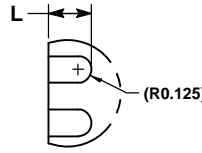
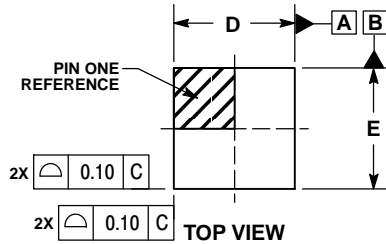
ON Semiconductor®



SCALE 2:1

WQFN24, 4x4, 0.5P
CASE 510BP
ISSUE O

DATE 03 FEB 2016

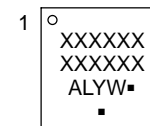


NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.25 AND 0.30 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

MILLIMETERS		
DIM	MIN	MAX
A	0.70	0.80
A1	0.00	0.05
A3	0.20 REF	
b	0.20	0.30
D	3.90	4.10
D2	2.44	2.64
E	3.90	4.10
E2	2.44	2.64
e	0.50 BSC	
L	0.30	0.50

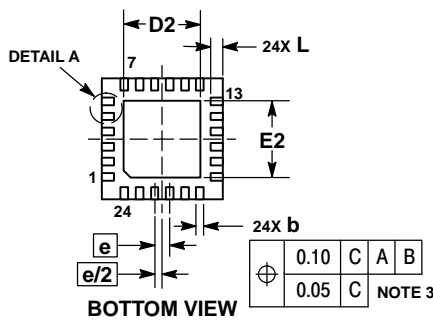
GENERIC MARKING DIAGRAM*



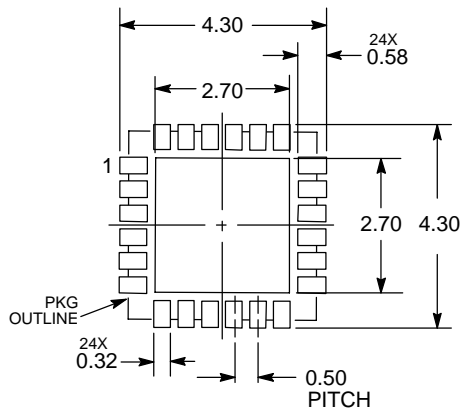
- XXXXXX= Specific Device Code
- A = Assembly Location
- L = Wafer Lot
- Y = Year
- W = Work Week
- = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.



SOLDERING FOOTPRINT*




DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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STATUS:	ON SEMICONDUCTOR STANDARD	
NEW STANDARD:		
DESCRIPTION:	WQFN24, 4X4, 0.5P	PAGE 1 OF 2



ISSUE	REVISION	DATE
O	THIS IS A CUSTOM VERSION OF CASE 510BC TO MEET CUSTOMER'S SPECIAL REQUIREMENT OF LEAD CONSTRUCTION OPTION. REQ. BY J. LIU.	03 FEB 2016

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