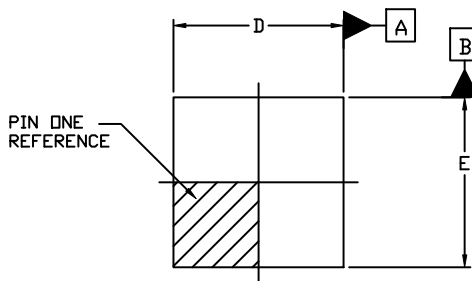
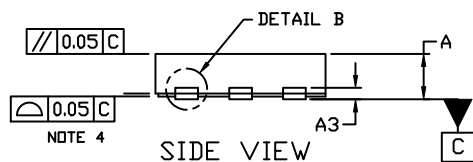



DFNW6 3X3, 0.95P
CASE 507AW
ISSUE 0

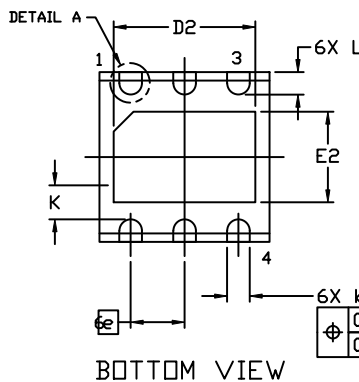
DATE 21 MAY 2019



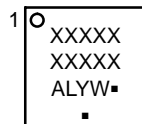
TOP VIEW



SIDE VIEW



BOTTOM VIEW

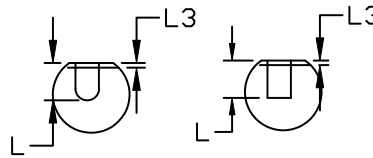
GENERIC
MARKING DIAGRAM*


XXXXX = Specific Device Code
A = Assembly Location
L = Wafer Lot
Y = Year
W = Work Week
▪ = Pb-Free Package

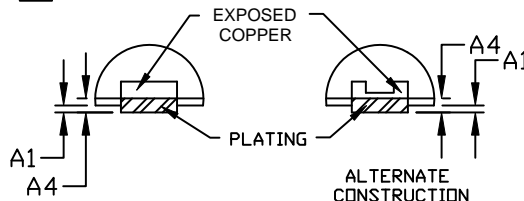
*This information is generic. Please refer to device data sheet for actual part marking.
Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

NOTES:

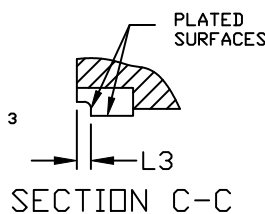
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE TERMINALS.



DETAIL A

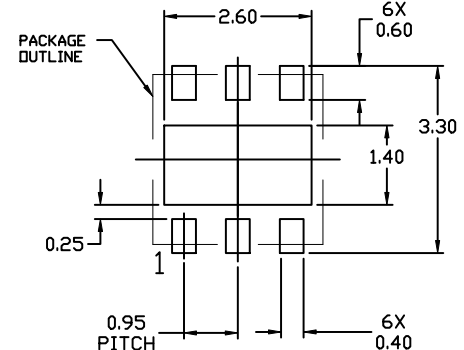


DETAIL B



SECTION C-C

DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.80	0.90	1.00
A1	0.00	---	0.05
A3	0.20 REF		
A4	0.10	---	---
b	0.35	0.40	0.45
D	2.90	3.00	3.10
D2	2.40	2.50	2.60
E	2.90	3.00	3.10
E2	1.50	1.60	1.70
e	0.95 BSC		
K	0.30	---	---
L	0.30	0.40	0.50
L3	---	---	0.10


RECOMMENDED
MOUNTING FOOTPRINT

- For additional information on our Pb-Free strategy and soldering details, please download the [ON Semiconductor Soldering and Mounting Techniques Reference Manual](#), SOLDERRM/D.

DOCUMENT NUMBER: 98AON07566H

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DESCRIPTION: DFNW6 3X3, 0.95P

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