



PIN ONE REFERENCE

0.15

2X △ 0.15 C

// 0.10 C

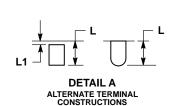
| △ 0.08 C

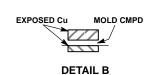
NOTE 4

С

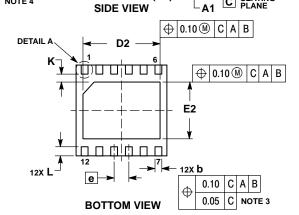
DFN12, 4x4, 0.65P CASE 506CE **ISSUE O** 

**DATE 23 FEB 2012** 









(A3)

AB

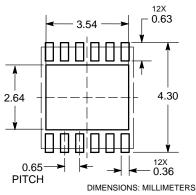
Ε

D

**TOP VIEW DETAIL B** 

<del>- (-)</del>

## **SOLDERING FOOTPRINT**



<sup>\*</sup>For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## NOTES:

- DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS.
- DIMESNION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN
- 0.15 AND 0.30 MM FROM TERMINAL. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS		
DIM	MIN	MAX	
Α	0.80	1.00	
A1	0.00	0.05	
А3	0.20 REF		
b	0.25	0.35	
D	4.00 BSC		
D2	3.30	3.50	
Е	4.00 BSC		
E2	2.40	2.60	
е	0.65 BSC		
K	0.20		
L	0.30	0.50	
L1		0.15	

## **GENERIC MARKING DIAGRAM\***



XXXXXX= Specific Device Code

= Assembly Location

= Wafer Lot ı Υ = Year W = Work Week

= Pb-Free Package

(\*Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

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DESCRIPTION:	12 PIN DFN, 4X4, 0.65P		PAGE 1 OF 1

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