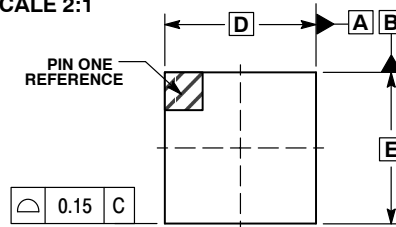
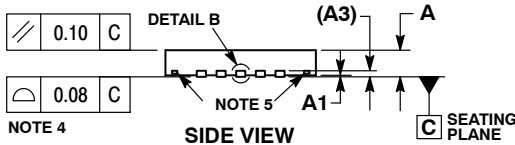


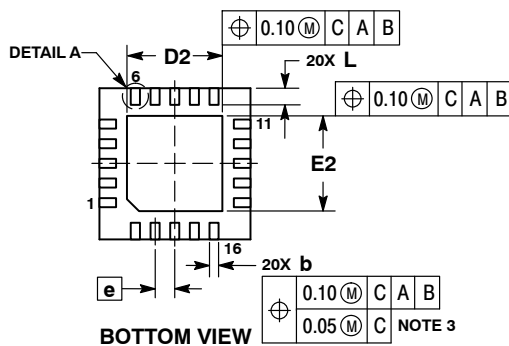
SCALE 2:1



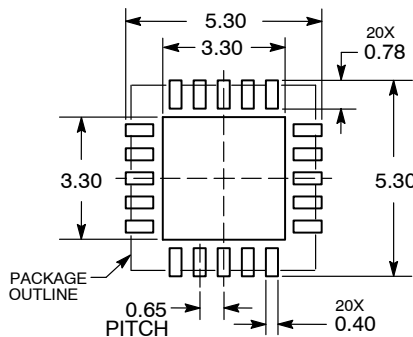
TOP VIEW



SIDE VIEW



BOTTOM VIEW

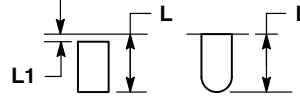
**RECOMMENDED  
SOLDERING FOOTPRINT\***


DIMENSIONS: MILLIMETERS

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

**QFN20 5x5, 0.65P**  
CASE 485DB  
ISSUE O

DATE 02 APR 2013

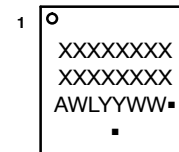

**DETAIL A**  
ALTERNATE TERMINAL  
CONSTRUCTIONS

**DETAIL B**  
ALTERNATE  
CONSTRUCTION

## NOTES:

1. DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.25 AND 0.30 MM FROM THE TERMINAL TIP.
4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
5. OPTIONAL FEATURES.

MILLIMETERS		
DIM	MIN	MAX
A	0.80	1.00
A1	---	0.05
A3	0.20	REF
b	0.25	0.35
D	5.00	BSC
D2	3.05	3.25
E	5.00	BSC
E2	3.05	3.25
e	0.65	BSC
L	0.45	0.65
L1	---	0.15

**GENERIC  
MARKING DIAGRAM\***


XXXXX = Specific Device Code  
A = Assembly Location  
WL = Wafer Lot  
YY = Year  
WW = Work Week  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking.

Pb-Free indicator, "G" or microdot "▪", may or may not be present.

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<b>DESCRIPTION:</b>	<b>QFN20 5x5, 0.65P</b>	<b>PAGE 1 OF 1</b>

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