





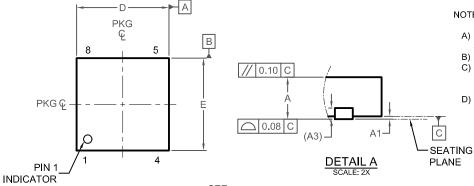
## PQFN8 3.3X3.3, 0.65P CASE 483AX **ISSUE B**

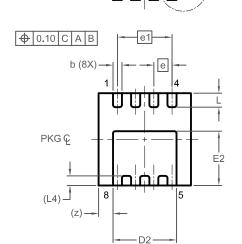
**DATE 24 JUN 2022** 

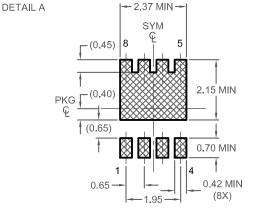
NOTES: UNLESS OTHERWISE SPECIFIED

- A) PACKAGE STANDARD REFERENCE: JEDEC MO-240, ISSUE A, VAR. BA,
- B) ALL DIMENSIONS ARE IN MILLIMETERS.
- C) DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH. MOLD FLASH OR BURRS DOES NOT EXCEED 0.10MM.
- D) DIMENSIONING AND TOLERANCING PER ASME Y14.5M-2009.

| DIM | MILLIMETERS |      |      |  |
|-----|-------------|------|------|--|
| DIM | MIN.        | NOM. | MAX. |  |
| Α   | 0.70        | 0.75 | 0.80 |  |
| A1  | 0.00        | ı    | 0.05 |  |
| А3  | 0.20 REF    |      |      |  |
| b   | 0.27        | 0.32 | 0.37 |  |
| D   | 3,20        | 3.30 | 3.40 |  |
| D2  | 2.17        | 2.27 | 2,37 |  |
| Е   | 3.20        | 3.30 | 3.40 |  |
| E2  | 1.84        | 1.94 | 2.04 |  |
| е   | 0.65 BSC    |      |      |  |
| e1  | 1.95 BSC    |      |      |  |
| L   | 0.40        | 0.50 | 0.60 |  |
| L4  | 0.34 REF    |      |      |  |
| Z   | 0.52 REF    |      |      |  |







## LAND PATTERN RECOMMENDATION

\*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

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