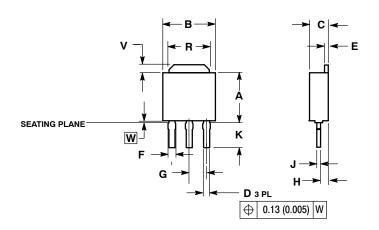
MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



3 IPAK, STRAIGHT LEAD CASE 369AC-01 ISSUE O



SCALE 1:1



DATE 08 FEB 2005

- NOTES:
 1... DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2.. CONTROLLING DIMENSION: INCH.
 3. SEATING PLANE IS ON TOP OF DAMBAR POSITION.
 4. DIMENSION A DOES NOT INCLUDE DAMBAR POSITION OR MOLD GATE.

			-	
	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.235	0.245	5.97	6.22
В	0.250	0.265	6.35	6.73
С	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
Е	0.018	0.023	0.46	0.58
F	0.037	0.043	0.94	1.09
G	0.090 BSC		2.29 BSC	
Н	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
κ	0.134	0.142	3.40	3.60
R	0.180	0.215	4.57	5.46
V	0.035	0.050	0.89	1.27
W	0.000	0.010	0.000	0.25

GENERIC **MARKING DIAGRAM***

Discrete

000	YWW xxx xxxxxxx	$\Big]$

XXXXXXXXX	= Device Code
Υ	= Year
WW	= Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

DOCUMENT NUMBER:	98AON20252D	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.				
DESCRIPTION:	3 IPAK, STRAIGHT LEAD		PAGE 1 OF 1			
ON Semiconductor and ()) are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the						