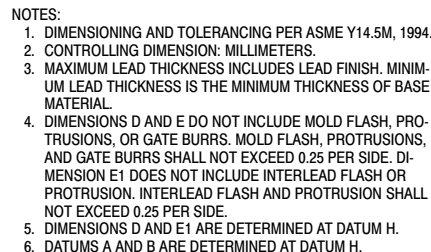


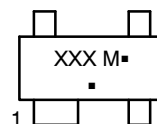
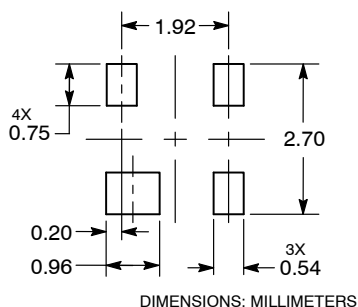


DATE 07 SEP 2011



DIM	MILLIMETERS	
	MIN	MAX
A	0.80	1.12
A1	0.01	0.15
b	0.30	0.51
b1	0.76	0.94
c	0.08	0.20
D	2.80	3.05
E	2.10	2.64
E1	1.20	1.40
e	1.92 BSC	
e1	0.20 BSC	
L	0.35	0.70
l2	0.25 BSC	

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code
M = Date Code
▪ = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking.
Pb-Free indicator, "G" or microdot "▪", may or may not be present.

- | | | | | | |
|------------------|---------------|---------------|---------------|---------------|------------|
| STYLE 1: | STYLE 2: | STYLE 3: | STYLE 4: | STYLE 5: | STYLE 6: |
| PIN 1. COLLECTOR | PIN 1. SOURCE | PIN 1. GROUND | PIN 1. OUTPUT | PIN 1. SOURCE | PIN 1. GND |
| 2. EMITTER | 2. DRAIN | 2. SOURCE | 2. GROUND | 2. DRAIN | 2. RF IN |
| 3. EMITTER | 3. GATE 1 | 3. INPUT | 3. GROUND | 3. GATE 1 | 3. VREG |
| 4. BASE | 4. GATE 2 | 4. OUTPUT | 4. INPUT | 4. SOURCE | 4. RF OUT |
| | | | | | |
| STYLE 7: | STYLE 8: | STYLE 9: | STYLE 10: | STYLE 11: | |
| PIN 1. SOURCE | PIN 1. SOURCE | PIN 1. GND | PIN 1. DRAIN | PIN 1. SOURCE | |
| 2. GATE | 2. GATE | 2. IOUT | 2. N/C | 2. GATE 1 | |
| 3. DRAIN | 3. DRAIN | 3. VCC | 3. SOURCE | 3. GATE 2 | |
| 4. SOURCE | 4. N/C | 4. VREF | 4. GATE | 4. DRAIN | |

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