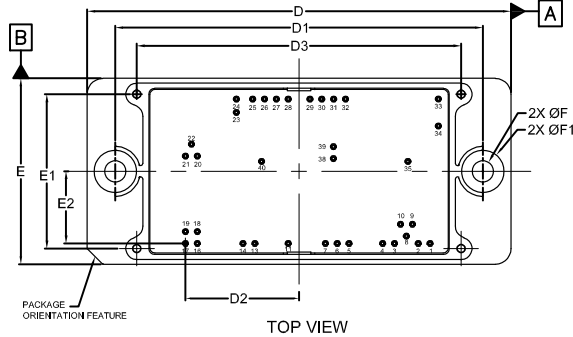


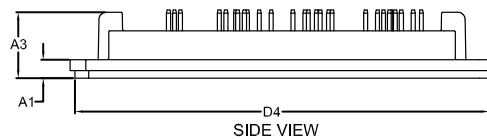
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CASE 180BE
ISSUE C

DATE 27 JUL 2022

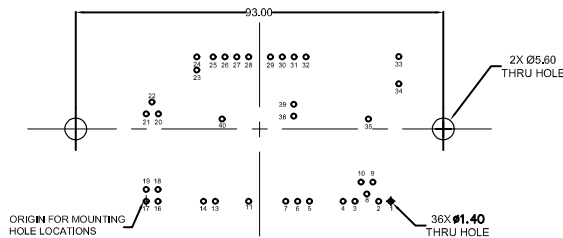


PACKAGE ORIENTATION FEATURE

TOP VIEW



SIDE VIEW

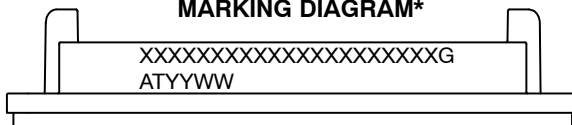


ORIGIN FOR MOUNTING HOLE LOCATIONS

RECOMMENDED MOUNTING PATTERN*

For additional information on our Pb-Free strategy and soldering details, please download the On Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

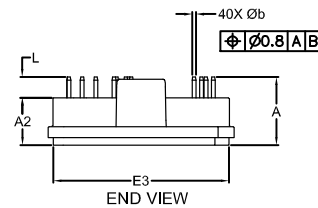
GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code
G = Pb-Free Package
AT = Assembly & Test Site Code
YYWW = Year and Work Week Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

MILLIMETERS			
DIM	MIN.	NOM.	MAX.
A	16.63	17.23	17.83
A1	4.50	4.70	4.90
A2	11.60	12.00	12.40
A3	16.40	16.70	17.00
b	0.95	1.00	1.05
D	106.80	107.20	107.60
D1	92.90	93.00	93.10
D2	28.40	28.70	29.00
D3	81.80	82.00	82.20
D4	104.35	104.75	105.15
E	46.60	47.00	47.40
E1	38.80	39.00	39.20
E2	17.95	18.25	18.55
E3	44.30	44.40	44.50
F1	5.40	5.50	5.60
F1	10.70 REF		
L	5.03	5.23	5.43



END VIEW

NOTE 4

PIN	PIN POSITION		PIN	PIN POSITION	
	X	Y		X	Y
1	61.85	0.0	21	0.0	22.1
2	58.85	0.0	22	1.5	25.1
3	52.85	0.0	23	12.85	33.15
4	49.85	0.0	24	12.85	36.5
5	41.35	0.0	25	16.95	36.5
6	38.35	0.0	26	19.95	36.5
7	35.35	0.0	27	22.95	36.5
8	55.85	1.85	28	25.95	36.5
9	57.35	4.85	29	31.45	36.5
10	54.35	4.85	30	34.45	36.5
11	25.95	0.0	31	37.45	36.5
13	17.5	0.0	32	40.45	36.5
14	14.5	0.0	33	63.9	36.55
16	3.0	0.0	34	63.9	29.7
17	0.0	0.0	35	56.2	20.75
18	3.0	3.0	38	37.4	21.5
19	0.0	3.0	39	37.4	24.5
20	3.0	22.1	40	19.2	20.75

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME 7 14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSIONS b APPLY TO THE PLATED TERMINALS AND ARE MEASURED WHERE THE PIN EXITS THE PACKAGE BODY.
4. POSITION OF THE CENTER OF THE TERMINALS IS DETERMINED FROM PIN 17. POSITIONAL TOLERANCE, AS NOTED IN THE DRAWING, APPLIES TO EACH TERMINAL.

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