Test Procedure for the NCV47822 Evaluation Board

The NCV47822 is dual channel High Side Switch with:

- Two adjustable current limits up to 350 mA
- Enable inputs with 3.3 V Logic compatible thresholds
- Diagnostic Features:
  - Short To Battery (STB) and Open Load (OL) in OFF State
  - Internal Components for OFF State Diagnostics
  - Open Collector Flag Output
  - Two Output Voltage Monitoring Outputs (Analog)

For detailed information read the technical specification.

Power supplying of the chip is possible from one or two independent sources. **INPUT1** must be always supplied and **INPUT2** as optional for $\mathbf{V_{in2}}$ supply.

Basic procedure of function verification, Diagnostic Functions inactive

1. **Power supplying**
   a. **Power supplying from one source**
   Connect the test setup as is shown in **Figure 1** (See **Table 1** with required equipment). Connect power supply to **INPUT1** connector $\mathbf{J_1}$ (Power supplying of **INPUT2** is not needed).
   - **Hi_F –** Positive Force line
   - **Hi_S –** Positive Sense line
   - **Lo_F** – Negative Force line
   - **Lo_S** – Negative Sense line

   Connect $\mathbf{V_{in2}}$ pin to **INPUT1** via appropriate position of jumper "**$\mathbf{V_{in2}}$ to IN1 or IN2 connection**".

   b. **Power supplying from two sources**
   Connect the test setup as is shown in **Figure 1** (See **Table 1** with required equipment). Connect two power supplies to **INPUT1** connector $\mathbf{J_1}$ and to **INPUT2** connector $\mathbf{J_2}$, respectively.
   - **Hi_F –** Positive Force line
   - **Hi_S –** Positive Sense line
   - **Lo_F** – Negative Force line
   - **Lo_S** – Negative Sense line

   Values of input voltages $\mathbf{V_{in1}}$ and $\mathbf{V_{in2}}$ can be different. This option is suitable for reducing of power dissipation on chip.
   Connect $\mathbf{V_{in2}}$ pin to **INPUT2** via appropriate position of jumper "**$\mathbf{V_{in2}}$ to IN1 or IN2 connection**".

2. **Current Limit settings**
   Connect jumpers $\mathbf{J_{10} – J_{13}}$ for output current limitation from $\mathbf{V_{out1}}$ pin and $\mathbf{J_{20} – J_{23}}$ for output current limitation from $\mathbf{V_{out2}}$ pin.
   - $\mathbf{J_{n0}} – I_{LIMn0} \sim 14.4$ mA
   - $\mathbf{J_{n1}} – I_{LIMn1} \sim 154.6$ mA
   - $\mathbf{J_{n2}} – I_{LIMn2} \sim 330$ mA
   - $\mathbf{J_{n3}} – I_{LIMn3} – R_{CSOn3}$ positions available for individual current limit setting by resistor from range 2185 Ω to 76.5 kΩ

3. **Diagnostic Function (inactive)**
   Connect **DE** and **CS** inputs to GND via appropriate jumper to disable diagnostic function.

4. **Power ON**
   Set Input Voltage and turn on Power Supply/Supplies.
   Enable output of the channel to power the regulated output voltage by connecting the **ENABLE** input to corresponding $\mathbf{V_{in}}$ via jumper. Enabling can be performed by external voltage source as well.

5. Load the outputs by resistive loads connected via jumpers:
   - **J_5, J_7** – 60 Ω (I_{out} ~ 220 mA)
   - **J_6, J_8** – 1 kΩ (I_{out} ~ 13 mA)

   External loads can be used instead build-in resistive loads as well.

6. Monitor Output Feedback Voltages on $V_{out\_FB1,2}$ outputs, given according to Equation 1

$$V_{out\_FB\_n} = \frac{V_{out\_n}}{6} \qquad \text{(eq. 1)}$$

7. Monitor Current Sense Output voltages on appropriate connector. They should be max 2.55 V in steady state. The CSO voltages are proportional to output currents according to Equation 2.

$$V_{CSO\_n} = I_{out\_n}\left(R_{CSO\_n} \times \frac{1}{RATIO}\right) \qquad \text{(eq. 2)}$$

   Where:
   RATIO = 265 for $I_{out1,2}$ from 10 mA to 50 mA
   RATIO = 285 for $I_{out1,2}$ from 50 mA to 350 mA

8. Compare your results with measured results in **Table 2.**

9. **Power OFF**
   Disable output of the channel by connecting the **ENABLE** input to **GND** via jumper. Or Turn OFF external voltage source.

Diagnostic Functions verification

1. **Diagnostic Function (active)**
   Perform steps from 1 to 4 of Basic procedure of function verification and keep both channels disabled.

   Short to battery detection (in OFF State)

   Set **DE** input to **5 V** via appropriate jumper.
   Set **CS** input to **GND.** If **EF** is **LOW**, Short to Battery on **CH1** event occurred.
   Set **CS** input to **5 V.** If **EF** is **LOW**, Short to Battery on **CH2** event occurred.

   Open Load detection (in OFF State)

   Set **DE** input to **5 V** via appropriate jumper.
   Enable detection by connecting the **ENABLE** input to corresponding $\mathbf{V_{in}}$ via jumper. Enabling can be performed by external voltage source as well.
   Set **CS** input to **GND.** If **EF** is **LOW**, Open Load on **CH1** event occurred.
   Set **CS** input to **5 V.** If **EF** is **LOW**, Open Load on **CH2** event occurred.
   Combine selection $\mathbf{R_{CSO1,2}}$ and Resistive Loads. You can use external load as well.

   Overcurrent detection

   Set **DE** input to **GND** via appropriate jumper.
   Enable output of the channel to power the regulated output voltage by connecting the **ENABLE** input to corresponding $\mathbf{V_{in}}$ via jumper. Enabling can be performed by external voltage source as well.
   Set **CS** input to **GND.** If **EF** is **LOW**, Overcurrent on **CH1** occurred and $\mathbf{CSO_1}$ output is **HIGH**.
   Set **CS** input to **5 V.** If **EF** is **LOW**, Overcurrent on **CH2** occurred and $\mathbf{CSO_2}$ output is **HIGH**.
   Combine selection $\mathbf{R_{CSO1,2}}$ and Resistive Loads. You can use external load as well.

   For supplying **DE** and **CS** inputs is used an 5 V LDO regulator $\mathbf{U_2}$ powered from **INPUT1**.

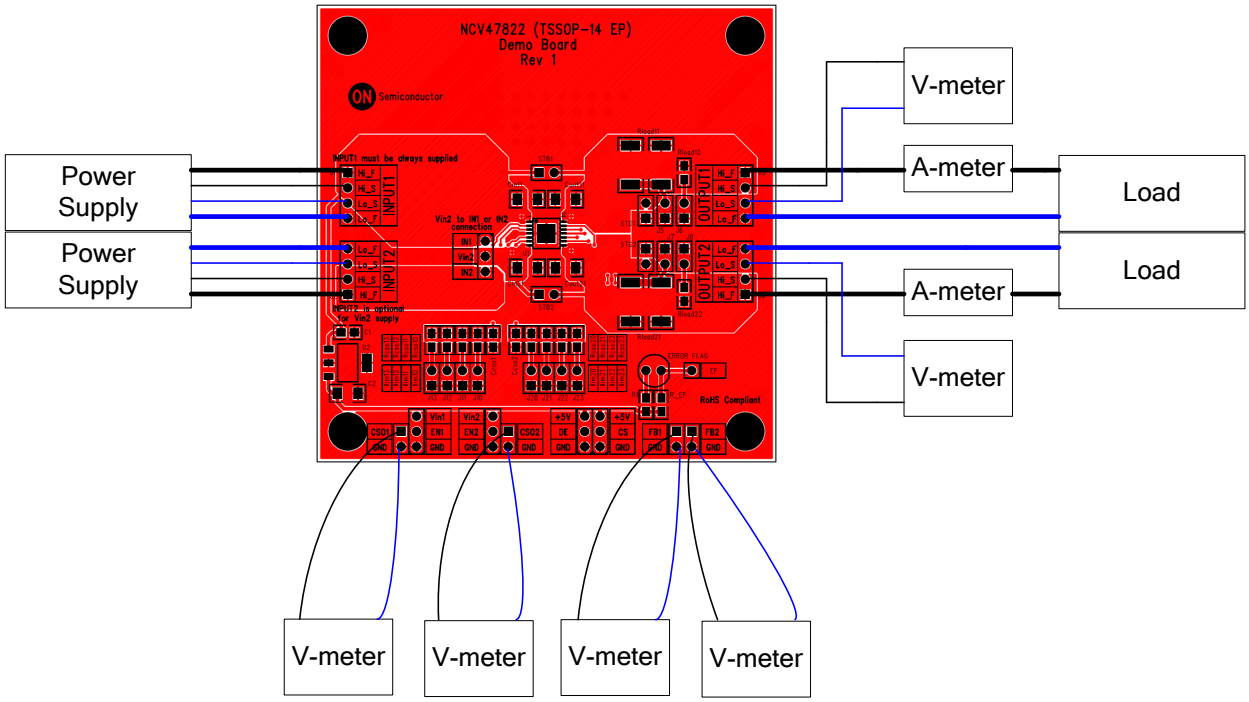

Figure 1. General Test Setup

Table 1: Required Equipment

Equipment	Ranges
Power Supply	0 V – 45 V / 2 A
Load	0 mA – 1 A
V - meter	0 V – 45 V
A - meter	0 mA – 1 A

Table 2: Measured Results

Parameter	Test Conditions	Symbol	Value		Unit
			Nominal	Measured	
Output Voltage	V_{in} = 13.5 V, I_{out_n} = 200 mA, R_{CSO_n} = Short to ground	V_{out1}	13.3	13.23	V
		V_{out2}		13.23	
		V_{out_FB1}	2.21	2.22	
		V_{out_FB2}		2.22	
Output Current	V_{in} = 13.5 V, V_{out_n} = 12.5 V, R_{CSO_n} = 47 kΩ	I_{out1}	14.4	16.1	mA
		I_{out2}		15.7	
	V_{in} = 13.5 V, V_{out_n} = 12.5 V, R_{CSO_n} = 4.7 kΩ	I_{out1}	154.6	156	
		I_{out2}		161	
	V_{in} = 13.5 V, V_{out_n} = 12.5 V, R_{CSO_n} = 2.2 kΩ	I_{out1}	330	343	
		I_{out2}		342	
Output Current	V_{in} = 13.5 V, V_{out_n} = 0 V, R_{CSO_n} = 47 kΩ	I_{out1}	14.4	16.7	mA
		I_{out2}		16.7	
	V_{in} = 13.5 V, V_{out_n} = 0 V, R_{CSO_n} = 4.7 kΩ	I_{out1}	154.6	168	
		I_{out2}		167	
	V_{in} = 13.5 V, V_{out_n} = 0 V, R_{CSO_n} = 2.2 kΩ	I_{out1}	347	360	
		I_{out2}		360	

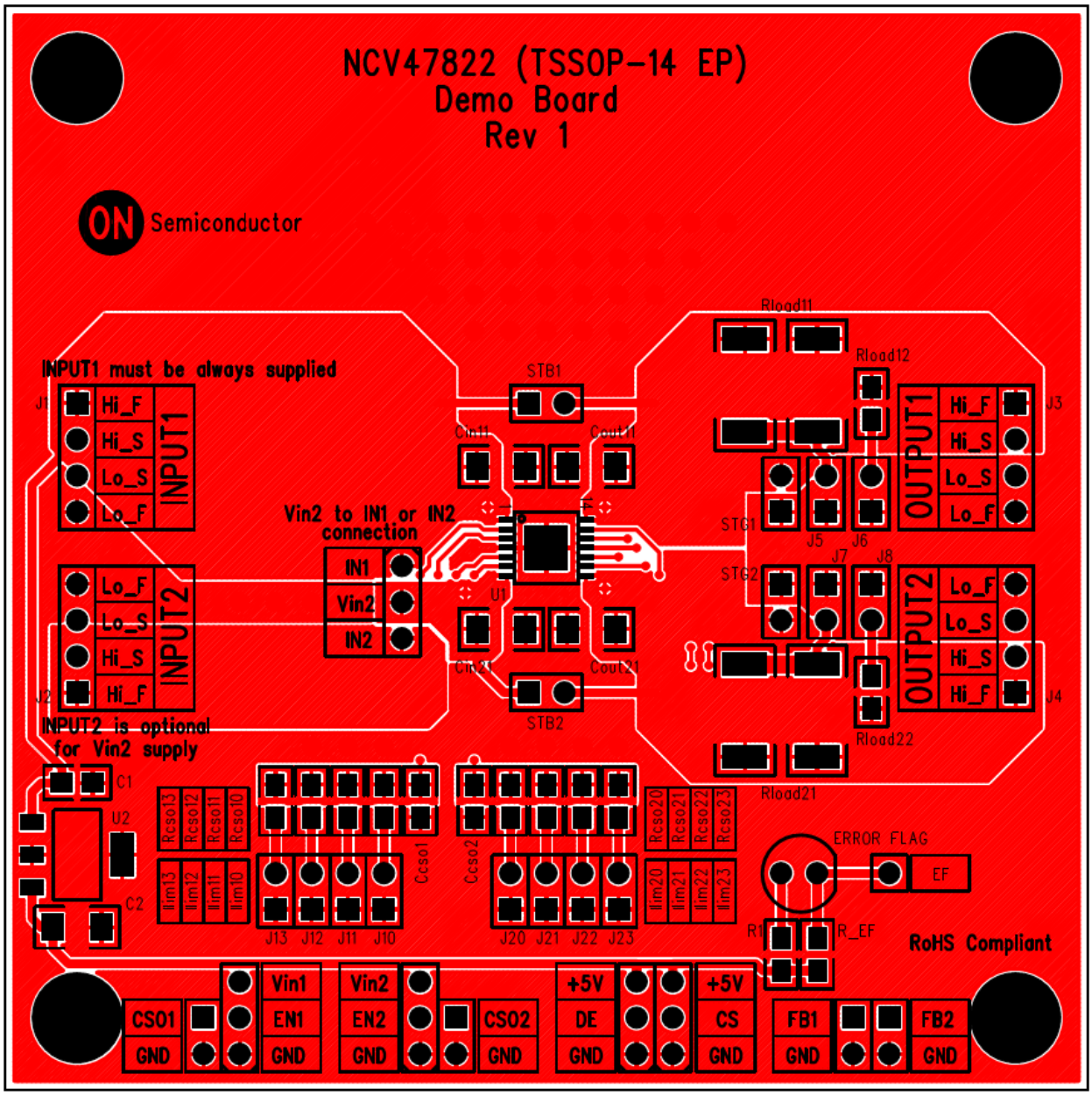

Figure 2. Top side PCB Layout (3 x 3 inch)