

- 1 Material: Isola IS400 or similar recommended
- 2 Finish: ENIG (Electroless Nickel Immersion Gold), nickel layer $1 \div 4 \mu\text{m}$, gold layer $0.076 \div 0.2 \mu\text{m}$
- 3 All gerber files generated as a top view
4. Fabricate according IPC-A-600
5. Non-conductive epoxy ink recommended for silkscreen
6. Silkscreen should not cover any exposed copper, silkscreen gerber data have to be trimmed eventually
7. All holes diameter refer to final diameter after eventual plating

Gerber and drill file extensions table

Gerber files	Description
.GTO	Top side silkscreen
.GTP	Top side solder paste mask
.GTS	Top side solder mask
.GTL	L1_TOP - Top Layer
.GBL	L2_BOTTOM - Bottom Layer
.GBS	Bottom side solder mask
.GBP	Bottom side solder paste mask
.GBO	Bottom side silkscreen
.GM1	Board outline
Drill files	
.TXT	Layer pair L1_TOP to L2_BOTTOM Layer

Lighting kit - LED Driver		Revision: rev 1.1	State: released
PCB fabrication notes and requirements		Fabrication document	Sheet 1 / 13
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PCB File: Lighting_kit_LED_driver.PcbDoc		ON Semiconductor Solution Engineering Center Piestany	
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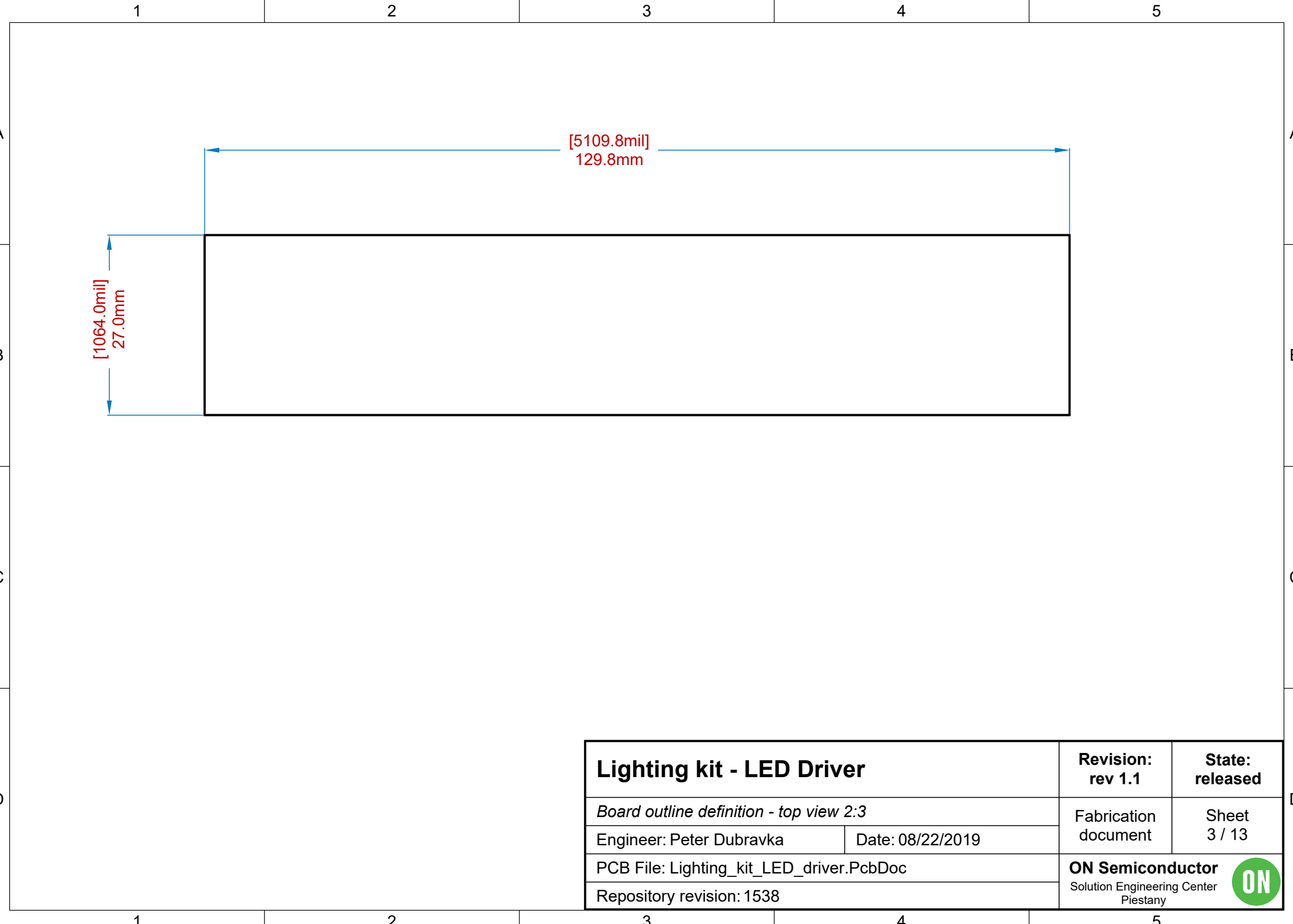
D


Layer Stack

Material	Layer	Thickness	Dielectric Material	Type	Gerber
	Top Overlay			Legend	GTO
Surface Material	Top Solder	0.0102mm(0.400mil)	Solder Resist	Solder Mask	GTS
Copper	Top Layer	0.0356mm(1.400mil)		Signal	GTL
Core		1.5240mm(60.000mil)	FR-4	Dielectric	
Copper	Bottom Layer	0.0356mm(1.400mil)		Signal	GBL
Surface Material	Bottom Solder	0.0102mm(0.400mil)	Solder Resist	Solder Mask	GBS
	Bottom Overlay			Legend	GBO
Total thickness: 1.6154mm(63.600mil)					

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Layer stack details		Fabrication document	Sheet 2 / 13
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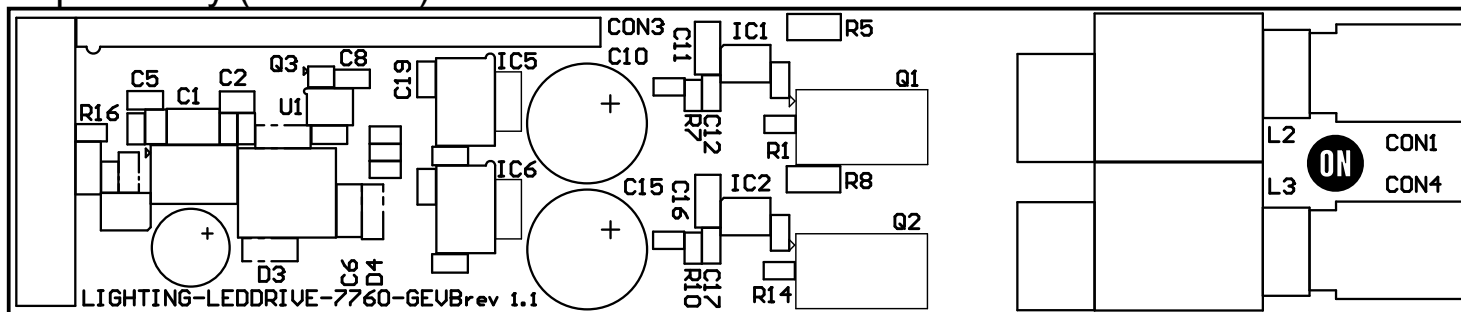




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Board outline definition - top view 2:3		Fabrication document	Sheet 3 / 13
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Top Overlay (Scale 3:2)



Lighting kit - LED Driver

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Top side silkscreen - top view

Fabrication
document

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Date: 08/22/2019

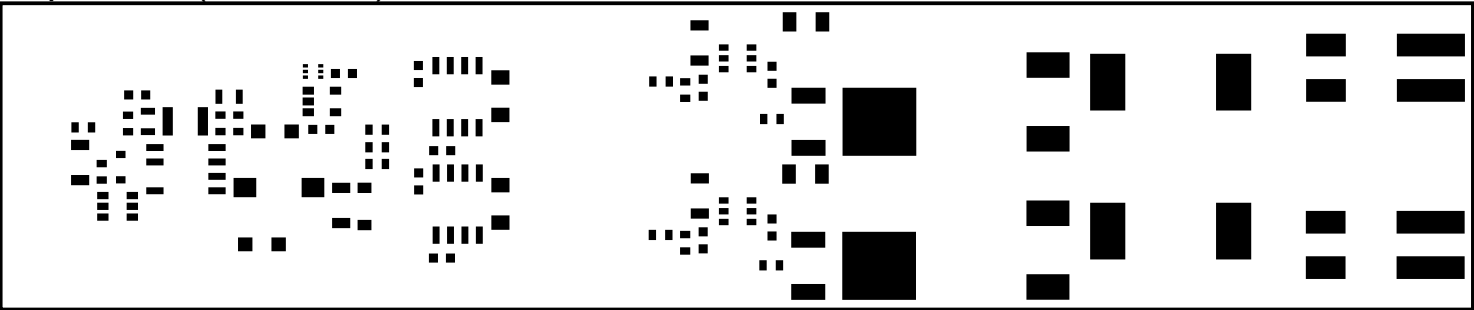
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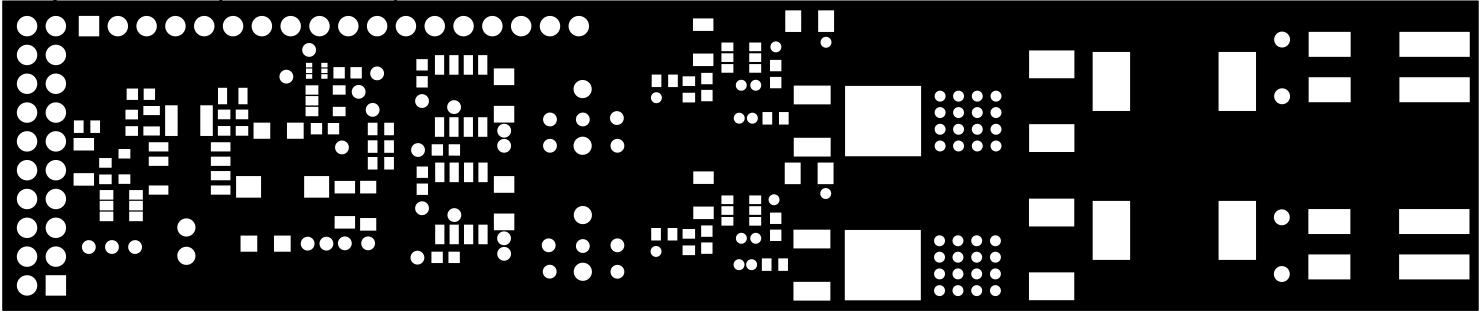
Top Paste (Scale 3:2)




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Top side solder paste - top view		Fabrication document	Sheet 5 / 13
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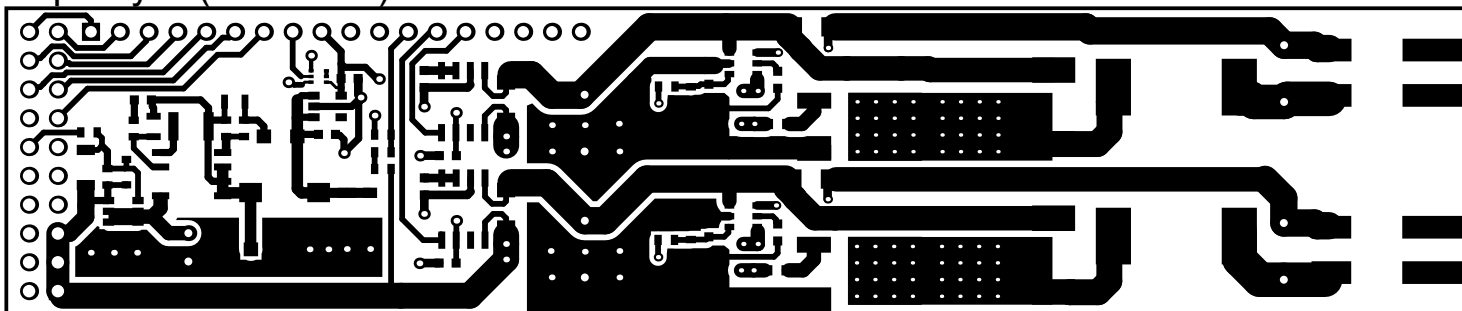


Top Solder (Scale 3:2)



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Top side solder mask - top view		Fabrication document	Sheet 6 / 13
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Top Layer (Scale 3:2)



Lighting kit - LED Driver

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State:
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Top Layer - top view

Fabrication
document

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Date: 08/22/2019

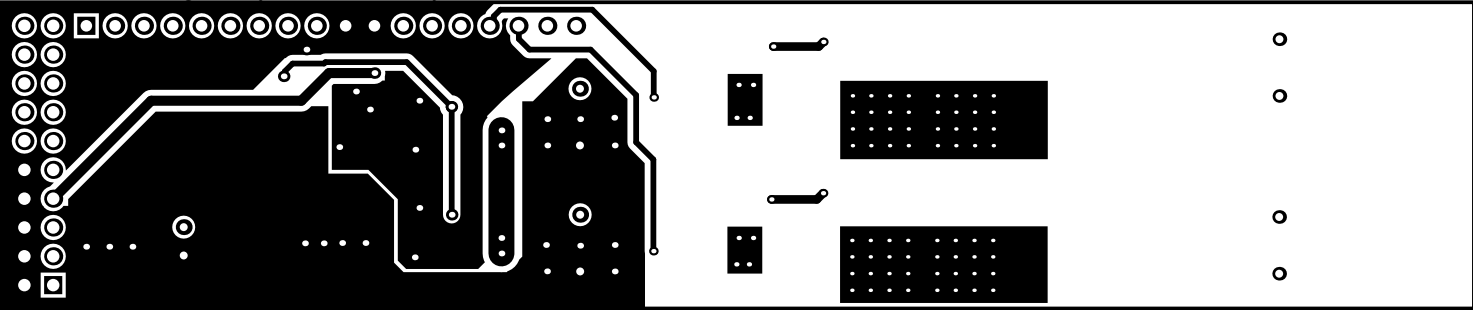
PCB File: Lighting_kit_LED_driver.PcbDoc


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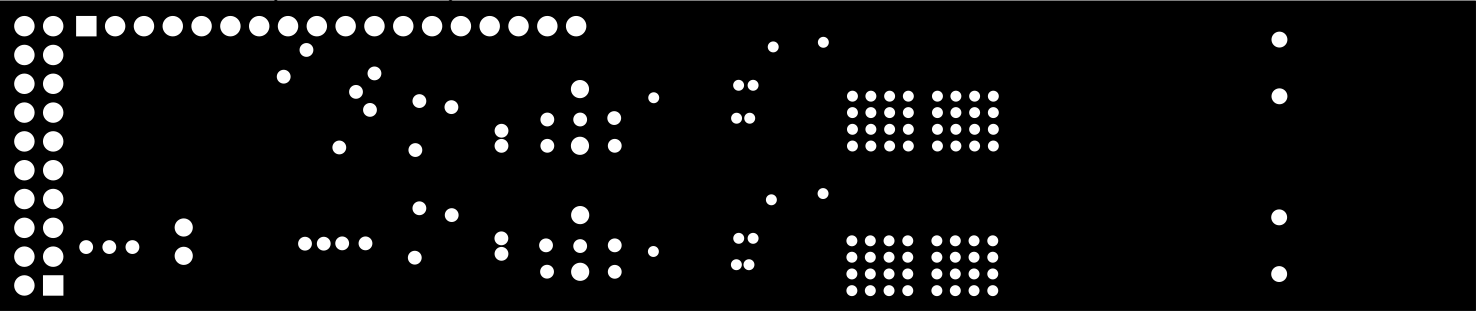
Bottom Layer (Scale 3:2)




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Bottom Layer - top view		Fabrication document	Sheet 8 / 13
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Bottom Solder (Scale 3:2)



Lighting kit - LED Driver		Revision: rev 1.1	State: released
Bottom side solder mask - top view		Fabrication document	Sheet 9 / 13
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Bottom Paste (Scale 3:2)



8. No parts at the bottom

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Bottom side solder paste - top view		Fabrication document	Sheet 10 / 13
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Bottom Overlay (Scale 3:2)

 ON Semiconductor

LIGHTING-LEDDRIVE-7760-GEVB
rev 1.1

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Lighting kit - LED Driver		Revision: rev 1.1	State: released
Bottom side silkscreen - bottom view		Fabrication document	Sheet 11 / 13
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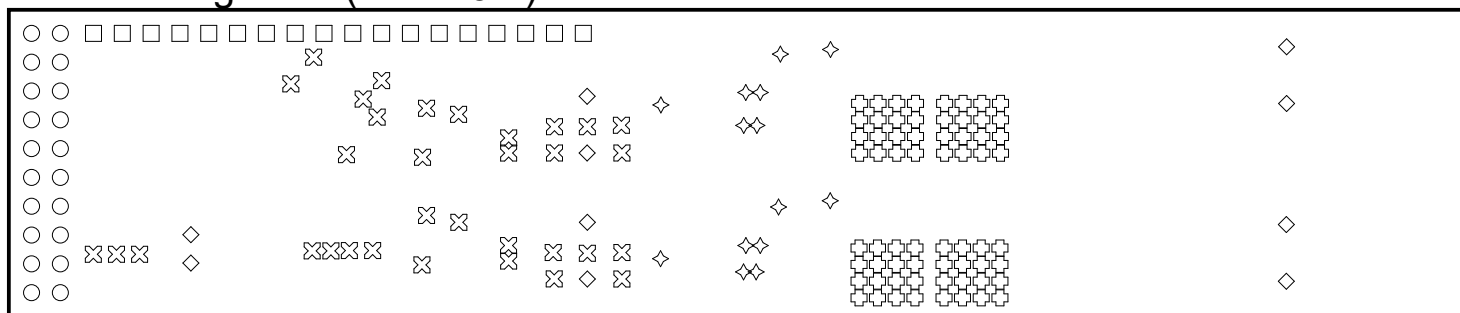
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Drill Drawing View (Scale 3:2)




⑨ Related drill table can be found on page 13

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Drill drawing		Fabrication document	Sheet 12 / 13
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Drill Table

Symbol	Count	Hole Size	Plated	Drill Layer Pair	Via / Pad	Template
⊕	64	0.305mm(12.0mil)	Plated	Top Layer - Bottom Layer	Via	v76h30
◇	14	0.381mm(15.0mil)	Plated	Top Layer - Bottom Layer	Via	v76h38
⊗	33	0.508mm(20.0mil)	Plated	Top Layer - Bottom Layer	Via	v102h51
◇	10	0.700mm(27.6mil)	Plated	Top Layer - Bottom Layer	Pad	(Mixed)
□	18	1.020mm(40.2mil)	Plated	Top Layer - Bottom Layer	Pad	(Mixed)
○	20	1.100mm(43.3mil)	Plated	Top Layer - Bottom Layer	Pad	(Mixed)
159 Total						

⑩ Related drill drawing can be found on page 12

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