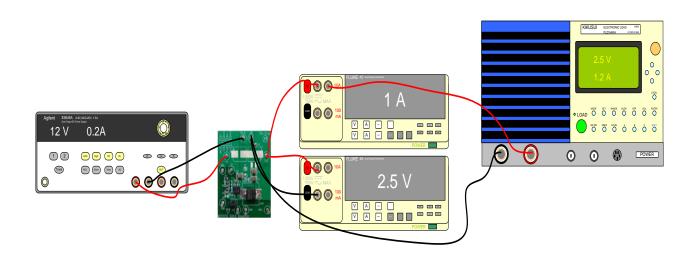


Test Procedure for the NCP3170BGEVB Evaluation Board



The following steps describe the test procedure for all these boards:

Suggested Equipment:

1pc
2pcs
1pc
1pc
1pc

Test Procedure:

- 1. Connect the test setup as shown in Figure 1
- 2. Apply an input voltage, $V_{IN} = 5.0-18$ Vdc
- 3. Apply Iout(load) = 0 A
- 4. Ensure the EN SIP Header is not shorted
- 5. Check that VOUT = 0V Vdc
- 6. Short EN SIP Header with provided jumper
- 7. Observe Soft and PG signal proper behavior as shown in Figure 2
- 8. Check that $V_{OUT} = 3.3 \pm 5\%$ Vdc
- 9. Set Iout to desired level, 0A 3A, and measure voltage and current the efficiency should be similar to that shown in Figure 3 and 4
- 10. Check that $V_{OUT} = 3.3 \pm 5\%$ Vdc under desired operating condition
- 11. Turn off the load
- 12. Turn off VIN
- 13.End of the test



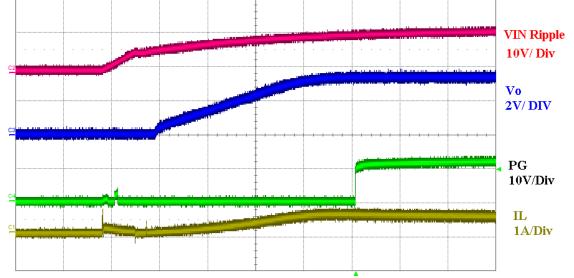


Figure 2: NCP3170B Startup and Regulation with PG Indication

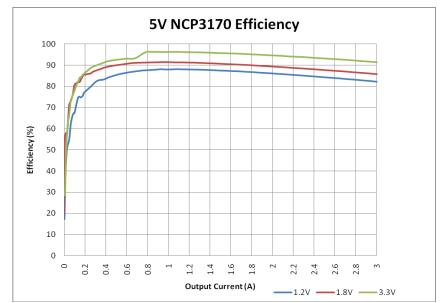


Figure 3: NCP3170 Efficiency at 5V Input and 1.2V, 1.8V, and 3.3V Output



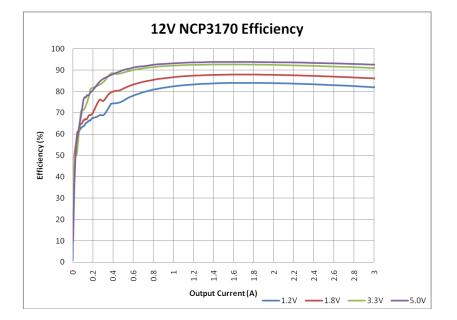


Figure 4: NCP3170 Efficiency at 12V Input and 1.2V, 1.8V, and 3.3V Output