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Launching Arm Cortex-M3 Debug Sessions with Ezairo 8300 Evaluation and Development Boards

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DESIGN NOTE

DN05135/D

When booting the Arm® Cortex®−M3 core from non−volatile memory (NVM), the Ezairo 8300 Evaluation and Development Board starts in a state in which launching a debug session can fail under certain conditions. Described below is a work−around for these conditions.

When creating a GDB SEGGER® J−Link® debugging configuration for use with the Arm Cortex−M3 core, the following options may need to be cleared (unchecked):

Under the Startup tab:

1. **Initial Reset and Halt** (must be unchecked)
2. **Pre−run/Restart Reset**
   (can optionally be unchecked if debugging is still failing)

The image below shows where these options are located in the tab (indicated with red boxes).
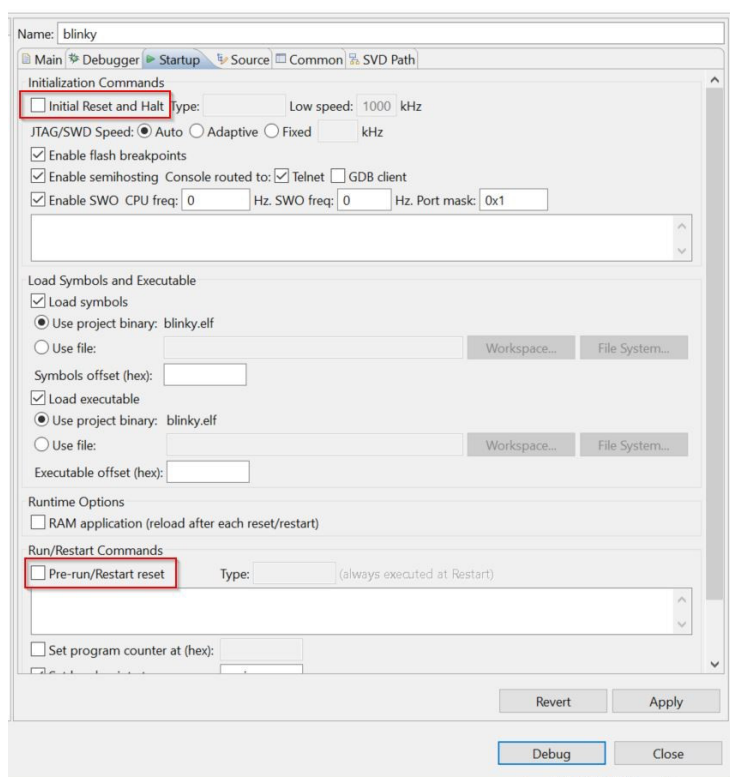
With this workaround, the debug session is expected to start normally and allow full debugging on the Arm Cortex−M3 core.

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