Ultrafast Diode 50 A, 600 V

RURG5060

Description

The RURG5060 is an ultrafast diode with low forward voltage drop. This device is intended for use as freewheeling and clamping diodes in a variety of switching power supplies and other power switching applications. It is specially suited for use in switching power supplies and industrial application.

Features

- Ultrafast Recovery ($t_{rr} = 75 \text{ ns}$ (@ $I_F = 50 \text{ A}$)
- Max Forward Voltage($V_F = 1.6 \text{ V}$ (@ $T_C = 25 \,^{\circ}\text{C}$)
- 600 V Reverse Voltage and High Reliability
- Avalanche Energy Rated
- This Device is Pb-Free and is RoHS Compliant

Applications

- Switching Power Supplies
- Power Switching Circuits
- General Purpose

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise noted)

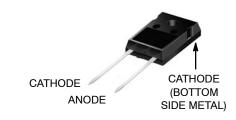
Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	600	V
Working Peak Reverse Voltage	V_{RWM}	600	V
DC Blocking Voltage	V_{R}	600	V
Average Rectified Forward Current (T _C = 102 °C)	I _{F(AV)}	50	Α
Repetitive Peak Surge Current (Square Wave, 20 kHz)	I _{FRM}	100	Α
Nonrepetitive Peak Surge Current (Halfwave 1 Phase, 60 Hz)	I _{FSM}	500	Α
Maximum Power Dissipation	P_{D}	150	W
Avalanche Energy (See Figure 7 and Figure 8)	E _{AVL}	40	mJ
Operating and Storage Temperature	T _{STG,} T _J	-65 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



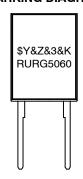
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JEDEC STYLE 2 LEAD TO-247 340CL

MARKING DIAGRAM



\$Y = ON Semiconductor Logo &Z = Assembly Plant Code &3 = Numeric Date Code &K = Lot Code RURG5060 = Specific Device Code



ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

RURG5060

PACKAGE MARKING AND ORDERING INFORMATION

Part Number	Package	Brand
RURG5060	TO-247-2L	RURG5060

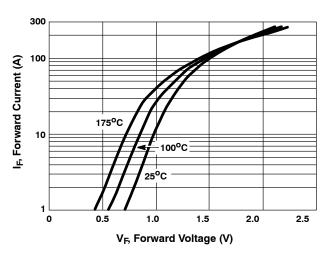
ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
V _F Instantaneous Forward Voltage	V_F Instantaneous Forward Voltage (Pulse Width = 300 μs, Duty Cycle = 2%) $I_F = 50 \text{ A}$ $I_F = 50 \text{ A}$ $T_C = 150 ^{\circ}\text{C}$	I _F = 50 A			1.6	V
		I _F = 50 A, T _C = 150°C			1.4	V
I _R	Instantaneous Reverse Current	V _R = 600 V			250	μΑ
		V _R = 600 V T _C = 150°C			1.5	mA
T _{rr}	Reverse Recovery Time at dIF/dt = 100A/ μ s (See Figure 6) Summation of t_a + t_b	I _F = 1 A, dI _F /dt = 100 A/μs			65	ns
		$I_F = 50 \text{ A},$ $dI_F/dt = 100 \text{ A}/\mu\text{s}$			75	ns
t _a	Time to Reach Peak Reverse Current at dIF/dt = 100A/μs (See Figure 6)	I _F = 50 A, dI _F /dt = 100 A/μs		30		ns
t _b	Time from Peak I_{RM} to Projected Zero Crossing of I_{RM} Based on a Straight Line from Peak I_{RM} Through 25% of I_{RM} (See Figure 6)	$I_F = 50 \text{ A},$ $dI_F/dt = 100 \text{ A}/\mu\text{s}$		20		ns
$R_{ heta JC}$	Thermal Resistance Junction to Case				1.0	°C/W

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

RURG5060

TYPICAL PERFORMANCE CURVES



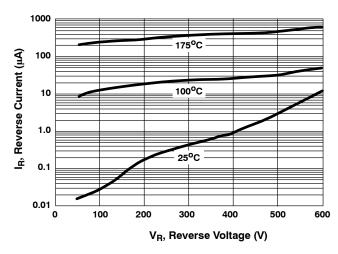
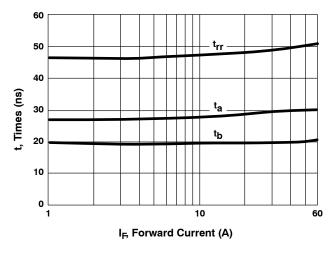


Figure 1. Forward Current vs. Forward Voltage

Figure 2. Reverse Current vs. Reverse Voltage



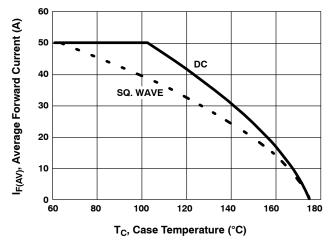


Figure 3. $T_{rr},\,t_a$ and t_b Curves vs. Forward Current

Figure 4. Current Derating Curve

RURG5060

TEST CIRCUITS AND WAVEFORMS

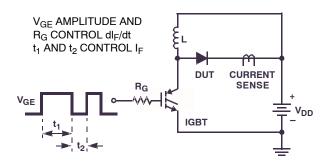


Figure 5. T_{rr} Test Circuit

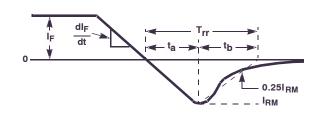


Figure 6. T_{rr} Waveforms and Definitions

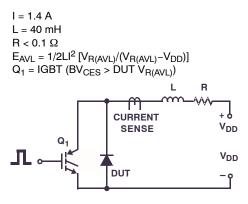


Figure 7. Avalanche Energy Test Circuit

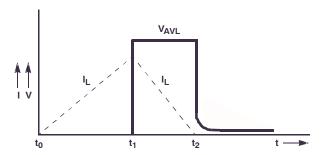
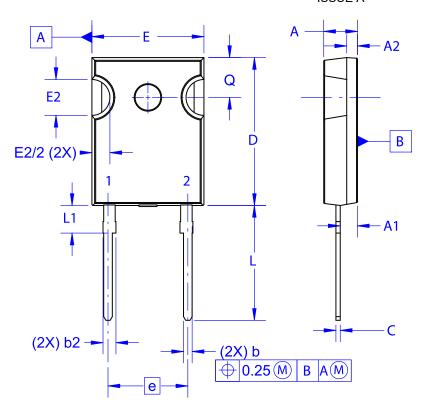


Figure 8. Avalanche Current and Voltage Waveforms

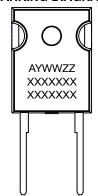
TO-247-2LD CASE 340CL **ISSUE A**





- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
 D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code

= Assembly Location

= Year

WW = Work Week

= Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

	DATE 03 DEC 2019		
Ø P —		Ø P1 D2	
E1 —	1	D1	
,		9	

DIM	MIL	LIMETER	S
	MIN	NOM	MAX
Α	4.58	4.70	4.82
A1	2.29	2.40	2.66
A2	1.30	1.50	1.70
b	1.17	1.26	1.35
b2	1.53	1.65	1.77
С	0.51	0.61	0.71
D	20.32	20.57	20.82
D1	16.37	16.57	16.77
D2	0.51	0.93	1.35
Е	15.37	15.62	15.87
E1	12.81	~	~
E2	4.96	5.08	5.20
е	~	11.12	~
L	15.75	16.00	16.25
L1	3.69	3.81	3.93
ØΡ	3.51	3.58	3.65
ØP1	6.61	6.73	6.85
Q	5.34	5.46	5.58
S	5.34	5.46	5.58

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