

# **MOSFET** - Power, Single N-Channel, Small Signal, **SOT-23** 40 V, 6.6 A

## **NVRGS042N04CL**

#### **Features**

- Low R<sub>DS(on)</sub>
- Small Footprint Surface Mount Package
- Trench Technology
- AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			$V_{DSS}$	40	٧
Gate-to-Source Voltage			$V_{GS}$	±20	V
Continuous Drain		T <sub>L</sub> = 25°C	I <sub>D</sub>	6.6	Α
Current R <sub>θJL</sub>	Steady	T <sub>L</sub> = 100°C		4.6	
Power Dissipation	State	T <sub>L</sub> = 25°C	$P_{D}$	2.4	W
$R_{\theta JL}$		T <sub>L</sub> = 100°C		1.2	
Continuous Drain		T <sub>A</sub> = 25°C	I <sub>D</sub>	5.2	Α
Current R <sub>θJA</sub>	Steady State	T <sub>A</sub> = 100°C		3.6	
Power Dissipation $R_{\theta JA}$		T <sub>A</sub> = 25°C	$P_{D}$	1.5	W
		T <sub>A</sub> = 100°C		0.7	
Pulsed Drain Current	$T_A = 25$	°C, t <sub>p</sub> = 10 μs	I <sub>DM</sub>	151	Α
Operating Junction and Storage Temperature Range			T <sub>J</sub> , T <sub>stg</sub> -55 to +175		°C
Source Current (Body Diode)			IS	2.0	Α
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

#### THERMAL CHARACTERISTICS

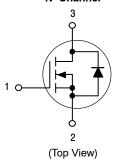
Characteristic	Symbol	Max	Unit
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	101	°C/ W
Junction-to-Lead - Steady State (Notes 1, 2)	$R_{ heta JL}$	62.3	VV

- device mounted to 1 inch2 1s2p 2 oz copper
- 2. lead temp is referenced at the top of the drain pin

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> MAX	I <sub>D</sub> MAX (Note 1)
40 V	49 mΩ @ 4.5 V	6.6 A
40 V	34 m $\Omega$ @ 10 V	0.0 A

#### **Simplified Schematic**

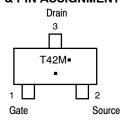
#### N-Channel



#### **MARKING DIAGRAM & PIN ASSIGNMENT**



**CASE 318** STYLE 21



T42 = Device Code = Date Code

= Pb-Free Package (Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NVRGS042N04CLT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

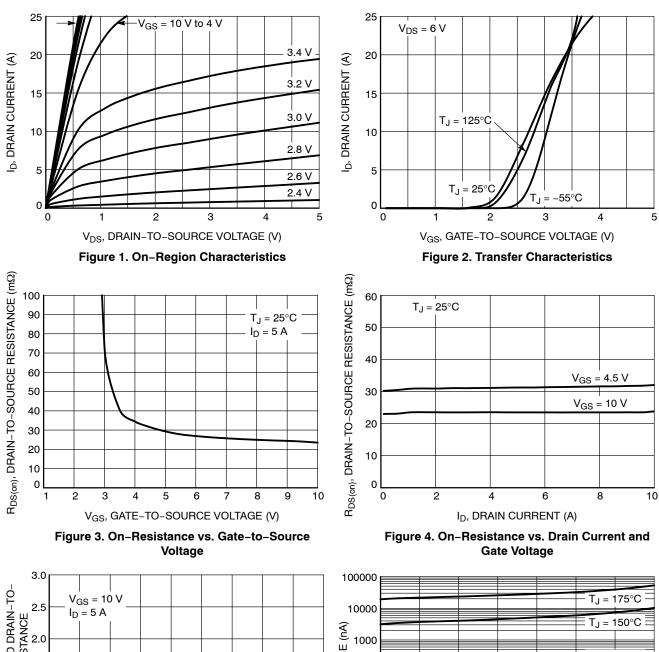
## **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Тур	Max	Units
OFF CHARACTERISTICS	•	•	•			•
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	40			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>					mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 40 V, T <sub>J</sub> = 25°C			10	μΑ
		V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 40 V, T <sub>J</sub> = 125°C			250	
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = 20 V			100	nA
ON CHARACTERISTICS (Note 3)					•	
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}, I_D = 250 \mu A$	1.2		2.2	V
Negative Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>			-5.4		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5 A		23.5	34	mΩ
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 5 A		31.5	49	
Forward Transconductance	9FS	V <sub>DS</sub> = 6 V, I <sub>D</sub> = 5 A		13.9		S
CHARGES AND CAPACITANCES					•	
Input Capacitance	C <sub>ISS</sub>			423		pF
Output Capacitance	C <sub>OSS</sub>	$V_{GS} = 0 \text{ V, f} = 1 \text{ MHz,} $ $V_{DS} = 25 \text{ V}$		189		
Reverse Transfer Capacitance	C <sub>RSS</sub>	- 100 - 20 1		9.7		
Total Gate Charge	Q <sub>G(TOT)</sub>			8.8		nC
Threshold Gate Charge	Q <sub>G(TH)</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 32 V;		0.8		
Gate-to-Source Charge	$Q_GS$	I <sub>D</sub> = 5 Å		1.4		
Gate-to-Drain Charge	$Q_GD$	1		1.6		
SWITCHING CHARACTERISTICS, V <sub>GS</sub>	= <b>V</b> (Note 4)	•		•		•
Turn-On Delay Time	t <sub>d(ON)</sub>			6.0		ns
Rise Time	t <sub>r</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 32 V,		6.3		
Turn-Off Delay Time	t <sub>d(OFF)</sub>	$I_D = 5 \text{ A}, R_G = 1 \Omega$		16		
Fall Time	t <sub>f</sub>	1		2.3		
DRAIN-SOURCE DIODE CHARACTER	ISTICS					
Forward Diode Voltage	V <sub>SD</sub>	$V_{GS} = 0 \text{ V}, I_S = 5 \text{ A}, T_J = 25^{\circ}\text{C}$		0.86	1.2	V
		V <sub>GS</sub> = 0 V, I <sub>S</sub> = 5 A, T <sub>J</sub> = 125°C		0.74		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulse Test: pulse width  $\leq 300~\mu s$ , duty cycle  $\leq 2\%$ 4. Switching characteristics are independent of operating junction temperatures

#### **TYPICAL CHARACTERISTICS**



2.5 V<sub>GS</sub> = 10 V I<sub>D</sub> = 5 A V<sub>GS</sub> = 10 V I<sub>D</sub> = 5 A I<sub>D</sub> = 5 A O -50 -25 0 25 50 75 100 125 150 175

Figure 5. On–Resistance Variation with Temperature

T<sub>J</sub>, JUNCTION TEMPERATURE (°C)

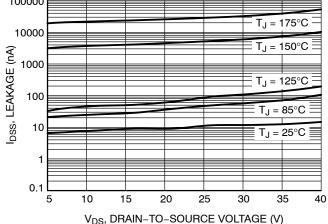


Figure 6. Drain-to-Source Leakage Current vs. Voltage

#### **TYPICAL CHARACTERISTICS**

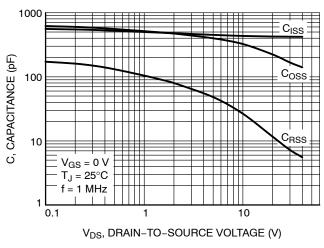


Figure 7. Capacitance Variation

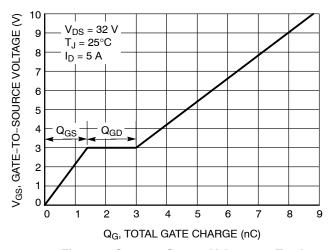


Figure 8. Gate-to-Source Voltage vs. Total Charge

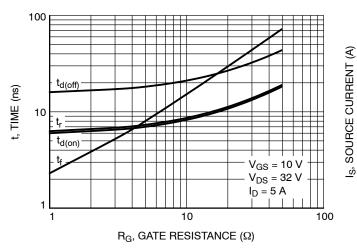


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

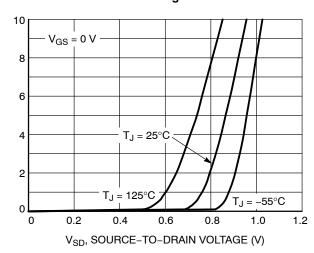


Figure 10. Diode Forward Voltage vs. Current

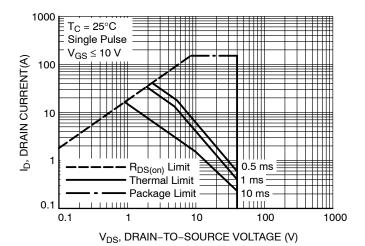


Figure 11. Maximum Rated Forward Biased Safe Operating Area

## **TYPICAL CHARACTERISTICS**

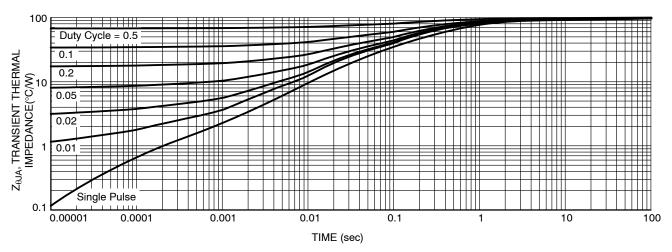
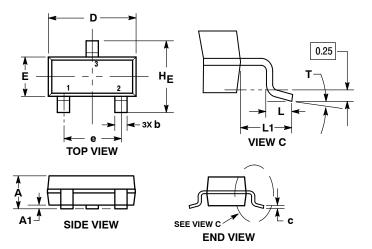


Figure 12. Thermal Response

#### PACKAGE DIMENSIONS

#### SOT-23 (TO-236) CASE 318-08 **ISSUE AS**



#### NOTES:

- NOTES:

  1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.

  2. CONTROLLING DIMENSION: MILLIMETERS.

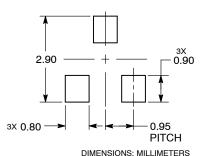
  3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH.
  MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
  DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,
- PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS				INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.89	1.00	1.11	0.035	0.039	0.044	
A1	0.01	0.06	0.10	0.000	0.002	0.004	
b	0.37	0.44	0.50	0.015	0.017	0.020	
С	0.08	0.14	0.20	0.003	0.006	0.008	
D	2.80	2.90	3.04	0.110	0.114	0.120	
Е	1.20	1.30	1.40	0.047	0.051	0.055	
е	1.78	1.90	2.04	0.070	0.075	0.080	
L	0.30	0.43	0.55	0.012	0.017	0.022	
L1	0.35	0.54	0.69	0.014	0.021	0.027	
HE	2.10	2.40	2.64	0.083	0.094	0.104	
Т	0°		10°	0°		10°	

#### STYLE 21:

- PIN 1. GATE
  - SOURCE 2.
  - DRAIN

#### **RECOMMENDED SOLDERING FOOTPRINT**



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