MOSFET – Power, P-Channel, Schottky Diode, μCool 2x2 mm

-20 V, -4.1 A, 2.0 A

NTLJF3117P

Features

- FETKY™ Configuration with MOSFET plus Low Vf Schottky Diode
- µCOOL™ Package Provides Exposed Drain Pad for Excellent Thermal Conduction
- 2x2 mm Footprint Same as SC-88 Package Design
- Independent Pinout Provides Circuit Design Flexibility
- Low Profile (< 0.8 mm) for Easy Fit in Thin Environment
- High Current Schottky Diode: 2 A Current Rating
- This is a Pb-Free Device

Applications

- Optimized for Portable Applications like Cell Phones, Digital Cameras, Media Players, etc.
- DC-DC Buck Circuit
- Li-Ion Battery Applications
- Color Display and Camera Flash Regulators

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Param	Symbol	Value	Unit		
Drain-to-Source Voltag	е		V_{DSS}	-20	V
Gate-to-Source Voltage	€		V_{GS}	±8.0	V
Continuous Drain	Steady	T _A = 25°C	I _D	-3.3	Α
Current (Note 1)	State	T _A = 85°C		-2.4	
	t ≤ 5 s	T _A = 25°C		-4.1	
Power Dissipation (Note 1)	Steady State	T _A = 25°C	P _D	1.5	W
	t ≤ 5 s			2.3	
Continuous Drain		T _A = 25°C	I _D	-2.3	Α
Current (Note 2)	Steady	T _A = 85°C		-1.6	
Power Dissipation (Note 2)	State	T _A = 25°C	P _D	0.71	W
Pulsed Drain Current	t _p =	: 10 μs	I _{DM}	-20	Α
Operating Junction and Storage Temperature			T _J , T _{STG}	–55 to 150	ç
Source Current (Body D	I _S	-1.9	Α		
Lead Temperature for S (1/8" from case for 10 s)	TL	260	°C		

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [1 oz] including traces).
- Surface Mounted on FR4 Board using the minimum recommended pad size of 30 mm², 2 oz Cu.



ON Semiconductor®

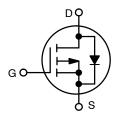
www.onsemi.com

MOSFET

V _{(BR)DSS}	R _{DS(on)} MAX	I _D MAX (Note 1)
	100 mΩ @ -4.5 V	
-20 V	135 mΩ @ –2.5 V	-4.1 A
	200 mΩ @ -1.8 V	

SCHOTTKY DIODE

V _R MAX	V _F TYP	I _F MAX
30 V	0.47 V	2.0 A





P-CHANNEL MOSFET

SCHOTTKY DIODE

MARKING DIAGRAM



WDFN6 CASE 506AN



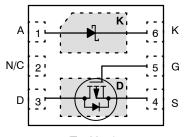
JH = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

PIN CONNECTIONS



(Top View)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 7 of this data sheet.

SCHOTTKY DIODE MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V_{RRM}	30	V
DC Blocking Voltage	V _R	30	V
Average Rectified Forward Current	I _F	2.0	Α

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Ambient - Steady State (Note 3)	$R_{ heta JA}$	83	
Junction–to–Ambient – $t \le 5 s$ (Note 3)	$R_{ heta JA}$	54	°C/W
Junction-to-Ambient - Steady State Min Pad (Note 4)	$R_{ heta JA}$	177	

- Surface Mounted on FR4 Board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
 Surface Mounted on FR4 Board using the minimum recommended pad size of 30 mm², 2 oz Cu.

$\textbf{MOSFET ELECTRICAL CHARACTERISTICS} \ (T_J = 25^{\circ}\text{C unless otherwise noted})$

Parameter	Symbol	Test Conditions		Min	Тур	Max	Unit
OFF CHARACTERISTICS							
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = -25$	60 μΑ	-20			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J	I _D = -250 μA, Ref to	25°C		9.95		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	V 40VV 0V	T _J = 25°C			-1.0	μΑ
		$V_{DS} = -16 \text{ V}, V_{GS} = 0 \text{ V}$	T _J = 85°C			-10	
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm$	8.0 V			±100	nA
ON CHARACTERISTICS (Note 5)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D = -25$	50 μΑ	-0.4	-0.7	-1.0	V
Negative Threshold Temperature Coefficient	V _{GS(TH)} /T _J				2.44		mV/°C
Drain-to-Source On-Resistance	R _{DS(on)}	$V_{GS} = -4.5$, $I_D = -2.0$ A $V_{GS} = -2.5$, $I_D = -2.0$ A $V_{GS} = -1.8$, $I_D = -1.6$ A			75	100	mΩ
					101	135	1
					150	200	1
Forward Transconductance	9FS	$V_{DS} = -5.0 \text{ V}, I_D = -2.0 \text{ A}$			3.1		S
CHARGES, CAPACITANCES AND GA	ATE RESISTAN	CE					
Input Capacitance	C _{ISS}				531		pF
Output Capacitance	C _{OSS}	$V_{GS} = 0 \text{ V, f} = 1.0 \text{ M}$ $V_{DS} = -10 \text{ V}$	ЛHz,		91		1
Reverse Transfer Capacitance	C _{RSS}	100			56		1
Total Gate Charge	Q _{G(TOT)}				5.5	6.2	nC
Threshold Gate Charge	Q _{G(TH)}				0.7		1
Gate-to-Source Charge	Q_{GS}	$V_{GS} = -4.5 \text{ V}, V_{DS} = -10 \text{ V},$ $I_{D} = -2.0 \text{ A}$			1.0		1
Gate-to-Drain Charge	Q_{GD}				1.4		1
Gate Resistance	R_{G}				8.8		Ω
SWITCHING CHARACTERISTICS (No	ote 6)						
Turn-On Delay Time	t _{d(ON)}				5.2		ns
Rise Time	t _r	V _{GS} = -4.5 V, V _{DD} = -	-5.0 V,		13.2		1
Turn-Off Delay Time	t _{d(OFF)}	$I_D = -1.0 \text{ A}, R_G = 6.0 \Omega$			13.7		1
	•	1		-	+	•	-1

5. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

Fall Time

6. Switching characteristics are independent of operating junction temperatures.

19.1

MOSFET ELECTRICAL CHARACTERISTICS ($T_J = 25$ °C unless otherwise noted)

Parameter	Symbol	Test Condition	าร	Min	Тур	Max	Unit	
SWITCHING CHARACTERISTICS	(Note 6)							
Turn-On Delay Time	t _{d(ON)}				5.5		ns	
Rise Time	t _r	$V_{GS} = -4.5 \text{ V}, V_{DD} =$	$V_{GS} = -4.5 \text{ V}, V_{DD} = -10 \text{ V},$		15			
Turn-Off Delay Time	t _{d(OFF)}	$I_D = -2.0 \text{ A}, R_G = 2.0 \Omega$			19.8		1	
Fall Time	t _f				21.6		1	
DRAIN-SOURCE DIODE CHARA	DRAIN-SOURCE DIODE CHARACTERISTICS							
Forward Recovery Voltage	V _{SD}	V 0V IS 10A	T _J = 25°C		-0.75	-1.0	V	
		$V_{GS} = 0 \text{ V, IS} = -1.0 \text{ A}$	T _J = 125°C		-0.64]	
Reverse Recovery Time	t _{RR}				16.2			

$$\begin{split} V_{GS} = 0 \ V, \ d_{ISD}/d_t = 100 \ A/\mu s, \\ I_S = -1.0 \ A \end{split}$$

10.6

5.6

5.7

ns

nC

5. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.

Charge Time

Discharge Time

Reverse Recovery Time

6. Switching characteristics are independent of operating junction temperatures.

SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

ta

tb

 \mathbf{Q}_{RR}

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Maximum Instantaneous	V _F	I _F = 0.1 A		0.34	0.39	V
Forward Voltage		I _F = 1.0 A		0.47	0.53	
Maximum Instantaneous	I _R	V _R = 30 V		17	20	μΑ
Reverse Current		V _R = 20 V		3.0	8.0	
		V _R = 10 V		2.0	4.5	1

SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS ($T_J = 85^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Maximum Instantaneous	V _F	I _F = 0.1 A		0.22	0.35	V
Forward Voltage		I _F = 1.0 A		0.40	0.50	
Maximum Instantaneous	I _R	V _R = 30 V		0.22	2.5	mA
Reverse Current		V _R = 20 V		0.11	1.6	
		V _R = 10 V		0.06	1.2	

SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T_J = 125°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Maximum Instantaneous	V _F	I _F = 0.1 A		0.2	0.29	V
Forward Voltage		I _F = 1.0 A		0.4	0.47	
Maximum Instantaneous	I _R	V _R = 30 V		2.0	20	mA
Reverse Current		V _R = 20 V		1.1	10.9	
		V _R = 10 V		0.63	8.4	

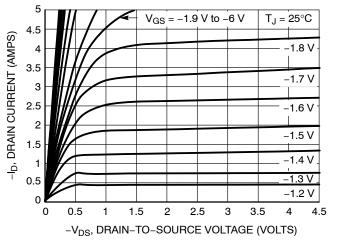
SCHOTTKY DIODE ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Capacitance	С	$V_R = 5.0 \text{ V}, f = 1.0 \text{ MHz}$		38		pF

- 7. Surface-mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [2 oz] including traces).
 8. Surface-mounted on FR4 board using the minimum recommended pad size of 30 mm², 2 oz cu.
- 9. Pulse Test: pulse width \leq 300 μ s, duty cycle \leq 2%.
- 10. Switching characteristics are independent of operating junction temperatures.

TYPICAL PERFORMANCE CURVES (T_J = 25°C unless otherwise noted)

 $V_{DS} \ge 10 \text{ V}$

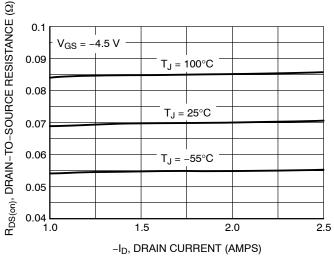


90 4 3 2 25°C T_J = -55°C 1 0 0 0.5 1 1.5 2 2.5 3

Figure 1. On-Region Characteristics

-V_{GS}, GATE-TO-SOURCE VOLTAGE (VOLTS)

Figure 2. Transfer Characteristics



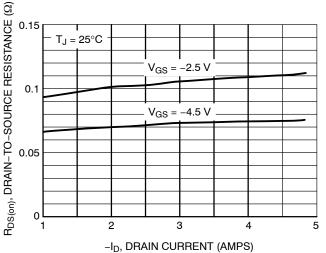
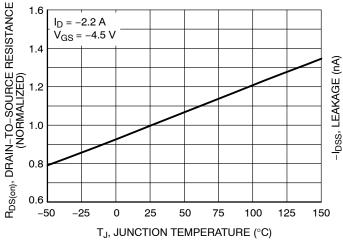


Figure 3. On-Resistance versus Drain Current

Figure 4. On-Resistance versus Drain Current and Gate Voltage



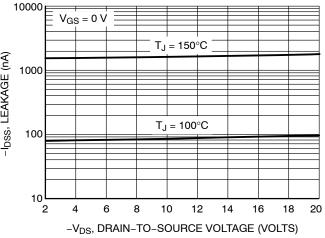
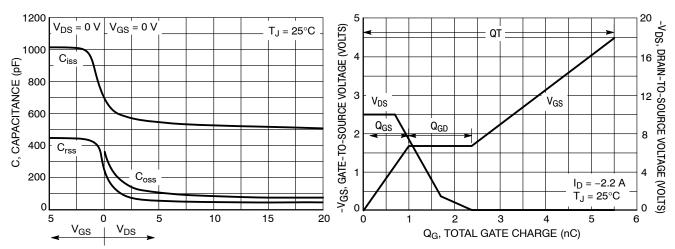


Figure 5. On–Resistance Variation with Temperature

Figure 6. Drain-to-Source Leakage Current versus Voltage

TYPICAL PERFORMANCE CURVES (T_J = 25°C unless otherwise noted)



GATE-TO-SOURCE OR DRAIN-TO-SOURCE VOLTAGE (VOLTS)

Figure 7. Capacitance Variation

Figure 8. Gate-To-Source and Drain-To-Source Voltage versus Total Charge

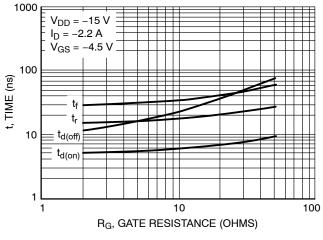


Figure 9. Resistive Switching Time Variation versus Gate Resistance

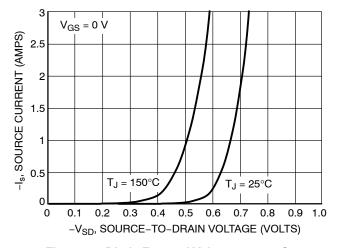


Figure 10. Diode Forward Voltage versus Current

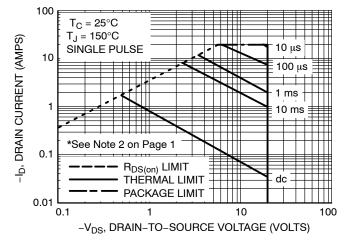


Figure 11. Maximum Rated Forward Biased Safe Operating Area

$\textbf{TYPICAL PERFORMANCE CURVES} \ \, (T_J = 25^{\circ}\text{C unless otherwise noted})$

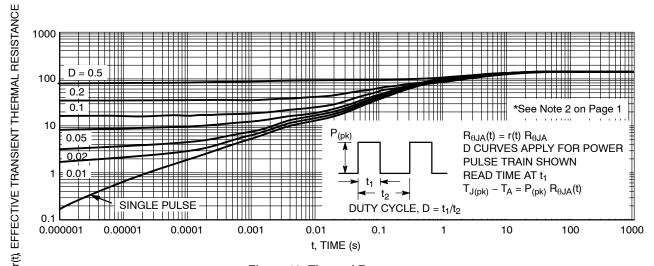
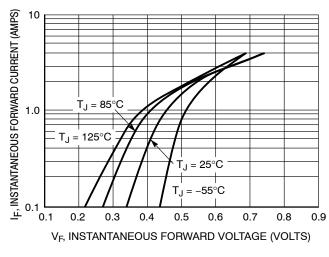


Figure 12. Thermal Response

TYPICAL SCHOTTKY PERFORMANCE CURVES (T_J = 25°C unless otherwise noted)



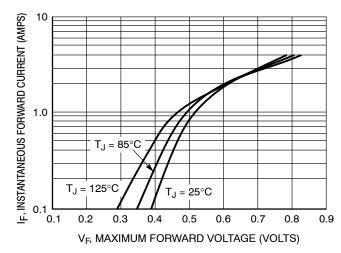


Figure 13. Typical Forward Voltage

Figure 14. Maximum Forward Voltage

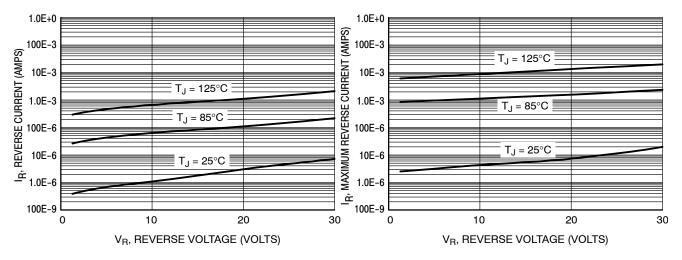


Figure 15. Typical Reverse Current

Figure 16. Maximum Reverse Current

ORDERING INFORMATION

Device	Package	Shipping [†]
NTLJF3117PT1G	WDFN6 (Pb-Free)	3000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

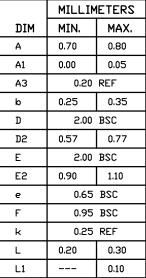


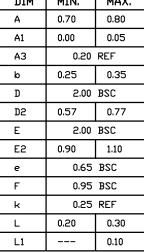


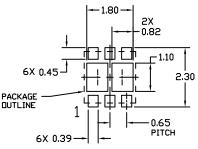
DATE 25 JAN 2022

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER. ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- DIMENSION 6 APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 MM FROM THE TERMINAL TIP.
- 4. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE TERMINALS.

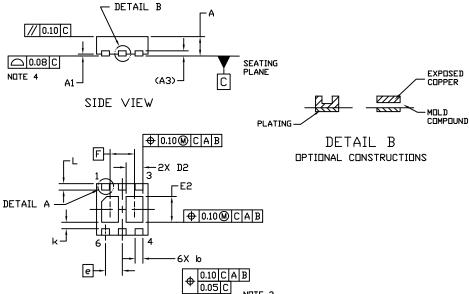






RECOMMENDED MOUNTING FOOTPRINT SOLDERMASK DEFINED

PIN ONE REFERENCE	D	B		
□[0.10]C			<u>↓</u>	
(a) 0.10 (c)			L1 □ DE T4	AIL A
	TOP VIEW		OPTIONAL CONSTRUCTIONS	



GENERIC MARKING DIAGRAM*

BOTTOM VIEW



XX = Specific Device Code = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

DOCUMENT NUMBER:	98AON20861D	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.			
DESCRIPTION:	WDFN6 2x2, 0.65P		PAGE 1 OF 1		

onsemi and ONSEMI. are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. **onsemi** makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, ONSEMI., and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems. or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales