

Silicon Carbide (SiC) MOSFET – EliteSiC, 20 mohm, 900 V, M2, TO-247-3L

NTHL020N090SC1

Features

- Typ. $R_{DS(on)} = 20 \text{ m}\Omega @ V_{GS} = 15 \text{ V}$
- Typ. $R_{DS(on)} = 16 \text{ m}\Omega @ V_{GS} = 18 \text{ V}$
- Ultra Low Gate Charge $(Q_{G(tot)} = 196 \text{ nC})$
- Low Effective Output Capacitance (Coss = 296 pF)
- 100% UIL Tested
- This Device is Halide Free and RoHS Compliant with exemption 7a, Pb–Free 2LI (on second level interconnection)

Typical Applications

- UPS
- DC-DC Converter
- Boost Inverter

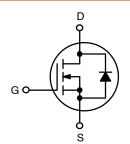
MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V _{DSS}	900 ss	
Gate-to-Source Voltaç	ge		V _{GS}	+22/-8	V
Recommended Operation Values of Gate – Source Voltage		V_{GSop}	+15/-5	V	
Continuous Drain Current R ₀ JC	Steady State	T _C = 25°C	I _{DC}	118	Α
Power Dissipation $R_{\theta JC}$			P _{DC}	503	W
Continuous Drain Current R _{θJC}	Steady State	T _C = 100°C	I _{DC}	83	Α
Power Dissipation $R_{\theta JC}$			P _{DC}	251	W
Pulsed Drain Current (Note 2) T _A = 25°C			I _{DM}	472	Α
Operating Junction and Storage Temperature Range			T _J , T _{stg}	-55 to +175	°C
Source Current (Body Diode)			Is	153	Α
Single Pulse Drain-to-Source Avalanche Energy ($I_L = 23 A_{pk}, L = 1 \text{ mH}$) (Note 3)			E _{AS}	264	mJ

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- The entire application environment impacts the thermal resistance values shown, they are not constants and are only valid for the particular conditions noted.
- 2. Repetitive rating, limited by max junction temperature.
- 3. E_{AS} of 162 mJ is based on starting T_J = 25°C; L = 1 mH, I_{AS} = 23 A, V_{DD} = 100 V, V_{GS} = 15 V.

V _{(BR)DSS}	R _{DS(ON)} MAX	I _D MAX
900 V	28 mΩ @ 15 V	118 A

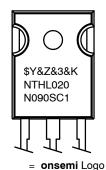


N-CHANNEL MOSFET



TO-247-3LD CASE 340CX

MARKING DIAGRAM



\$Y = **onsemi** Logo &Z = Assembly Plant Code &3 = Date Code (Year & Week) &K = Lot

NTHL020N090SC1 = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping
NTHL020N090SC1	TO-247-3LD	30 Units / Tube

Table 1. THERMAL CHARACTERISTICS

Parameter	Symbol	Max	Unit
Thermal Resistance Junction-to-Case (Note 1)	$R_{ heta JC}$	0.30	°C/W
Thermal Resistance Junction-to-Ambient (Note 1)	$R_{ hetaJA}$	40	°C/W

Table 2. ELECTRICAL CHARACTERISTICS (T_{.I} = 25°C unless otherwise stated)

Parameter	Symbol	Test Condition	Min	Тур	Max	Unit
OFF CHARACTERISTICS		•				
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 1 mA	900			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /	I _D = 1 mA, refer to 25°C		500		mV/
Zero Gate Voltage Drain Current	I _{DSS}	V _{GS} = 0 V, T _J = 25°C			100	μΑ
		V _{DS} = 900 V T _J = 175°C			250	μΑ
Gate-to-Source Leakage Current	I _{GSS}	V _{GS} = +22/-8 V, V _{DS} = 0 V			±1	μΑ
ON CHARACTERISTICS	•		•	•		•
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}$, $I_D = 20 \text{ mA}$	1.8	2.7	4.3	V
Recommended Gate Voltage	V_{GOP}		-5		+15	V
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 15 V, I _D = 60 A, T _J = 25°C		20	28	mΩ
		V _{GS} = 18 V, I _D = 60 A, T _J = 25°C		16		1
		V _{GS} = 15 V, I _D = 60 A, T _J = 175°C		27		
Forward Transconductance	9FS	V _{DS} = 20 V, I _D = 60 A		49		S
CHARGES, CAPACITANCES & GATE RES	ISTANCE	•		•		
Input Capacitance	C _{ISS}	V _{GS} = 0 V, f = 1 MHz,		4415		pF
Output Capacitance	C _{OSS}	V _{DS} = 450 V		296		
Reverse Transfer Capacitance	C _{RSS}	-		24		
Total Gate Charge	Q _{G(TOT)}	$V_{GS} = -5/15 \text{ V}, V_{DS} = 720 \text{ V},$ $I_D = 60 \text{ A}$		196		nC
Threshold Gate Charge	Q _{G(TH)}			42		
Gate-to-Source Charge	Q _{GS}			78		
Gate-to-Drain Charge	Q_{GD}	1		55		
Gate-Resistance	R_{G}	f = 1 MHz		1.6		Ω
SWITCHING CHARACTERISTICS		•				
Turn-On Delay Time	t _{d(ON)}	$V_{GS} = -5/15 \text{ V}, V_{DS} = 720 \text{ V},$		40		ns
Rise Time	t _r	I_D = 60 A, R_G = 2.5 Ω , Inductive Load		63		1
Turn-Off Delay Time	t _{d(OFF)}	1		55		1
Fall Time	t _f	1		13		1
Turn-On Switching Loss	E _{ON}	1		2025		μJ
Turn-Off Switching Loss	E _{OFF}	1		201		1
Total Switching Loss	E _{TOT}	1		2226		1
DRAIN-SOURCE DIODE CHARACTERIST			•			
Continuous Drain-Source Diode Forward Current	I _{SD}	V _{GS} = -5 V, T _J = 25°C			153	Α
Pulsed Drain–Source Diode Forward Current (Note 2)	I _{SDM}	V _{GS} = -5 V, T _J = 25°C			472	Α
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Table 2. ELECTRICAL CHARACTERISTICS (T_{.I} = 25°C unless otherwise stated)

Table 2. LELEST THOME STATEMENT THIS TOP (1) - 25 G at the control of table 4)							
Parameter	Symbol	Test Condition	Min	Тур	Max	Unit	
DRAIN-SOURCE DIODE CHARACTERISTICS							
Reverse Recovery Time	t _{RR}	$V_{GS} = -5/15 \text{ V}, I_{SD} = 60 \text{ A},$ $dI_S/dt = 1000 \text{ A}/\mu\text{s}, V_{DS} = 720 \text{ V}$		28		ns	
Reverse Recovery Charge	Q _{RR}			199		nC	
Reverse Recovery Energy	E _{REC}			4		μJ	
Peak Reverse Recovery Current	I _{RRM}			14		Α	
Charge Time	Ta			16		ns	
Discharge Time	Tb			12		ns	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

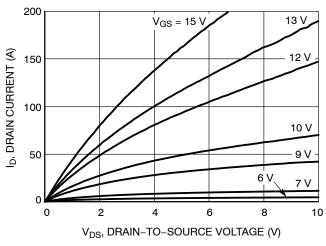


Figure 1. On-Region Characteristics

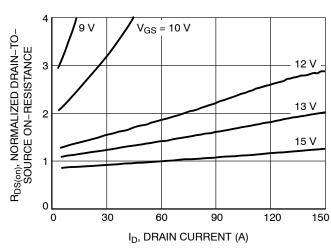


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

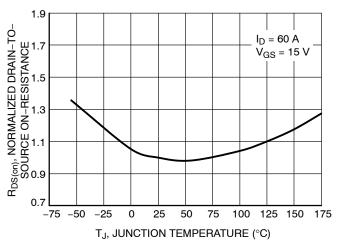


Figure 3. On–Resistance Variation with Temperature

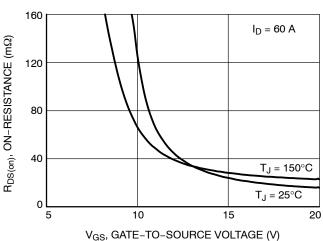


Figure 4. On-Resistance vs. Gate-to-Source Voltage

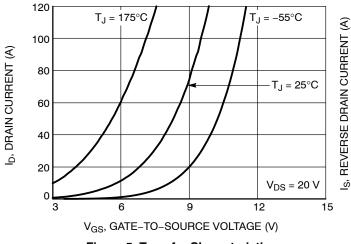


Figure 5. Transfer Characteristics

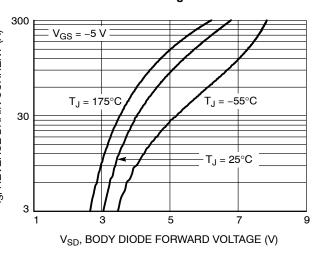


Figure 6. Diode Forward Voltage vs. Current

TYPICAL CHARACTERISTICS (continued)

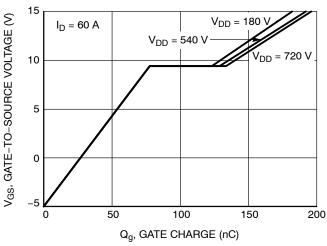


Figure 7. Gate-to-Source Voltage vs. Total Charge

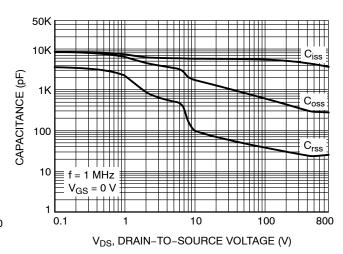


Figure 8. Capacitance vs. Drain-to-Source Voltage

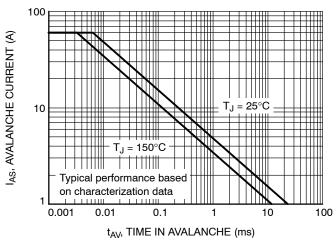


Figure 9. Unclamped Inductive Switching Capability

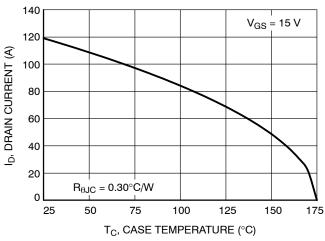


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

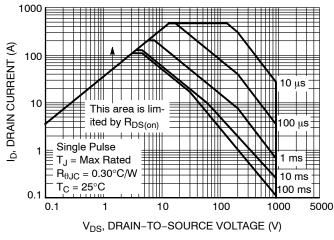


Figure 11. Safe Operating Area

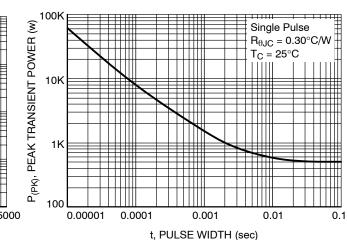


Figure 12. Single Pulse Maximum Power Dissipation

TYPICAL CHARACTERISTICS (continued)

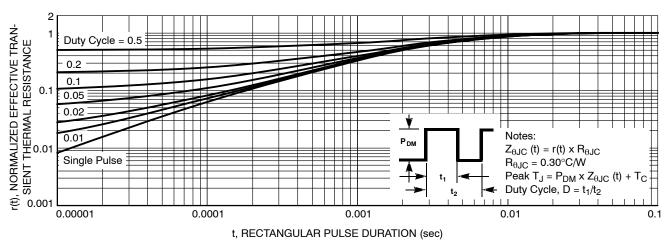
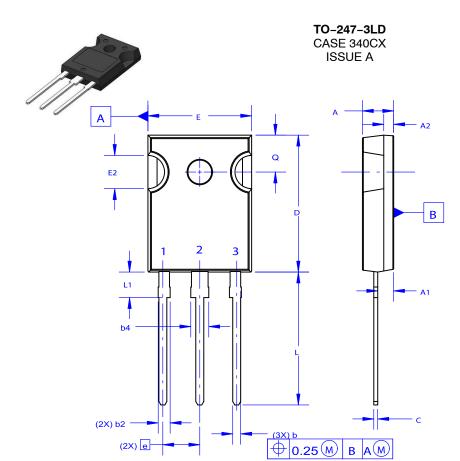
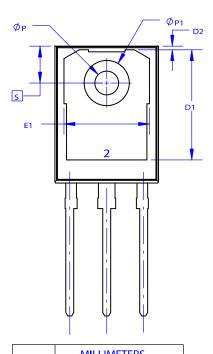


Figure 13. Junction-to-Ambient Transient Thermal Response Curve



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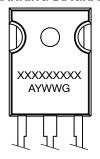


NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.

 B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code = Assembly Location

= Year WW = Work Week G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " =", may or may not be present. Some products may not follow the Generic Marking.

DIM	MILLIMETERS					
DIM	MIN	NOM	MAX			
Α	4.58	4.70	4.82			
A 1	2.20	2.40	2.60			
A2	1.40	1.50	1.60			
D	20.32	20.57	20.82			
Е	15.37	15.62	15.87			
E2	4.96	5.08	5.20			
е	~	5.56	~			
L	19.75	20.00	20.25			
L1	3.69	3.81	3.93			
ØΡ	3.51	3.58	3.65			
Q	5.34	5.46	5.58			
S	5.34	5.46	5.58			
b	1.17	1.26	1.35			
b2	1.53	1.65	1.77			
b4	2.42	2.54	2.66			
С	0.51	0.61	0.71			
D1	13.08	~	~			
D2	0.51	0.93	1.35			
E1	12.81	~	~			
Ø P 1	6.60	6.80	7.00			

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