# Schottky Barrier Diode, 500 mA, 30 V

#### NSR05T304MX

These Schottky barrier diodes are optimized for low forward voltage drop and low leakage current that offers the most optimal power dissipation in applications. They are housed in spacing saving micro-packaging ideal for space constraint applications.

#### **Features**

- Low Forward Voltage Drop 410 mV (Typ.) @  $I_F = 500 \text{ mA}$
- Low Reverse Current 35  $\mu$ A (Typ.) @  $V_R$  = 30 V
- 500 mA of Continuous Forward Current
- High Switching Speed
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

#### **Typical Applications**

- LCD and Keypad Backlighting
- Camera Photo Flash
- Buck and Boost dc-dc Converters
- Reverse Voltage and Current Protection
- Clamping & Protection

#### **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Reverse Voltage	V <sub>R</sub>	30	V
Forward Current (DC)	lF	500	mA
Forward Surge Current (60 Hz @ 1 cycle)	I <sub>FSM</sub>	2.2	А
Repetitive Peak Forward Current (Pulse Wave = 1 sec, Duty Cycle = 66%)	I <sub>FRM</sub>	1.4	Α

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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MARKING DIAGRAM

X2DFN2 CASE 714AB <sup>O</sup> ≻ M

= Specific Device Code

M = Date Code

= Pb-Free Package



#### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NSR05T304MXT5G	X2DFN2 (Pb-Free)	2 mm Pitch 8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

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#### NSR05T304MX

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Min	Тур	Max	Unit
Thermal Resistance Junction-to-Ambient (Note 1) Total Power Dissipation @ T <sub>A</sub> = 25°C	R <sub>θJA</sub> P <sub>D</sub>			310 480	°C/W mW
Thermal Resistance Junction-to-Ambient (Note 2) Total Power Dissipation @ T <sub>A</sub> = 25°C	R <sub>θJA</sub> P <sub>D</sub>			150 1000	°C/W mW
Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>		-55 to +85		°C

- 1. Mounted onto a 4 in square FR-4 board 50 mm sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.
- 2. Mounted onto a 4 in square FR-4 board 650 mm sq. 1 oz. Cu 0.06" thick single sided. Operating to steady state.

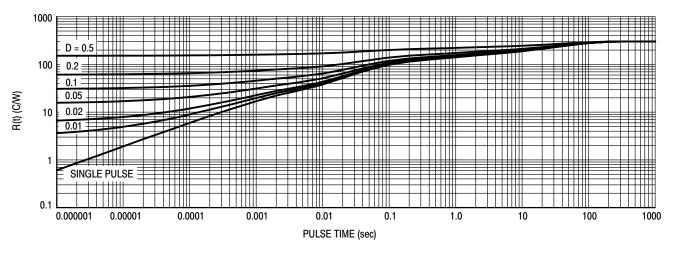


Figure 1. Thermal Response (Note 1)

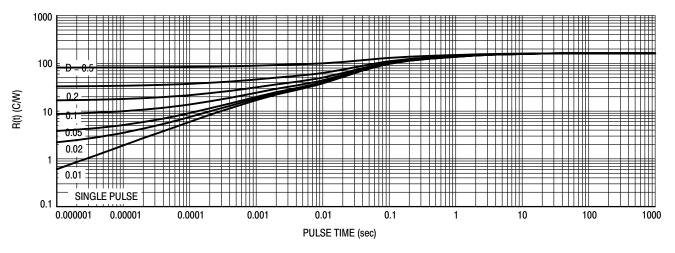
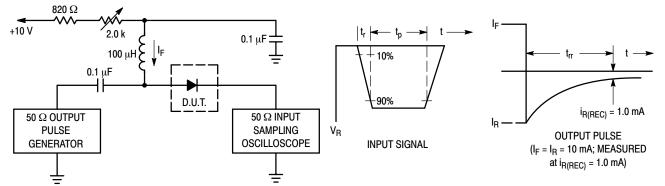


Figure 2. Thermal Response (Note 2)

#### NSR05T304MX

#### **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
Reverse Leakage (V <sub>R</sub> = 10 V) (V <sub>R</sub> = 30 V)	I <sub>R</sub>		20 35	50 75	μΑ
Forward Voltage (I <sub>F</sub> = 10 mA) (I <sub>F</sub> = 100 mA) (I <sub>F</sub> = 200 mA) (I <sub>F</sub> = 500 mA)	V <sub>F</sub>		215 295 330 410	245 320 355 435	mV
Total Capacitance (V <sub>R</sub> = 1.0 V, f = 1.0 MHz)	C <sub>T</sub>		30		pF
Reverse Recovery Time (I <sub>F</sub> = I <sub>R</sub> = 10 mA, I <sub>R(REC)</sub> = 1.0 mA, Figure 3)	t <sub>rr</sub>		15		ns
Peak Forward Recovery Voltage (I <sub>F</sub> = 100 mA, t <sub>r</sub> = 20 ns, Figure 4)	V <sub>FRM</sub>		430		mV



Notes: 1. A 2.0  $k\Omega$  variable resistor adjusted for a Forward Current (IF) of 10 mA.

- 2. Input pulse is adjusted so  $I_{R(peak)}$  is equal to 10 mA.
- 3. t<sub>n</sub> » t<sub>rr</sub>

Figure 3. Recovery Time Equivalent Test Circuit

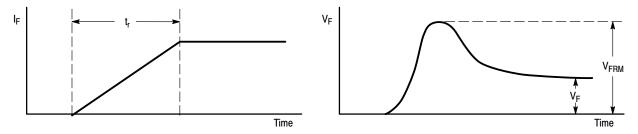


Figure 4. Peak Forward Recovery Voltage Definition

#### NSR05T304MX

#### **TYPICAL CHARACTERISTICS**

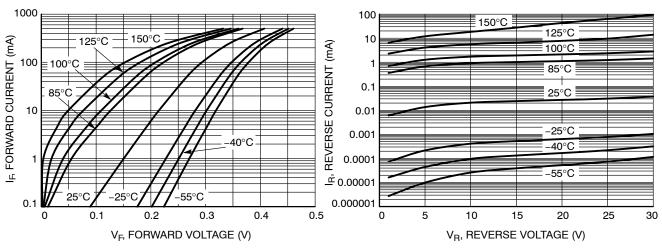
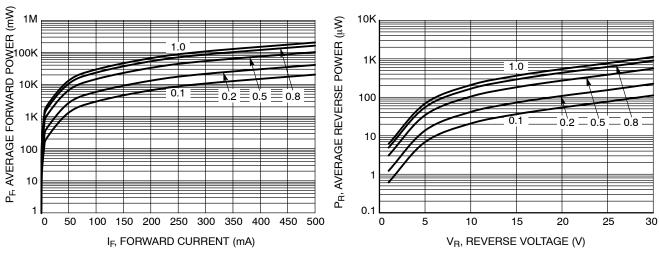


Figure 5. Forward Voltage

Figure 6. Leakage Current



f = 1.0 MHz

Figure 7. Average Forward Power Dissipation

90

80

70

60

50

40

30 20

10

0

5

10

C<sub>T</sub>, TOTAL CAPACITANCE (pF)

V<sub>R</sub>, REVERSE VOLTAGE (V)

Figure 9. Total Capacitance

15



Figure 8. Average Reverse Power Dissipation

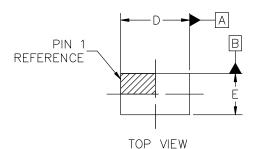
30

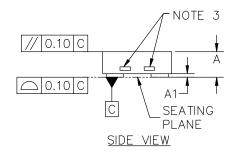


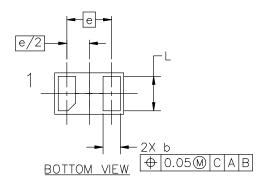


## **X2DFN2 1.00x0.60x0.37, 0.65P**CASE 714AB ISSUE C

**DATE 21 FEB 2024** 



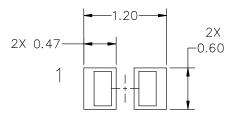




#### NOTES:

- DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
- 2. ALL DIMENSION ARE IN MILLIMETERS.
- 3. EXPOSED COPPER ALLOWED AS SHOW.

DIM	М	ILLIMETER	LIMETERS		
I IVII	MIN.	NOM.	MAX.		
А	0.34	0.37	0.40		
A1		0.03	0.050		
b	0.20	0.25	0.30		
D	0.95	1.00	1.05		
Е	0.55	0.60	0.65		
е	0.65 BSC				
L	0.45	0.50	0.55		



RECOMMENDED MOUNTING FOOTPRINT\*

\* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

### GENERIC MARKING DIAGRAM\*



XX = Specific Device Code M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present. Some products may not follow the Generic Marking.

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