NRTSAF260E, NRVTSAF260E

Very Low Forward Voltage Trench-based Schottky Rectifier

Features

- Fine Lithography Trench-based Schottky Technology for Very Low Forward Voltage and Low Leakage
- Fast Switching with Exceptional Temperature Stability
- Low Power Loss and Lower Operating Temperature
- Higher Efficiency for Achieving Regulatory Compliance
- Low Thermal Resistance
- High Surge Capability
- NRV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These are Pb-Free and Halide-Free Devices

Mechanical Characteristics:

- Case: Molded Epoxy
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight: 95 mg (Approximately)
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Maximum for 10 Seconds
- MSL 1

Typical Applications

 Switching Power Supplies including Compact Adapters and Flat Panel Display

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- High Frequency and DC-DC Converters
- Freewheeling and OR-ing diodes
- Reverse Battery Protection
- Instrumentation
- Automotive LED Lighting (Interior and Exterior)



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TRENCH SCHOTTKY
RECTIFIER
2.0 AMPERES
60 VOLTS



SMA-FL CASE 403AA SYTLE 6

MARKING DIAGRAM



26E = Specific Device Code A = Assembly Location

Y = Year
WW = Work Week
= Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping [†]
NRTSAF260ET3G	SMA-FL (Pb-Free)	10,000/ Tape & Reel
NRVTSAF260ET3G	SMA-FL (Pb-Free)	10,000/ Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

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MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	60	V
Average Rectified Forward Current $(T_L = 150^{\circ}C)$	Io	2.0	Α
Peak Repetitive Forward Current (Square Wave, 20 kHz, T _L = 147°C)	I _{FRM}	4.0	Α
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	50	Α
Storage and Operating Junction Temperature Range (Note 1)	T _{stg} , T _J	-65 to +175	°C
Voltage Rate of Change (Rated V_R , $T_J = 25^{\circ}C$)	dv/dt	10,000	V/μs
Controlled Avalanche Energy	W _{AVAL}	20	mJ

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic		Symbol	Value	Unit
Thermal Resistance, Junction-to-Lead (Note 2)		Ψ_{JCL}	24.6	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2)		$R_{ heta JA}$	79	°C/W
Thermal Resistance, Junction-to-Ambient (Note 3)	N	$R_{\theta JA}$	239	°C/W

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Тур	Max	Unit
$\label{eq:maximum Instantaneous Forward Voltage (Note 4)} \begin{tabular}{ll} (I_F=1.0 \ A, \ T_J=25^\circ C) \\ (I_F=2.0 \ A, \ T_J=25^\circ C) \\ (I_F=1.0 \ A, \ T_J=125^\circ C) \\ (I_F=2.0 \ A, \ T_J=125^\circ C) \\ \end{tabular}$	V _F	0.47 0.38 0.53 0.47	0.55 0.65 0.47 0.58	V
Maximum Instantaneous Reverse Current (Note 4) (Rated dc Voltage, T_J = 25°C) (Rated dc Voltage, T_J = 125°C)	I _R	3.0 1.0	12 3.0	μA mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- 2. Mounted with 700 mm² copper pad size (Approximately 1 in²) 1 oz FR4 Board.
- Mounted with pad size approximately 20 mm² copper, 1 oz FR4 Board.
 Pulse Test: Pulse Width ≤ 380 μs, Duty Cycle ≤ 2.0%.

^{1.} The heat generated must be less than the thermal conductivity from Junction–to–Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.

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TYPICAL CHARACTERISTICS

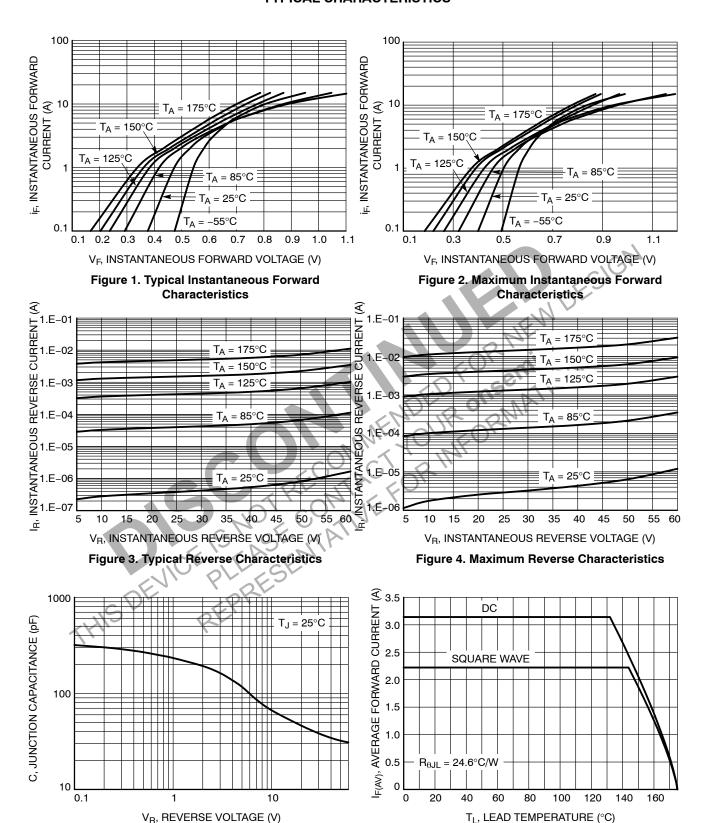


Figure 6. Current Derating

Figure 5. Typical Junction Capacitance

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TYPICAL CHARACTERISTICS

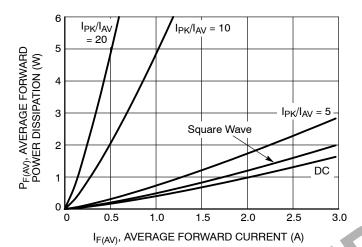


Figure 7. Forward Power Dissipation

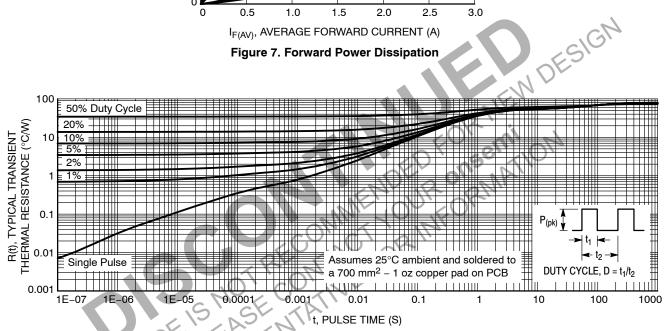


Figure 8. Typical Transient Thermal Response, Junction-to-Ambient

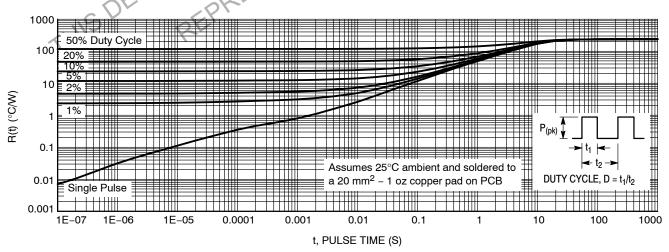


Figure 9. Typical Transient Thermal Response, Junction-to-Ambient





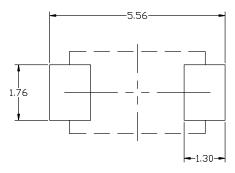
SMA 2.60x4.30x1.00 CASE 403AA ISSUE A

DATE 18 JAN 2024

NOTES:

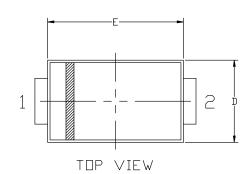
- DIMENSIONING AND TOLERANCING AS PER ASME Y14.5M, 2018.
- 2. CONTROLLING DIMENSION: MILLIMETERS.
- 3. FL

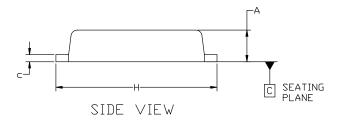
MILLIMETERS				
DIM	MIN	NDM	MAX	
Α	0.90	1.00	1.10	
b	1.25	1.45	1.65	
\subset	0.15	0.225	0.30	
D	2.40	2.60	2,80	
Ε	4.00	4.30	4.60	
Н	4.80	5.10	5.40	
L	0.70	0.90	1.10	

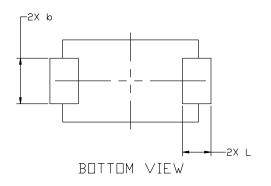


RECOMMENDED MOUNTING FOOTPRINT

*For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference manual, SDLDERRM/D.







GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code

A = Assembly Location

Y = Year

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= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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