# Onsemi

# IGBT NGTB75N65FL2WG

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on state voltage and minimal switching loss.

#### Features

- Extremely Efficient Trench with Field Stop Technology
- $T_{Jmax} = 175^{\circ}C$
- Soft Fast Reverse Recovery Diode
- Optimized for High Speed Switching
- 5 µs Short–Circuit Capability
- These are Pb-Free Devices

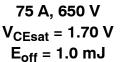
#### **Typical Applications**

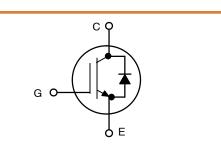
- Solar Inverters
- Uninterruptible Power Supplies (UPS)
- Welding

#### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-emitter Voltage	V <sub>CES</sub>	650	V
Collector Current @ $T_C = 25^{\circ}C$ @ $T_C = 100^{\circ}C$	Ι <sub>C</sub>	100 75	A
Diode Forward Current @ $T_C = 25^{\circ}C$ @ $T_C = 100^{\circ}C$	IF	100 75	A
Diode Pulsed Current $T_{\text{PULSE}}$ Limited by $T_{\text{J}}$ Max	I <sub>FM</sub>	200	A
Pulsed Collector Current, T <sub>pulse</sub> Limited by T <sub>Jmax</sub>	I <sub>CM</sub>	200	A
Short–circuit Withstand Time $V_{GE}$ = 15 V, $V_{CE}$ = 400 V, $T_J \le +150^{\circ}C$	t <sub>SC</sub>	5	μs
Gate-emitter Voltage	V <sub>GE</sub>	±20	V
Transient Gate-emitter Voltage ( $T_{PULSE}$ = 5 $\mu$ s, D < 0.10)		±30	V
Power Dissipation @ $T_C = 25^{\circ}C$ @ $T_C = 100^{\circ}C$	P <sub>D</sub>	595 265	W
Operating Junction Temperature Range	TJ	–55 to +175	°C
Storage Temperature Range	T <sub>stg</sub>	–55 to +175	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T <sub>SLD</sub>	260	°C

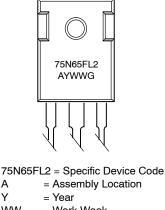
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.







#### MARKING DIAGRAM



WW = Work Week G

А Y

= Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping
NGTB75N65FL2WG	TO-247 (Pb-Free)	30 Units / Rail

#### THERMAL CHARACTERISTICS

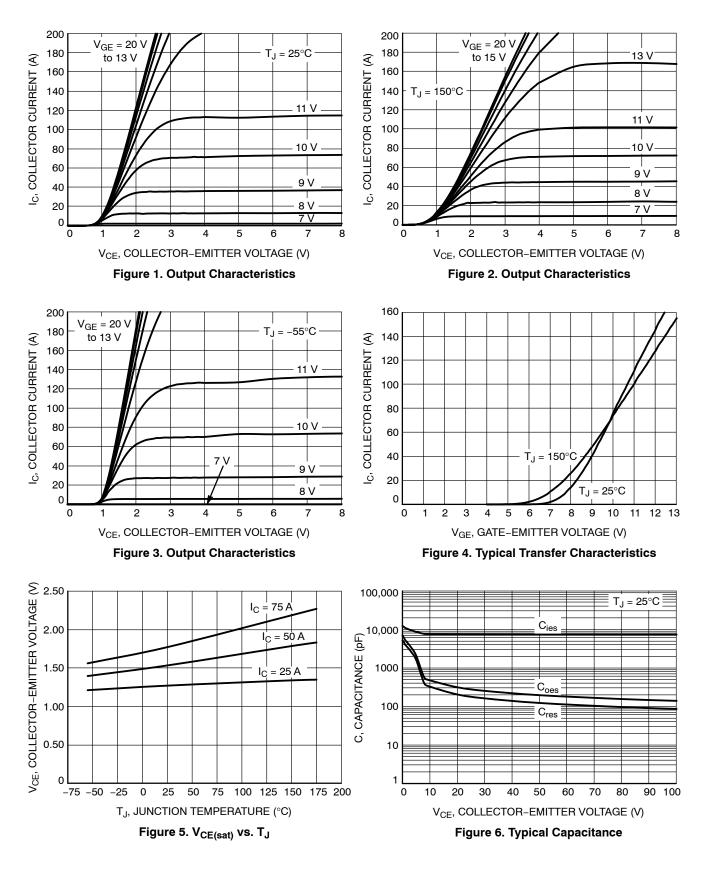
Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{ ext{ heta}JC}$	0.28	°C/W
Thermal resistance junction-to-case, for Diode	$R_{ ext{ heta}JC}$	0.62	°C/W
Thermal resistance junction-to-ambient	$R_{ heta JA}$	40	°C/W

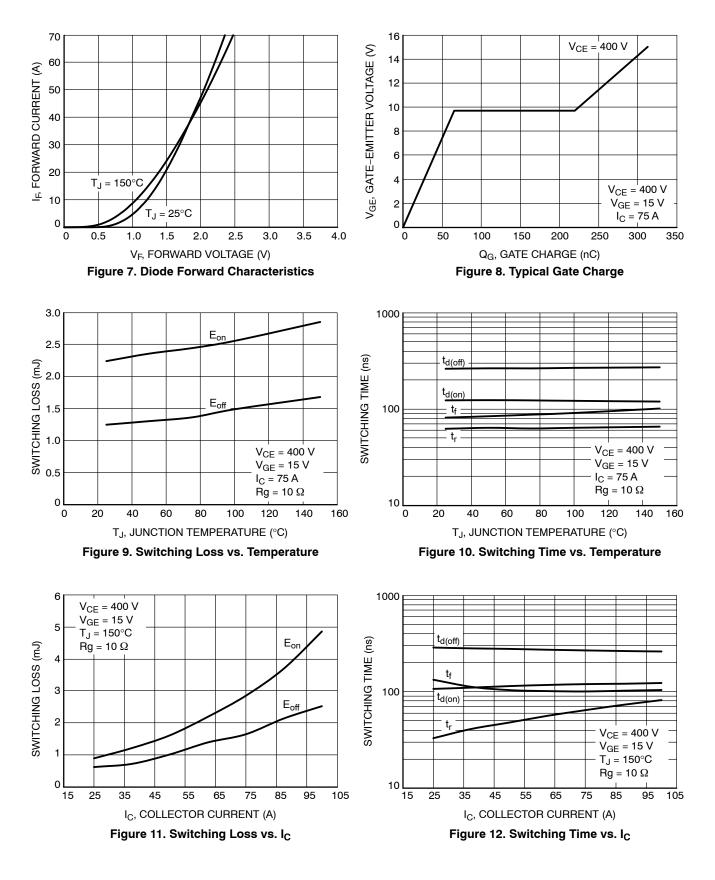
#### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

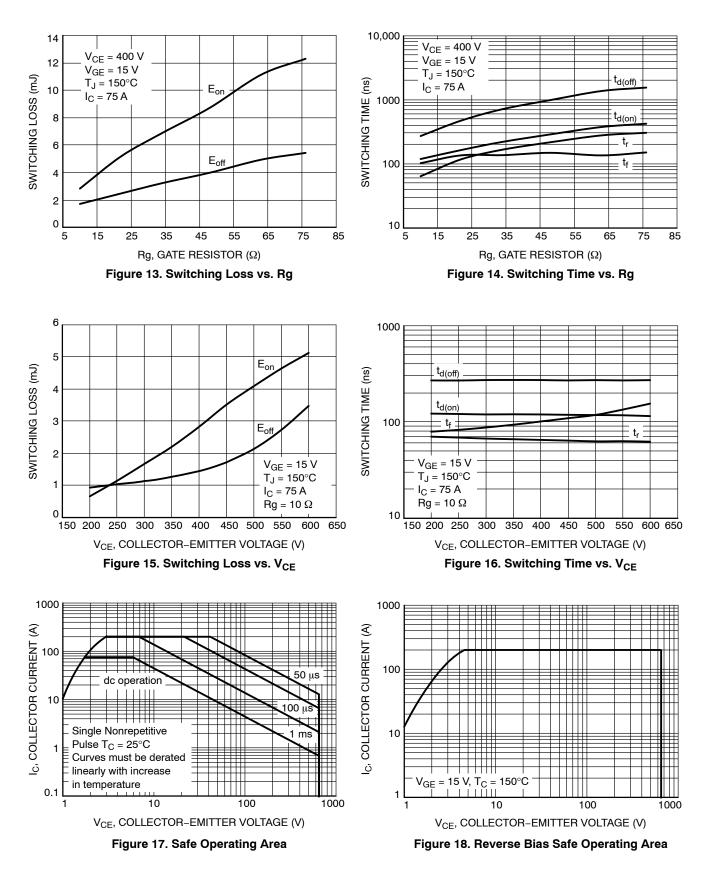
Parameter Test Conditions		Symbol	Min	Тур	Мах	Unit
STATIC CHARACTERISTIC				•		
Collector-emitter breakdown voltage, gate-emitter short-circuited	$V_{GE}$ = 0 V, I <sub>C</sub> = 500 µA	V <sub>(BR)CES</sub>	650	_	-	V
Collector-emitter saturation voltage	$V_{GE}$ = 15 V, I <sub>C</sub> = 75 A $V_{GE}$ = 15 V, I <sub>C</sub> = 75 A, T <sub>J</sub> = 175°C	V <sub>CEsat</sub>	1.50 -	1.75 2.30	2.00	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_C = 350 \ \mu A$	V <sub>GE(th)</sub>	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	$V_{GE} = 0 V, V_{CE} = 650 V$ $V_{GE} = 0 V, V_{CE} = 650 V, T_{J=} 175^{\circ}C$	I <sub>CES</sub>			0.1 4.0	mA
Gate leakage current, collector-emitter short-circuited	$V_{GE}$ = 20 V , $V_{CE}$ = 0 V	I <sub>GES</sub>	_	-	200	nA
DYNAMIC CHARACTERISTIC						
Input capacitance		Cies	-	7500	-	pF
Output capacitance	V <sub>CE</sub> = 20 V, V <sub>GE</sub> = 0 V, f = 1 MHz	C <sub>oes</sub>	-	300	-	
Reverse transfer capacitance	-	C <sub>res</sub>	_	190	-	
Gate charge total		Qg	_	310	-	nC
Gate to emitter charge	$V_{CE}$ = 480 V, I <sub>C</sub> = 50 A, V <sub>GE</sub> = 15 V	Q <sub>ge</sub>	-	60	-	
Gate to collector charge	7	Q <sub>gc</sub>	-	150	-	
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD					
Turn-on delay time		t <sub>d(on)</sub>	-	110	-	ns
Rise time	7	t <sub>r</sub>	-	48	-	
Turn–off delay time	T <sub>J</sub> = 25°C	t <sub>d(off)</sub>	-	270	-	
Fall time	$V_{CC} = 400$ V, I <sub>C</sub> = 75 A R <sub>g</sub> = 10 Ω	t <sub>f</sub>	-	70	-	
Turn–on switching loss	$V_{GE} = 0 V/15 V$	Eon	-	2.2	-	mJ
Turn–off switching loss	7	E <sub>off</sub>	-	1.1	-	
Total switching loss	7	E <sub>ts</sub>	-	3.3	-	]
Turn-on delay time		t <sub>d(on)</sub>	-	100	-	ns
Rise time	7	t <sub>r</sub>	-	50	-	
Turn-off delay time	T <sub>J</sub> = 150°C	t <sub>d(off)</sub>	-	280	-	
Fall time	$V_{CC} = 400$ V, I <sub>C</sub> = 75 A R <sub>g</sub> = 10 Ω	t <sub>f</sub>	-	100	-	
Furn–on switching loss	$V_{GE} = 0 V/15 V$	Eon	-	2.8	-	mJ
Turn–off switching loss	7	E <sub>off</sub>	-	1.6	-	
Total switching loss	1	E <sub>ts</sub>	-	4.4	-	
DIODE CHARACTERISTIC						
Forward voltage	V <sub>GE</sub> = 0 V, I <sub>F</sub> = 75 A V <sub>GE</sub> = 0 V, I <sub>F</sub> = 75 A. T <sub>1</sub> = 175°C	V <sub>F</sub>	1.50	2.20 2.40	2.90	V

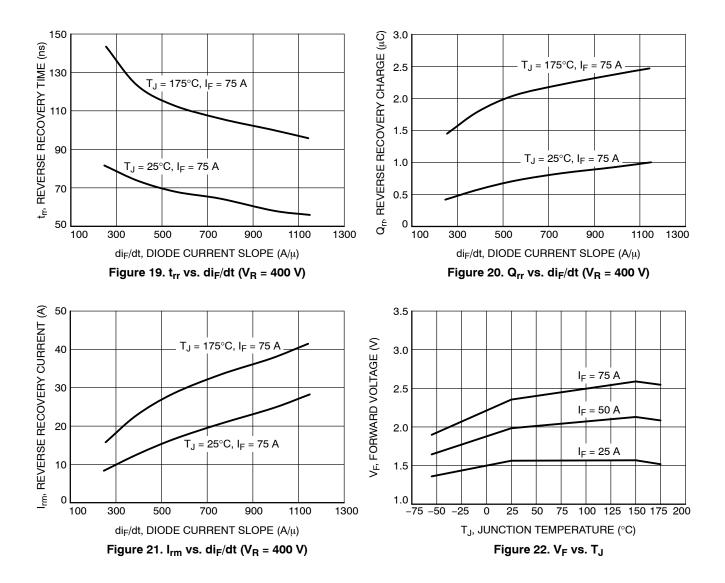
Forward voltage	V <sub>GE</sub> = 0 V, I <sub>F</sub> = 75 A V <sub>GE</sub> = 0 V, I <sub>F</sub> = 75 A, T <sub>J</sub> = 175°C	V <sub>F</sub>	1.50 -	2.20 2.40	2.90 -	V
Reverse recovery time	T <sub>.1</sub> = 25°C	t <sub>rr</sub>	-	80	-	ns
Reverse recovery charge	I <sub>F</sub> = 75 Å, V <sub>R</sub> = 400 V	Q <sub>rr</sub>	-	0.40	-	μC
Reverse recovery current	di <sub>F</sub> /dt = 200 A/µs	I <sub>rrm</sub>	-	8	-	A
Reverse recovery time	T.I = 175°C	t <sub>rr</sub>	-	143	-	ns
Reverse recovery charge	I <sub>F</sub> = 75 Å, V <sub>R</sub> = 400 V	Q <sub>rr</sub>	-	1.45	-	μC
Reverse recovery current	di <sub>F</sub> /dt = 200 A/µs	I <sub>rrm</sub>	-	16	-	Α

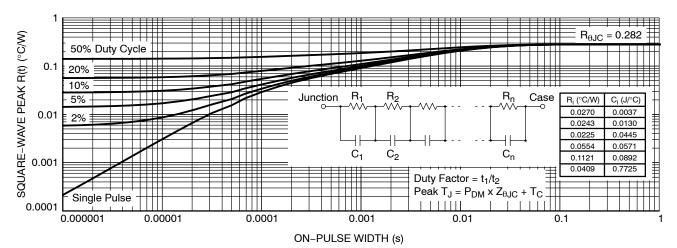
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.













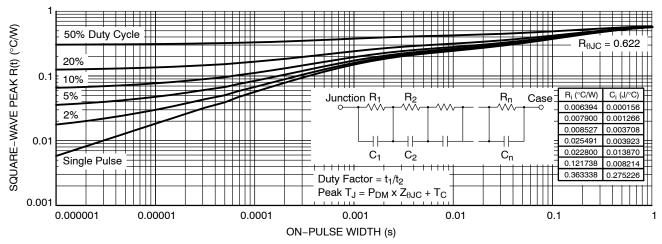
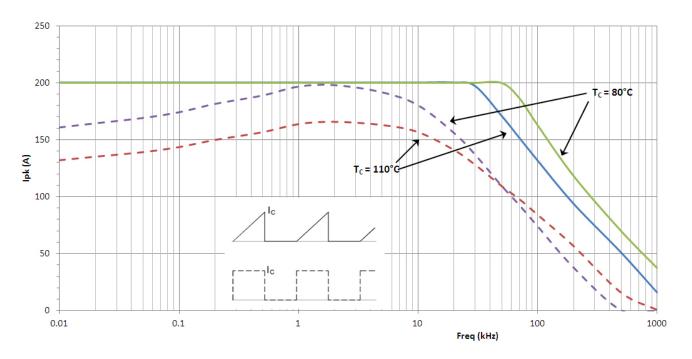


Figure 24. Diode Transient Thermal Impedance



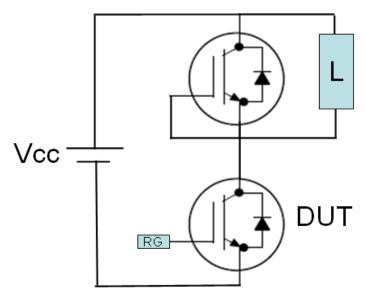
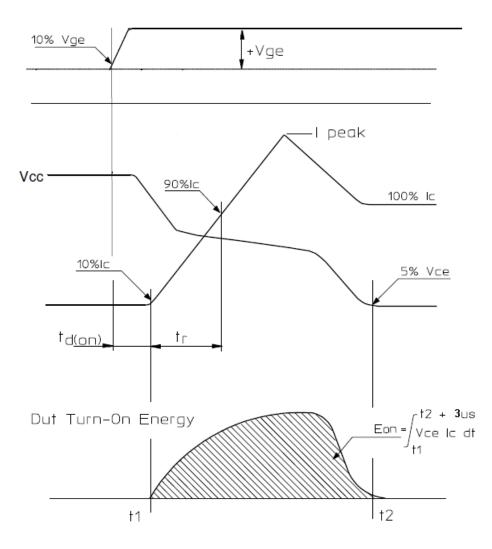
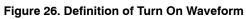


Figure 25. Test Circuit for Switching Characteristics





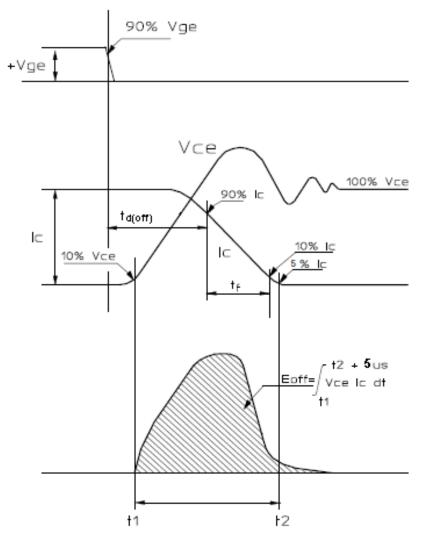
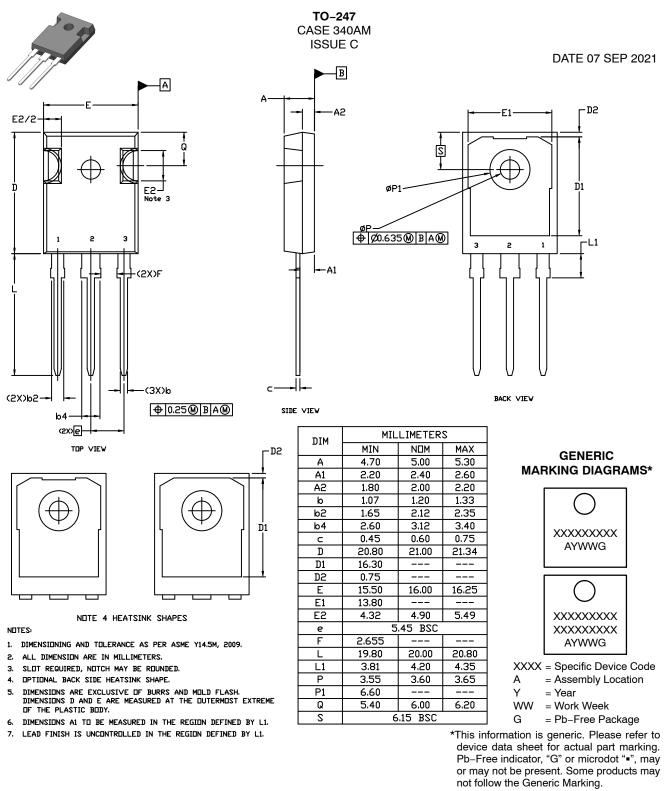


Figure 27. Definition of Turn Off Waveform

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