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May 2000 Revised December 2005

NC7WB3125 2-Bit Low Power Bus Switch

General Description

The NC7WB3125 is a 2-bit ultra high-speed CMOS FET bus switch with TTL-compatible active LOW control inputs. The low On Resistance of the switch allows inputs to be connected to outputs with minimal propagation delay and without generating additional ground bounce noise. The device is organized as a 2-bit switch with independent bus enable ($\overline{\text{OE}}$) controls. When $\overline{\text{OE}}$ is LOW, the switch is ON and Port A is connected to Port B. When $\overline{\text{OE}}$ is HIGH, the switch is OPEN and a high-impedance state exists between the two ports. Control inputs tolerate voltages up to 5.5V independent of VCC.

Features

- Space saving US8 surface mount package
- MicroPak™ Pb-Free leadless package
- Typical 3Ω switch resistance at 5.0V V_{CC}
- Minimal propagation delay through the switch
- Power down high impedance input/output
- Zero bounce in flow through mode
- TTL compatible active LOW control inputs
- Control inputs are overvoltage tolerant
- Bus switch replacement for Logic x125 part

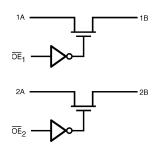
Ordering Code:

		Product		
Order	Package	Code	Package Description	Supplied As
Number	Number	Top Mark		
NC7WB3125K8X	MAB08A	WB25	8-Lead US8, JEDEC MO-187, Variation CA 3.1mm Wide	3k Units on Tape and Reel
NC7WB3125L8X (Preliminary)	MAC08A	T4	Pb-Free 8-Lead MicroPak, 1.6 mm Wide	5k Units on Tape and Reel

Pb-Free package per JEDEC J-STD-020B.

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Logic Diagram



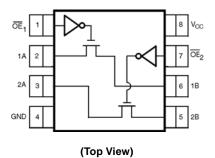
Pin Descriptions

Pin Name	Description
Α	Bus A
В	Bus B
OE	Bus Enable Input

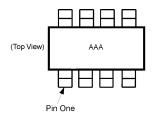
Function Table

Bus Enable Input OE	Function
L	B Connected to A
Н	Disconnected

Connection Diagrams



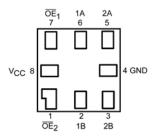
Pin One Orientation Diagram



AAA represents Product Code Top Mark - see ordering code

Note: Orientation of Top Mark determines Pin One location. Read the top product code mark left to right, Pin One is the lower left pin (see diagram).

Pad Assignments for MicroPak



(Top Through View)

4.0V to 5.5V

250°C/W

Absolute Maximum Ratings(Note 1)

 $\begin{array}{lll} \text{Supply Voltage (V}_{CC}) & -0.5 \text{V to } +7.0 \text{V} \\ \text{DC Switch Voltage (V}_{S}) & -0.5 \text{V to } +7.0 \text{V} \\ \text{DC Output Voltage (V}_{IN}) \text{ (Note 2)} & -0.5 \text{V to } +7.0 \text{V} \\ \end{array}$

DC Input Diode Current

 $\begin{array}{ll} (I_{\text{IK}}) \ V_{\text{IN}} < 0V & -50 \ \text{mA} \\ \text{DC Output } (I_{\text{OUT}}) \ \text{Current} & 128 \ \text{mA} \end{array}$

 $\operatorname{DC}\operatorname{V}_{\operatorname{CC}}$ or Ground Current

 $\begin{array}{ll} (I_{CC}/I_{GND}) & \pm 100 \text{ mA} \\ \text{Storage Temperature Range } (T_{STG}) & -65^{\circ}\text{C to } +150^{\circ}\text{C} \\ \text{Junction Temperature under Bias } (T_{J}) & +150^{\circ}\text{C} \end{array}$

Junction Lead Temperature (T_I)

(Soldering, 10 Seconds) $+260^{\circ}$ C Power Dissipation (P_D) @ +85°C 250 mW

Recommended Operating Conditions (Note 3)

Supply Operating (V_{CC})

Thermal Resistance (θ_{JA})

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Note 2: The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

Note 3: Unused logic inputs must be held HIGH or LOW. They may not float.

DC Electrical Characteristics

Symbol	Parameter	v _{cc}	Т	_A = -40°C to +85°	C	Units	Conditions
Oymboi	i arameter	(V)	Min	Тур	Max	Office	Conditions
V _{IK}	Clamp Diode Voltage	4.5			-1.2	V	I _{IN} = -18 mA
V _{IH}	HIGH Level Input Voltage	4.0 to 5.5	2.0			V	
V _{IL}	LOW Level Input Voltage	4.0 to 5.5			8.0	V	
V _{OH}	HIGH Level Output Voltage	4.0 to 5.5		See Figure 3		V	$V_{IN} = V_{CC}$
I _{IN}	Input Leakage Current	5.5			±1.0	μА	$0 \le V_{IN} \le 5.5V$
I _{OFF}	Switch OFF Leakage Current	5.5			±1.0	μА	$0 \le A, B \le V_{CC}$
R _{ON}	Switch On Resistance	4.5		3.0	7.0		V _{IN} = 0V, I _{IN} = 64 mA
	(Note 4)	4.5		3.0	7.0	Ω	V _{IN} = 0V, I _{IN} = 30 mA
		4.5		6.0	15.0	52	V _{IN} = 2.4V, I _{IN} = 15 mA
		4.0		10.0	20.0		V _{IN} = 2.4V, I _{IN} = 15 mA
I _{CC}	Quiescent Supply Current	5.5			3.0	μА	V _{IN} = V _{CC} or GND
							I _{OUT} = 0
Δ I _{CC}	Increase in I _{CC} per Input (Note 5)	5.5		1.0	2.5	mA	V _{IN} = 3.4V, One OE Input only,
							Other OE = V _{CC}

Note 4: Measured by the voltage drop between A and B pins at the indicated current through the switch. On Resistance is determined by the lower of the voltages on the two (A or B) pins.

Note 5: Per TTL driven input ($V_{IN} = 3.4V$, control input only). A and B pins do not contribute to I_{CC} .

AC Electrical Characteristics

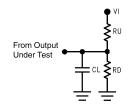
				= -40°C to +8				
Symbol	Parameter	V _{CC}	$C_L = 50 \text{ pF, RU} = RD = 500 \Omega$		Units	Conditions	Figure	
		(V)	Min	Тур	Max			Number
t _{PHL} ,	Propagation Delay Bus-to-Bus	4.0 to 5.5			0.25	ns	V _I = OPEN	Figures
t _{PLH}	(Note 6)							1, 2
t _{PZL} ,	Output Enable Time	4.5 to 5.5	0.8	2.5	4.2	ns	V _I = 7V for t _{PZL}	Figures
t_{PZH}		4.0	0.8	3.0	4.6	113	$V_I = 0V$ for t_{PZH}	1, 2
t_{PLZ} ,	Output Disable Time	4.5 to 5.5	0.8	3.1	4.8	ns	V _I = 7V for t _{PLZ}	Figures
t_{PHZ}		4.0	0.8	2.9	4.4	113	$V_I = 0V$ for t_{PHZ}	1Figure 2

Note 6: This parameter is guaranteed by design. The bus switch contributes no propagation delay other than the RC delay of the typical On Resistance of the switch and the 50 pF load capacitance, when driven by an ideal voltage source (zero output impedance). The specified limit is calculated on this basis.

Capacitance

Symbol	Parameter	Тур	Max	Units	Conditions
C _{IN}	Control Pin Input Capacitance	2.5		pF	V _{CC} = 0V
C _{I/O} (OFF)	Port Off Capacitance	6.0		pF	$V_{CC} = 5.0V = \overline{OE}$
C _{I/O} (ON)	Switch ON Capacitance	12.0		pF	$V_{CC} = 5.0V, \overline{OE} = 0V$

AC Loading and Waveforms

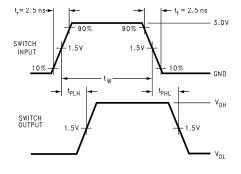


Input driven by 50Ω source terminated in 50Ω

 \mathbf{C}_{L} includes load and stray capacitance

Input PRR = 1.0 MHz; $t_W = 500 \text{ ns}$

FIGURE 1. AC Test Circuit



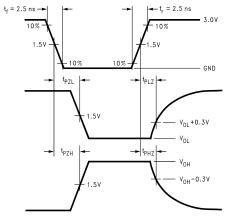
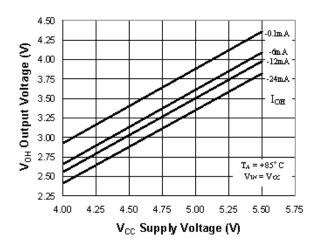
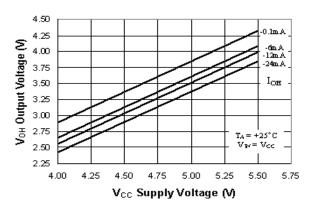


FIGURE 2. AC Waveforms

DC Characteristics





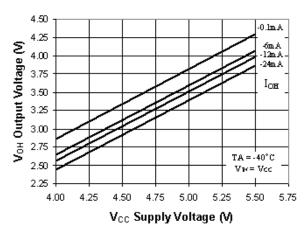


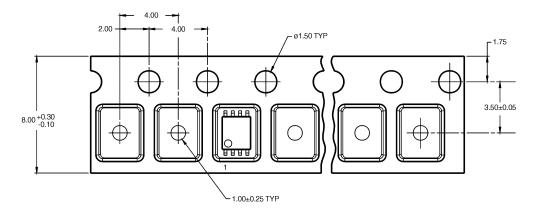
FIGURE 3. Typical High Level Output Voltage vs. Supply Voltage

Tape and Reel Specification

TAPE FORMAT for US8

Package Designator	Tape Section	Number Cavities	Cavity Status	Cover Tape Status
	Leader (Start End)	125 (typ)	Empty	Sealed
K8X	Carrier	250	Filled	Sealed
	Trailer (Hub End)	75 (typ)	Empty	Sealed

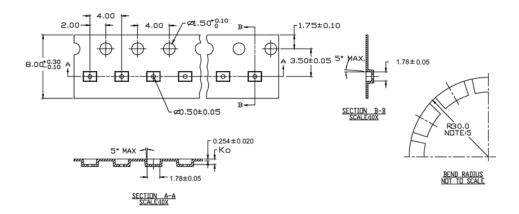
TAPE DIMENSIONS inches (millimeters)



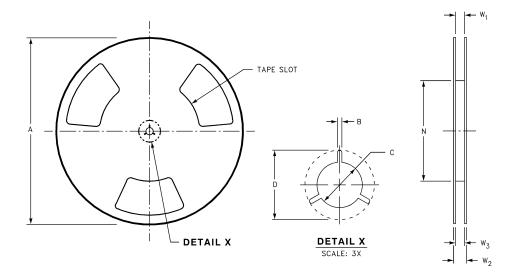
TAPE FORMAT for MicroPak

Package	Tape	Number	Cavity	Cover Tape
Designator	Section	Cavities	Status	Status
	Leader (Start End)	125 (typ)	Empty	Sealed
L8X	Carrier	250	Filled	Sealed
	Trailer (Hub End)	75 (typ)	Empty	Sealed

TAPE DIMENSIONS inches (millimeters)

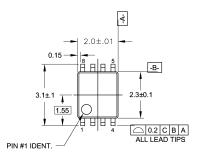


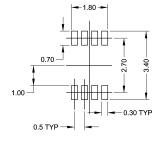
REEL DIMENSIONS inches (millimeters)



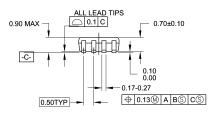
Tape Size	Α	В	С	D	N	W1	W2	W3
9 mm	7.0	0.059	0.512	0.795	2.165	0.331 + 0.059/-0.000	0.567	W1 + 0.078/-0.039
8 mm	(177.8)	(1.50)	(13.00)	(20.20)	(55.00)	(8.40 + 1.50/-0.00)	(14.40)	(W1 + 2.00/-1.00)

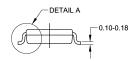
Physical Dimensions inches (millimeters) unless otherwise noted





LAND PATTERN RECOMMENDATION





GAGE PLANE 0.12 SEATING PLANE 0.20 - 0.35

NOTES:

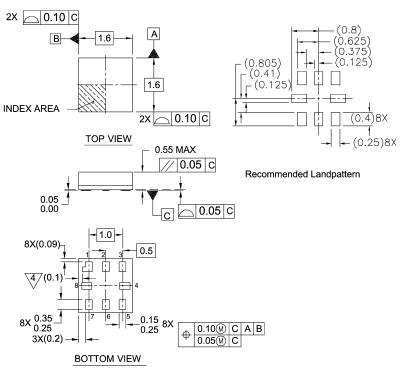
- A. CONFORMS TO JEDEC REGISTRATION MO-187 B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- D. DIMENSIONS AND TOLERANCES PER ANSI Y14.5M, 1982.

DETAIL A

MAB08AREVC

8-Lead US8, JEDEC MO-187, Variation CA 3.1mm Wide Package Number MAB08A

Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



Notes:

- 1. PACKAGE CONFORMS TO JEDEC MO-255 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y.14M-1994
- 4/PIN 1 FLAG, END OF PACKAGE OFFSET.

MAC08AREVC

Pb-Free 8-Lead MicroPak, 1.6 mm Wide Package Number MAC08A

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