TinyLogic ULP-A Buffer with Schmitt-Trigger Input

NC7SV17

The NC7SV17 is a single buffer with Schmitt–Trigger Input in tiny footprint packages. The device is designed to operate for $V_{CC} = 0.9$ V to 3.6 V.

Features

- Designed for 0.9 V to 3.6 V V_{CC} Operation
- 2.0 ns t_{PD} at 3.3 V (Typ)
- Inputs/Outputs Over-Voltage Tolerant up to 3.6 V
- I_{OFF} Supports Partial Power Down Protection
- Source/Sink 24 mA at 3.3 V
- Available in SC-88A and MicroPak[™] Packages
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

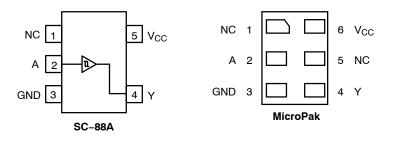


Figure 1. Pinout Diagrams (Top Views)

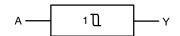
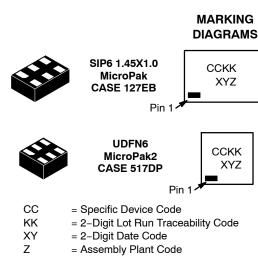


Figure 2. Logic Symbol

PIN ASSIGNMENT

Pin	SC88A	MicroPak
1	N.C.	N.C.
2	A	А
3	GND	GND
4	Y	Y
5	V _{CC}	N.C.
6	_	V _{CC}

N.C. = No Connect





- XXX = Specific Devic Code
- M = Date Code
- = Pb–Free Package

ORDERING INFORMATION

See detailed ordering, marking and shipping information on page 7 of this data sheet.

FUNCTION	TABLE

Input	Output				
A	Y				
L	L				
Н	Н				

MAXIMUM RATINGS

Symbol	Characteristic	s	Value	Unit
V _{CC}	DC Supply Voltage		-0.5 to +4.3	V
V _{IN}	DC Input Voltage		-0.5 to +4.3	V
V _{OUT}	DC Output Voltage	Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode (V _{CC} = 0 V)	-0.5 to V _{CC} + 0.5 -0.5 to +4.3 -0.5 to +4.3	V
I _{IK}	DC Input Diode Current	V _{IN} < GND	-50	mA
I _{OK}	DC Output Diode Current	V _{OUT} < GND	-50	mA
IOUT	DC Output Source/Sink Current	±50	mA	
I _{CC} or I _{GND}	DC Supply Current per Supply Pin or Ground F	Pin	±50	mA
T _{STG}	Storage Temperature Range		-65 to +150	°C
ΤL	Lead Temperature, 1 mm from Case for 10 Sec	conds	260	°C
TJ	Junction Temperature Under Bias		+150	°C
θ_{JA}	Thermal Resistance (Note 2)	SC-88A MicroPak	377 154	°C/W
PD	Power Dissipation in Still Air	SC-88A MicroPak	332 812	mW
MSL	Moisture Sensitivity		Level 1	-
F _R	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	-
V_{ESD}	ESD Withstand Voltage (Note 3)	Human Body Model Charged Device Model	1500 1000	V
I _{Latchup}	Latchup Performance (Note 4)		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

should not be assumed, damage may occur and reliability may be affected.
Applicable to devices with outputs that may be tri-stated.
Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow per JESD51-7.
HBM tested to EIA / JESD22-A114-A. CDM tested to JESD22-C101-A. JEDEC recommends that ESD qualification to EIA/JESD22-A115A (Machine Model) be discontinued.
Tested to EIA/JESD78 Class II.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V _{CC}	Positive DC Supply Voltage	0.9	3.6	V
V _{IN}	DC Input Voltage	0	3.6	V
V _{OUT}	DC Output Voltage Active–Mode (High or Low Tri–State Mode (Power–Down Mode (V _{CC}	Note 1) 0	V _{CC} 3.6 3.6	
T _A	Operating Temperature Range	-40	+85	°C
t _r , t _f	Input Transition Rise and Fall Time	0	No Limit	ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

DC ELECTRICAL CHARACTERISTICS

					T _A = 25°C		$T_A = -40^{\circ}C$	C to +85°C	
Symbol	Parameter	Condition	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit
VP	Positive		0.9	-	0.62	-	-	-	V
	Threshold Voltage		1.1	-	-	1.0	-	1.0	
			1.4	-	_	1.25	-	1.25	
			1.65	-	-	1.5	-	1.5	
			2.3	-	-	1.8	-	1.8	
			2.7	-	-	2.2	-	2.2	
V _N	Negative		0.9	-	0.34	-	-	-	V
	Threshold Voltage		1.1	0.15	-	-	0.15	-	
			0.2	-	-	0.2	-		
			0.25	-	-	0.25	-		
			2.3	0.4	-	-	0.4	-	
			2.7	0.6	-	-	0.6	-	
V _H	Hysteresis		0.9	-	0.29	-	-	-	V
	Voltage		1.1	0.08	-	0.6	0.08	0.6	
		1.4 0.09 1.65 0.15	0.09	-	0.8	0.09	0.8		
			0.15	-	1.0	0.15	1.0		
			2.3	0.25	-	1.1	0.25	1.1	
			2.7	0.6	-	1.2	0.6	1.2	

DC ELECTRICAL CHARACTERISTICS (continued)

					T _A = 25°C		T _A = -40°0	C to +85°C	
Symbol	Parameter	arameter Condition	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit
V _{OH}	High-Level Output	$V_{IN} = V_P \text{ or } V_N$							V
	Voltage	I _{OH} = -100 μA	0.9	-	$V_{CC} - 0.1$	-	-	-	
			1.1 to 1.3	$V_{CC} - 0.1$	-	-	$V_{CC} - 0.1$	-	
			1.4 to 1.6	$V_{CC} - 0.1$	-	-	$V_{CC} - 0.1$	_	
			1.65 to 1.95	$V_{CC}-0.2$	-	-	$V_{CC}-0.2$	_	
			2.3 to <2.7	$V_{CC} - 0.2$	-	-	$V_{CC} - 0.2$	_	
			2.7 to 3.6	$V_{CC} - 0.2$	-	-	$V_{CC}-0.2$	-	
		I _{OH} = -2 mA	1.1 o 1.3	0.75 x V _{CC}	-	-	$0.75 ext{ x V}_{CC}$	-	
		I _{OH} = -4 mA	1.4 to 1.6	0.75 x V _{CC}	-	-	$0.75 \times V_{CC}$	-	
		I _{OH} = -6 mA	1.65 to 1.95	1.25	-	-	1.25	-	
			2.3 to <2.7	2.0	-	-	2.0	-	
		I _{OH} = -12 mA	2.3 to <2.7	1.8	-	-	1.8	_	
			2.7 to 3.6	2.2	-	-	2.2	-	
		I _{OH} = -18 mA	2.3 to <2.7	1.7	-	-	1.7	_	
			2.7 to 3.6	2.4	-	-	2.4	-	
		I _{OH} = -24 mA	2.7 to 3.6	2.2	-	-	2.2	-	
V _{OL}	Low-Level Output Voltage	$V_{IN} = V_P \text{ or } V_N$							V
		I _{OL} = 100 μA	0.9	-	0.1	-	-	-	
			1.1 to 1.3	-	-	0.1	-	0.1	
			1.4 to 1.6	-	-	0.1	-	0.1	
			1.65 to 1.95	-	-	0.2	-	0.2	
			2.3 to < 2.7	-	-	0.2	-	0.2	
			2.7 to 3.6	-	-	0.2	-	0.2	
		I _{OL} = 2 mA	1.1 o 1.3	-	-	$0.25 \times V_{CC}$	-	$0.25 \times V_{CC}$	
		I _{OL} = 4 mA	1.4 to 1.6	-	-	$0.25 \times V_{CC}$	-	$0.25 \times V_{CC}$	
		I _{OL} = 6 mA	1.65 to 1.95	-	-	0.3	-	0.3	
		I _{OL} = 12 mA	2.3 to <2.7	-	-	0.4	-	0.4	
			2.7 to 3.6	-	-	0.4	-	0.4	
		I _{OL} = 18 mA	2.3 to <2.7	-	-	0.6	-	0.6	
			2.7 to 3.6	-	-	0.4	-	0.4	
		I _{OL} = 24 mA	2.7 to 3.6	-	-	0.55	-	0.55	
I _{IN}	Input Leakage Current	$V_{IN} = 0 V \text{ to } 3.6 V$	0.9 to 3.6	-	-	±0.1	-	±0.5	μA
I _{OFF}	Power Off Leakage Current	$V_{IN} = 0 V \text{ to } 3.6 V$	0	-	-	0.5	-	0.5	μA
		V _{OUT} = 0 V to 3.6 V							
I _{CC}	Quiescent Supply Current	$V_{IN} = V_{CC}$ or GND	0.9 to 3.6	-	-	0.9	-	0.9	μA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

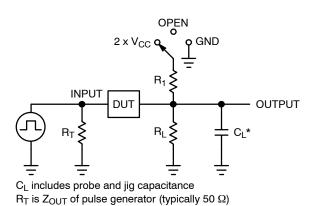
AC ELECTRICAL CHARACTERISTICS

				٦	[_A = 25°0	2	T _A = -40°C	to +85°C	
Symbol	Parameter	Condition	V _{CC} (V)	Min	Тур	Max	Min	Max	Unit
t _{PLH} ,	Propagation Delay,	R_L = 1 MΩ, C_L = 15 pF	0.9	-	17.7	-	-	-	ns
t _{PHL}	HL A to Y (Figures 3 and 4)	R_L = 2 k Ω , C_L = 15 pF	1.1 to 1.3	-	7.5	13.5	-	19.0	
			1.4 to 1.6	-	4.2	6.1	-	7.0	
		R_L = 500 Ω , C_L = 30 pF	1.65 to 1.95	-	3.1	5.2	-	6.2	
			2.3 to 2.7	-	2.4	3.7	-	4.4	
			2.7 to 3.6	-	2.0	3.3	-	3.8	

CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Test Condition	Typical (T _A = 25°C)	Unit
C _{IN}	Input Capacitance	V _{CC} = 0 V	2.0	pF
C _{PD}	Power Dissipation Capacitance (Note 5)	f = 10 MHz, V_{CC} = 0.9 to 3.6 V, V _{IN} = 0 V or V_{CC}	10.0	pF

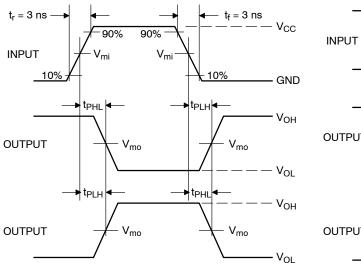
5. C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation $I_{CC(OPR)} = C_{PD} \bullet V_{CC} \bullet f_{in} + I_{CC}$. C_{PD} is used to determine the no-load dynamic power consumption: $P_D = C_{PD} \bullet V_{CC}^2 \bullet f_{in} + I_{CC} \bullet V_{CC}$.

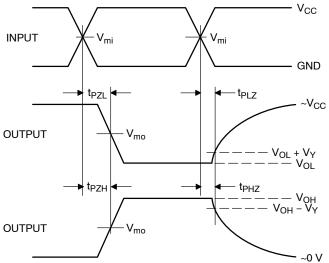


f = 1 MHz

Test	Switch Position
t _{PLH} / t _{PHL}	Open
t _{PLZ} / t _{PZL}	2 x V _{CC}
t _{PHZ} / t _{PZH}	GND







V _{CC} , V	V _{mi} , V	V _{mo} , V	V _Y , V
0.9	V _{CC} / 2	V _{CC} / 2	0.1
1.1 to 1.3	V _{CC} / 2	V _{CC} / 2	0.1
1.4 to 1.6	V _{CC} / 2	V _{CC} / 2	0.1
1.65 to 1.95	V _{CC} / 2	V _{CC} / 2	0.15
2.3 to 2.7	V _{CC} / 2	V _{CC} / 2	0.15
3.0 to 3.6	1.5	1.5	0.3

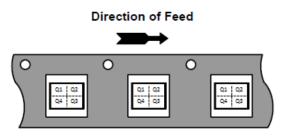
Figure 4. Switching Waveforms

ORDERING INFORMATION

Device	Package	Marking	Pin 1 Orientation (See below)	Shipping [†]
NC7SV17P5X	SC-88A	V17	Q4	3000 / Tape & Reel
NC7SV17L6X	MicroPak	G5	Q4	5000 / Tape & Reel
NC7SV17FHX	MicroPak2	G5	Q4	5000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PIN 1 ORIENTATION IN TAPE AND REEL

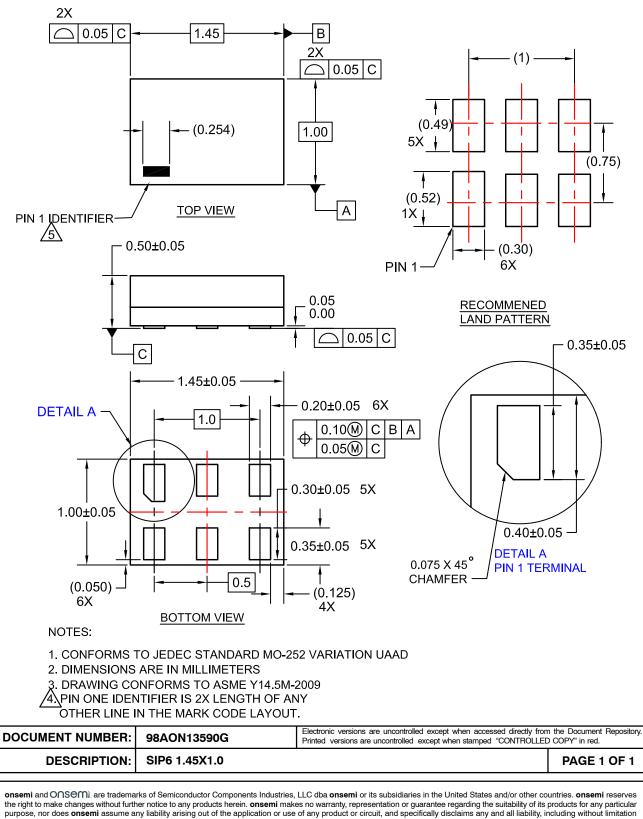


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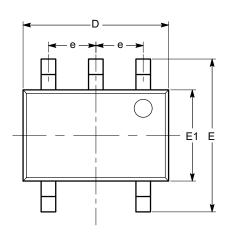


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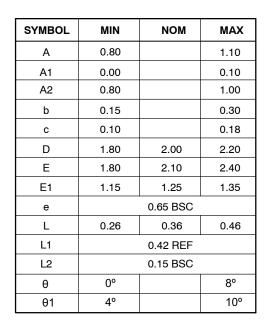


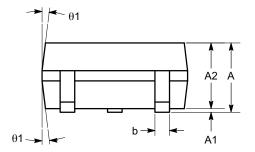
SC-88A (SC-70 5 Lead), 1.25x2 CASE 419AC-01 ISSUE A

DATE 29 JUN 2010

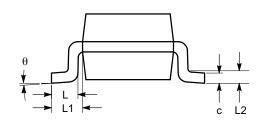








SIDE VIEW



END VIEW

Notes:

- (1) All dimensions are in millimeters. Angles in degrees.
- (2) Complies with JEDEC MO-203.

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UDFN6 1.0X1.0, 0.35P CASE 517DP ISSUE O DATE 31 AUG 2016 0.89 -ン|0.05|C в 1.00±0.050 А 0.35 2X 5X 0.40 PIN 1 MIN 250uM 0.66 1.00±0.050 1X 0.45 □ 0.05 C TOP VIEW - 6X 0.19 2X **RECOMMENDED LAND PATTERN** FOR SPACE CONSTRAINED PCB 0.05 C 0.90 -0.35 0.50±0.05 С 5X 0.52 SIDE VIEW 6X 0.14±0.05 (0.08) 4X — 0.73 2 DETAIL A 1 3 1X 0.57 – 0.20 6X ALTERNATIVE LAND PATTERN FOR UNIVERSAL APPLICATION - (0.05) 6X 5X 0.30±0.05 0.60 4 0.10(M) C B A 0.35 (0.08) .05 C 4X 0.35±0.050 BOTTOM VIEW NOTES: A. COMPLIES TO JEDEC MO-252 STANDARD **B. DIMENSIONS ARE IN MILLIMETERS.** C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 2009 0.075X45° DETAIL A CHAMFER PIN 1 LEAD SCALE: 2X

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