MPS918, MPS3563

MPS918 is a Preferred Device

Amplifier Transistors

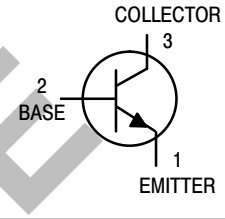
NPN Silicon

ON Semiconductor®

http://onsemi.com

Features

- Pb−Free Packages are Available*



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector − Emitter Voltage MPS918 MPS3563	V_{CEO}	 15 12	Vdc
Collector − Base Voltage MPS918 MPS3563	V_{CBO}	 30 30	Vdc
Emitter − Base Voltage MPS918 MPS3563	V_{EBO}	 3.0 2.0	Vdc
Collector Current − Continuous	I_C	50	mAdc
Total Device Dissipation @ T_A = 25°C Derate above 25°C	P_D	350 2.8	mW mW/°C
Total Device Dissipation @ T_C = 25°C Derate above 25°C	P_D	0.85 6.8	W mW/°C
Operating and Storage Junction Temperature Range	T_J, T_{stg}	−55 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction−to−Ambient (Note 1)	$R_{\theta JA}$	357	°C/W
Thermal Resistance, Junction−to−Case	$R_{\theta JC}$	147	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. $R_{\theta JA}$ is measured with the device soldered into a typical printed circuit board.

*For additional information on our Pb−Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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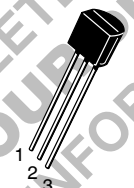
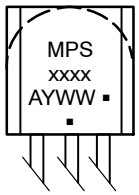


TO−92
CASE 29−11
STYLE 1

MARKING DIAGRAM



MPSxxxx = Device Code
xxxx = 918 or 3563
A = Assembly Location
Y = Year
WW = Work Week
▪ = Pb−Free Package
(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
MPS918	TO−92	5000 Units/Box
MPS918G	TO−92 (Pb−Free)	5000 Units/Box
MPS3563	TO−92	5000 Units/Box
MPS3563G	TO−92 (Pb−Free)	5000 Units/Box
MPS3563RLRA	TO−92	2000/Tape & Reel
MPS3563RLRAG	TO−92 (Pb−Free)	2000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

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ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (Note 2)		$V_{(BR)CEO}$			Vdc
(I_C = 3.0 mAdc, I_B = 0)	MPS918		15	–	
	MPS3563		12	–	
Collector – Base Breakdown Voltage		$V_{(BR)CBO}$			Vdc
(I_C = 1.0 μAdc, I_E = 0)	MPS918		30	–	
(I_C = 100 μAdc, I_E = 0)	MPS3563		30	–	
Emitter – Base Breakdown Voltage		$V_{(BR)EBO}$			Vdc
(I_E = 10 μAdc, I_C = 0)	MPS918		3.0	–	
	MPS3563		2.0	–	
Collector Cutoff Current		I_{CBO}			nAdc
(V_{CB} = 15 Vdc, I_E = 0)	MPS918		–	10	
	MPS3563		–	50	
ON CHARACTERISTICS					
DC Current Gain (Note 2)		h_{FE}			–
(I_C = 3.0 mAdc, V_{CE} = 1.0 Vdc)	MPS918		20	–	
(I_C = 8.0 mAdc, V_{CE} = 10 Vdc)	MPS3563		20	200	
Collector – Emitter Saturation Voltage		$V_{CE(sat)}$	–	0.4	Vdc
(I_C = 10 mAdc, I_B = 1.0 mAdc)	MPS918				
Base – Emitter Saturation Voltage		$V_{BE(sat)}$	–	1.0	Vdc
(I_C = 10 mAdc, I_B = 1.0 mAdc)	MPS918				
SMALL– SIGNAL CHARACTERISTICS					
Current – Gain – Bandwidth Product (Note 2)		f_T			MHz
(I_C = 4.0 mAdc, V_{CE} = 10 Vdc, f = 100 MHz)	MPS918		600	–	
(I_C = 8.0 mAdc, V_{CE} = 10 Vdc, f = 100 MHz)	MPS3563		600	1500	
Output Capacitance		C_{obo}			pF
(V_{CB} = 0 Vdc, I_E = 0, f = 1.0 MHz)	MPS918		–	3.0	
(V_{CB} = 10 Vdc, I_E = 0, f = 1.0 MHz)	MPS918		–	1.7	
(V_{CB} = 10 Vdc, I_E = 0, f = 1.0 MHz)	MPS3563		–	1.7	
Input Capacitance		C_{ibo}	–	2.0	pF
(V_{EB} = 0.5 Vdc, I_C = 0, f = 1.0 MHz)	MPS918				
Small–Signal Current Gain		h_{fe}	20	250	–
(I_C = 8.0 mAdc, V_{CE} = 10 Vdc, f = 1.0 kHz)	MPS3563				
Noise Figure		NF	–	6.0	dB
(I_C = 1.0 mAdc, V_{CE} = 6.0 Vdc, R_S = 400 kΩ, f = 60 MHz)	MPS918				
FUNCTIONAL TEST					
Common–Emitter Amplifier Power Gain		G_{pe}			dB
(I_C = 6.0 mAdc, V_{CB} = 12 Vdc, f = 200 MHz)	MPS918		15	–	
(I_C = 8.0 mAdc, V_{CE} = 10 Vdc, f = 200 MHz)	MPS3563		14	–	
(G_{fd} + G_{re} < –20 dB)					
Power Output		P_{out}	30	–	mW
(I_C = 8.0 mAdc, V_{CB} = 15 Vdc, f = 500 MHz)	MPS918				
Oscillator Collector Efficiency		η	25	–	%
(I_C = 8.0 mAdc, V_{CB} = 15 Vdc, P_{out} = 30 mW, f = 500 MHz)	MPS918				

2. Pulse Test: Pulse Width ≤ 300 μs; Duty Cycle ≤ 1.0%.

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REVISION HISTORY

Revision	Description of Changes	Date
4	Document Discontinued.	3/24/2026

This document has undergone updates prior to the inclusion of this revision history table. The changes tracked here only reflect updates made on the noted approval dates.

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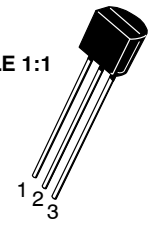
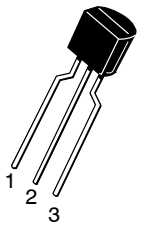
TO−92 (TO−226)
CASE 29−11
ISSUE AM

DATE 09 MAR 2007

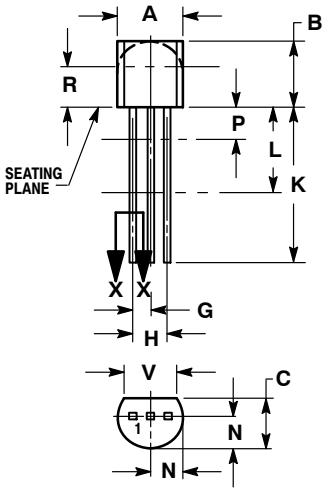
SCALE 1:1



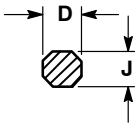
STRAIGHT LEAD
BULK PACK



BENT LEAD
TAPE & REEL
AMMO PACK



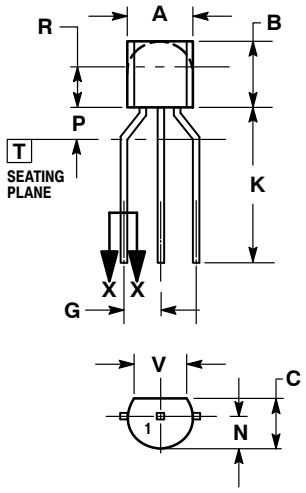
STRAIGHT LEAD
BULK PACK



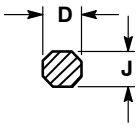
SECTION X−X

NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
A	0.175	0.205	4.45	5.20
B	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500	---	12.70	---
L	0.250	---	6.35	---
N	0.080	0.105	2.04	2.66
P	---	0.100	---	2.54
R	0.115	---	2.93	---
V	0.135	---	3.43	---



BENT LEAD
TAPE & REEL
AMMO PACK



SECTION X−X

NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	MILLIMETERS	
DIM	MIN	MAX
A	4.45	5.20
B	4.32	5.33
C	3.18	4.19
D	0.40	0.54
G	2.40	2.80
J	0.39	0.50
K	12.70	---
N	2.04	2.66
P	1.50	4.00
R	2.93	---
V	3.43	---

STYLES ON PAGE 2

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DESCRIPTION:	**TO−92 (TO−226)**		**PAGE 1 OF 2**

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TO−92 (TO−226)
CASE 29−11
ISSUE AM

DATE 09 MAR 2007

STYLE 1:
PIN 1. EMITTER
2. BASE
3. COLLECTOR

STYLE 2:
PIN 1. BASE
2. EMITTER
3. COLLECTOR

STYLE 3:
PIN 1. ANODE
2. ANODE
3. CATHODE

STYLE 4:
PIN 1. CATHODE
2. CATHODE
3. ANODE

STYLE 5:
PIN 1. DRAIN
2. SOURCE
3. GATE

STYLE 6:
PIN 1. GATE
2. SOURCE & SUBSTRATE
3. DRAIN

STYLE 7:
PIN 1. SOURCE
2. DRAIN
3. GATE

STYLE 8:
PIN 1. DRAIN
2. GATE
3. SOURCE & SUBSTRATE

STYLE 9:
PIN 1. BASE 1
2. EMITTER
3. BASE 2

STYLE 10:
PIN 1. CATHODE
2. GATE
3. ANODE

STYLE 11:
PIN 1. ANODE
2. CATHODE & ANODE
3. CATHODE

STYLE 12:
PIN 1. MAIN TERMINAL 1
2. GATE
3. MAIN TERMINAL 2

STYLE 13:
PIN 1. ANODE 1
2. GATE
3. CATHODE 2

STYLE 14:
PIN 1. EMITTER
2. COLLECTOR
3. BASE

STYLE 15:
PIN 1. ANODE 1
2. CATHODE
3. ANODE 2

STYLE 16:
PIN 1. ANODE
2. GATE
3. CATHODE

STYLE 17:
PIN 1. COLLECTOR
2. BASE
3. EMITTER

STYLE 18:
PIN 1. ANODE
2. CATHODE
3. NOT CONNECTED

STYLE 19:
PIN 1. GATE
2. ANODE
3. CATHODE

STYLE 20:
PIN 1. NOT CONNECTED
2. CATHODE
3. ANODE

STYLE 21:
PIN 1. COLLECTOR
2. EMITTER
3. BASE

STYLE 22:
PIN 1. SOURCE
2. GATE
3. DRAIN

STYLE 23:
PIN 1. GATE
2. SOURCE
3. DRAIN

STYLE 24:
PIN 1. EMITTER
2. COLLECTOR/ANODE
3. CATHODE

STYLE 25:
PIN 1. MT 1
2. GATE
3. MT 2

STYLE 26:
PIN 1. V_{CC}
2. GROUND 2
3. OUTPUT

STYLE 27:
PIN 1. MT
2. SUBSTRATE
3. MT

STYLE 28:
PIN 1. CATHODE
2. ANODE
3. GATE

STYLE 29:
PIN 1. NOT CONNECTED
2. ANODE
3. CATHODE

STYLE 30:
PIN 1. DRAIN
2. GATE
3. SOURCE

STYLE 31:
PIN 1. GATE
2. DRAIN
3. SOURCE

STYLE 32:
PIN 1. BASE
2. COLLECTOR
3. EMITTER

STYLE 33:
PIN 1. RETURN
2. INPUT
3. OUTPUT

STYLE 34:
PIN 1. INPUT
2. GROUND
3. LOGIC

STYLE 35:
PIN 1. GATE
2. COLLECTOR
3. EMITTER

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