

# NPN High Voltage Transistor

## MMBT5551M3

The MMBT5551M3 device is a spin-off of our popular SOT-23 three-leaded device. It is designed for general purpose high voltage applications and is housed in the SOT-723 surface mount package. This device is ideal for low-power surface mount applications where board space is at a premium.

### Features

- Reduces Board Space
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

### MAXIMUM RATINGS

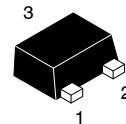
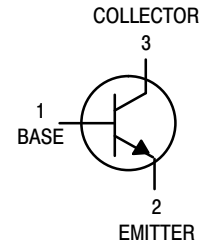
Rating	Symbol	Value	Unit
Collector – Emitter Voltage	$V_{CEO}$	160	Vdc
Collector – Base Voltage	$V_{CBO}$	180	Vdc
Emitter – Base Voltage	$V_{EBO}$	6.0	Vdc
Collector Current – Continuous	$I_C$	60	mAdc

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board (Note 1) $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	265 2.1	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	470	$^\circ\text{C}/\text{W}$
Total Device Dissipation Alumina Substrate, (Note 2) $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	640 5.1	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	195	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

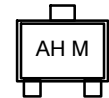
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. FR-5 =  $1.0 \times 0.75 \times 0.062$  in.
2. Alumina =  $0.4 \times 0.3 \times 0.024$  in. 99.5% alumina.



SOT-723  
CASE 631AA  
STYLE 1

### MARKING DIAGRAM



AH = Specific Device Code  
M = Date Code

### ORDERING INFORMATION

Device	Package	Shipping†
MMBT5551M3T5G	SOT-723 (Pb-Free)	8000 / Tape & Reel
NSVMMBT5551M3T5G	SOT-723 (Pb-Free)	8000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# MMBT5551M3

## ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Collector – Emitter Breakdown Voltage (Note 3) ( $I_C = 1.0\text{ mA}$ , $I_B = 0$ )	$V_{(BR)CEO}$	160	–	Vdc
Collector – Base Breakdown Voltage ( $I_C = 100\text{ }\mu\text{A}$ , $I_E = 0$ )	$V_{(BR)CBO}$	180	–	Vdc
Emitter – Base Breakdown Voltage ( $I_E = 10\text{ }\mu\text{A}$ , $I_C = 0$ )	$V_{(BR)EBO}$	6.0	–	Vdc
Collector Cutoff Current ( $V_{CB} = 120\text{ Vdc}$ , $I_E = 0$ ) ( $V_{CB} = 120\text{ Vdc}$ , $I_E = 0$ , $T_A = 100^\circ\text{C}$ )	$I_{CBO}$	– –	100 100	nAdc $\mu\text{Adc}$
Emitter Cutoff Current ( $V_{EB} = 4.0\text{ Vdc}$ , $I_C = 0$ )	$I_{EBO}$	–	50	nAdc
<b>ON CHARACTERISTICS</b>				
DC Current Gain ( $I_C = 1.0\text{ mA}$ , $V_{CE} = 5.0\text{ Vdc}$ ) ( $I_C = 10\text{ mA}$ , $V_{CE} = 5.0\text{ Vdc}$ ) ( $I_C = 50\text{ mA}$ , $V_{CE} = 5.0\text{ Vdc}$ )	$h_{FE}$	80 80 30	– 250 –	–
Collector – Emitter Saturation Voltage ( $I_C = 10\text{ mA}$ , $I_B = 1.0\text{ mA}$ ) ( $I_C = 50\text{ mA}$ , $I_B = 5.0\text{ mA}$ )	$V_{CE(sat)}$	– –	0.15 0.20	Vdc
Base – Emitter Saturation Voltage ( $I_C = 10\text{ mA}$ , $I_B = 1.0\text{ mA}$ ) ( $I_C = 50\text{ mA}$ , $I_B = 5.0\text{ mA}$ )	$V_{BE(sat)}$	– –	1.0 1.0	Vdc
Collector Emitter Cut-off ( $V_{CB} = 10\text{ V}$ ) ( $V_{CB} = 75\text{ V}$ )	$I_{CES}$	– –	50 100	nA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulse Test: Pulse Width = 300  $\mu\text{s}$ , Duty Cycle = 2.0%.

# MMBT5551M3

## TYPICAL CHARACTERISTICS

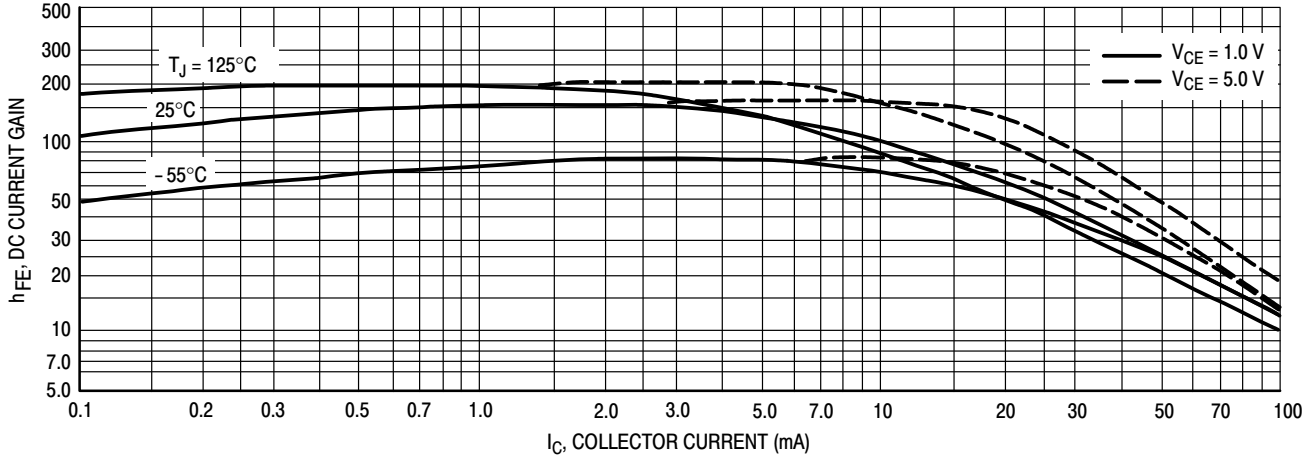


Figure 1. DC Current Gain

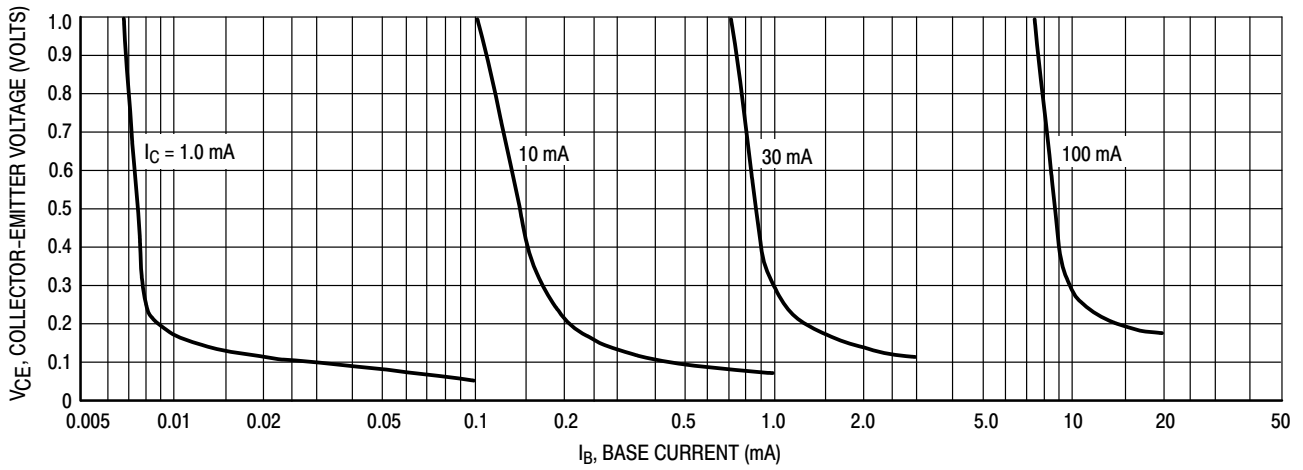


Figure 2. Collector Saturation Region

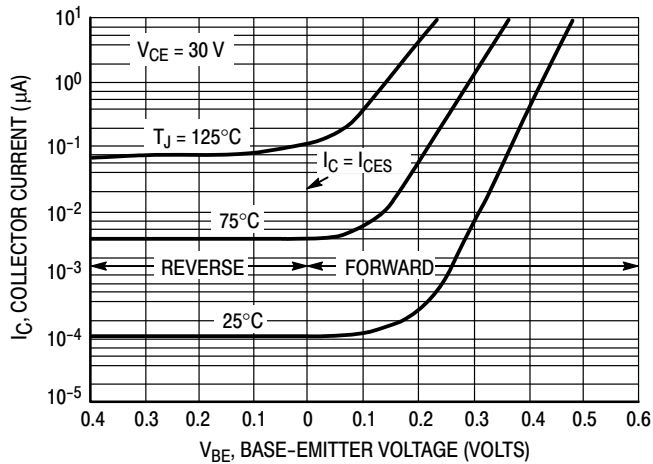


Figure 3. Collector Cut-Off Region

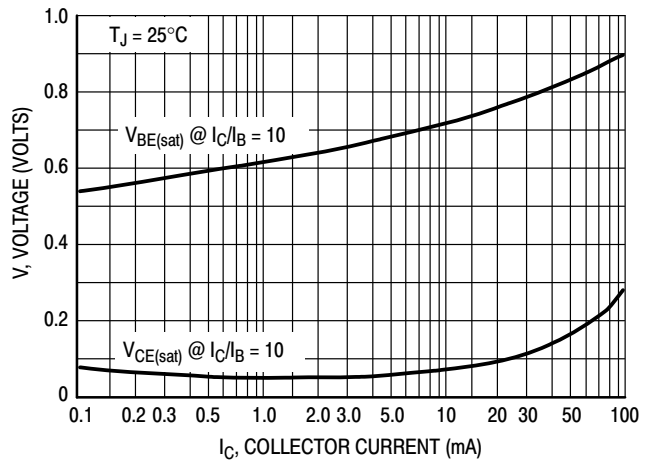


Figure 4. "On" Voltages

# MMBT5551M3

## TYPICAL CHARACTERISTICS

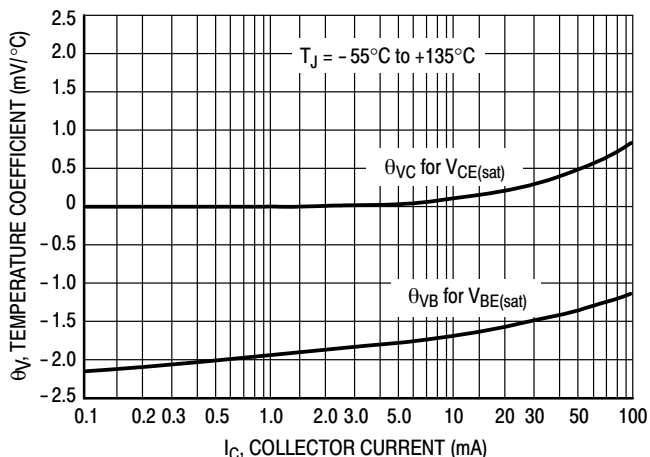
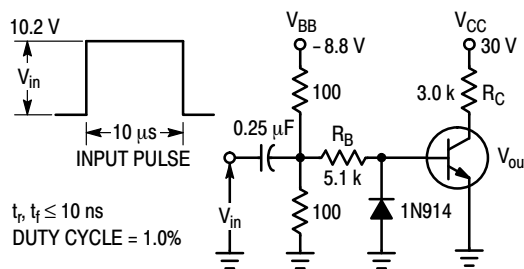


Figure 5. Temperature Coefficients



Values Shown are for  $I_C$  @ 10 mA

Figure 6. Switching Time Test Circuit

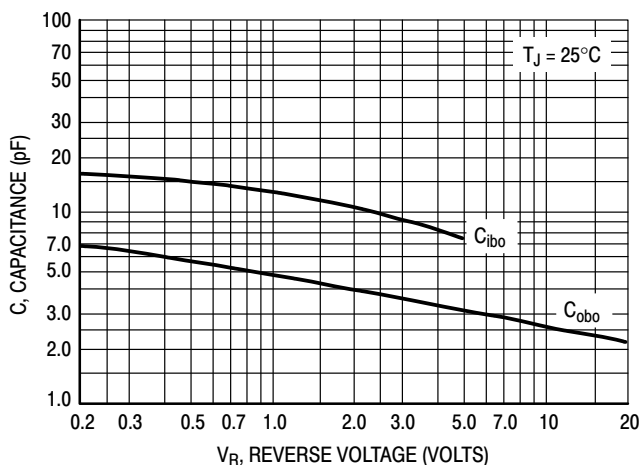


Figure 7. Capacitances

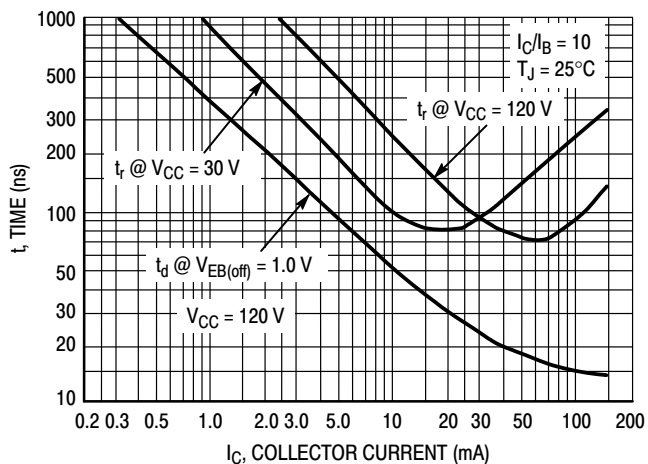


Figure 8. Turn-On Time

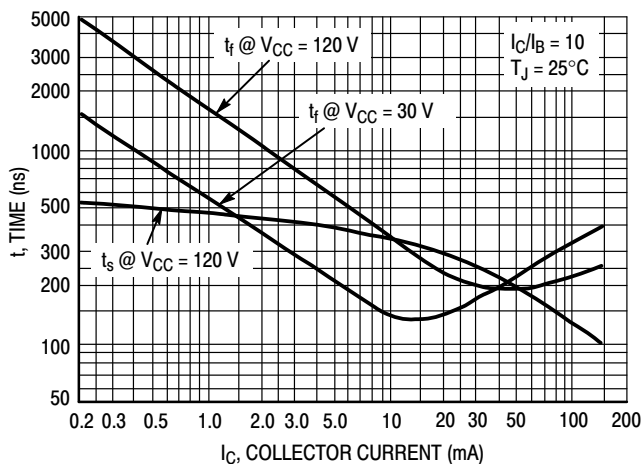


Figure 9. Turn-Off Time

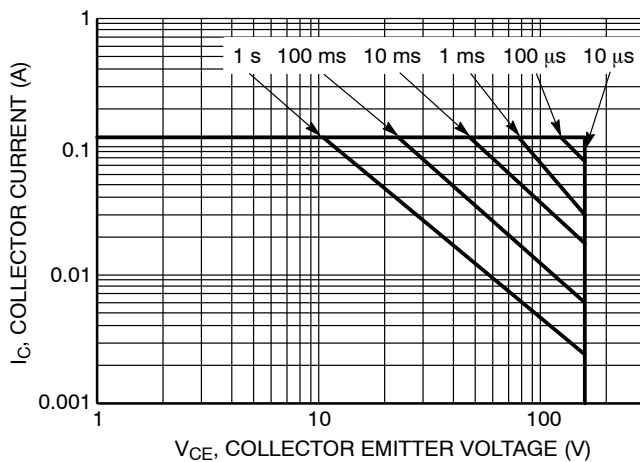
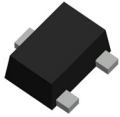


Figure 10. Safe Operating Area



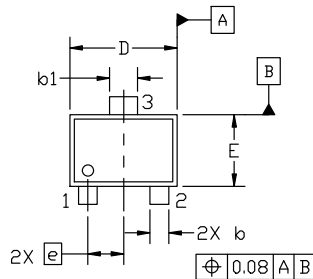
**SOT-723 1.20x0.80x0.50, 0.40P**  
**CASE 631AA**  
**ISSUE E**

DATE 24 JAN 2024

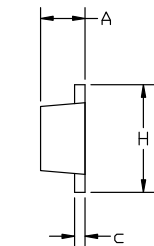
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

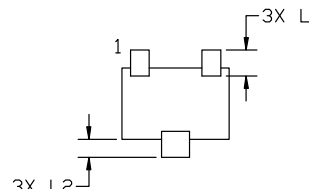
DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.45	0.50	0.55
b	0.15	0.21	0.27
b1	0.25	0.31	0.37
c	0.07	0.12	0.17
D	1.15	1.20	1.25
E	0.75	0.80	0.85
e	0.40 BSC		
H	1.15	1.20	1.25
L	0.29 REF		
L2	0.15	0.20	0.25



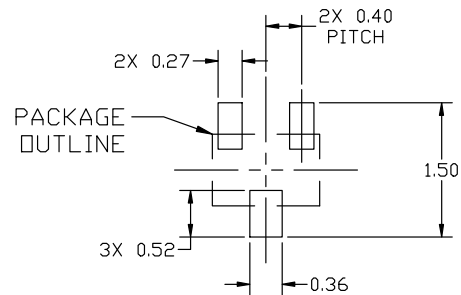
TOP VIEW



SIDE VIEW

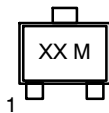


BOTTOM VIEW



RECOMMENDED MOUNTING FOOTPRINT

**GENERIC MARKING DIAGRAM\***



XX = Specific Device Code  
M = Date Code

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 2: PIN 1. ANODE 2. N/C 3. CATHODE	STYLE 3: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE	STYLE 5: PIN 1. GATE 2. SOURCE 3. DRAIN
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<b>DESCRIPTION:</b>	<b>SOT-723 1.20x0.80x0.50, 0.40P</b>	<b>PAGE 1 OF 1</b>

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