

General Purpose Transistor

NPN Silicon

MMBT2222ATT1G, NSVMMBT2222ATT1G

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-416/SC-75 package which is designed for low power surface mount applications.

Features

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_A = 25°C)

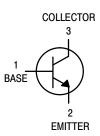
Rating	Symbol	Max	Unit
Collector-Emitter Voltage	V _{CEO}	40	Vdc
Collector-Base Voltage	V _{CBO}	75	Vdc
Emitter-Base Voltage	V _{EBO}	6.0	Vdc
Collector Current - Continuous	Ic	600	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation (Note 1) $T_A = 25^{\circ}C$	P _D	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	833	°C/W
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

 Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.





CASE 463 SOT-416/SC-75 STYLE 1

MARKING DIAGRAM



1P = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBT2222ATT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel
NSVMMBT2222ATT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure. BRD8011/D.

1

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Charac	eteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (Note 1) $(I_C = 10 \text{ mAdc}, I_B = 0)$		V _{(BR)CEO}	40	_	Vdc
Collector – Base Breakdown Voltage ($I_C = 10 \mu Adc, I_E = 0$)		V _{(BR)CBO}	75	-	Vdc
Emitter – Base Breakdown Voltage ($I_E = 10 \mu Adc, I_C = 0$)		V _{(BR)EBO}	6.0	-	Vdc
Base Cutoff Current (V _{CE} = 60 Vdc, V _{EB} = 3.0 Vdc)		I _{BL}	-	20	nAdc
Collector Cutoff Current (V _{CE} = 60 Vdc, V _{EB} = 3.0 Vdc)		I _{CEX}	-	10	nAdc
ON CHARACTERISTICS (Note 2)		<u> </u>			
DC Current Gain $ \begin{aligned} &(I_C=0.1 \text{ mAdc, } V_{CE}=10 \text{ Vdc)} \\ &(I_C=1.0 \text{ mAdc, } V_{CE}=10 \text{ Vdc)} \\ &(I_C=10 \text{ mAdc, } V_{CE}=10 \text{ Vdc)} \\ &(I_C=150 \text{ mAdc, } V_{CE}=10 \text{ Vdc)} \\ &(I_C=500 \text{ mAdc, } V_{CE}=10 \text{ Vdc)} \end{aligned} $		H _{FE}	35 50 75 100 40	- - - -	_
Collector – Emitter Saturation Voltage ($I_C = 150 \text{ mAdc}$, $I_B = 15 \text{ mAdc}$) ($I_C = 500 \text{ mAdc}$, $I_B = 50 \text{ mAdc}$)		V _{CE(sat)}	- -	0.3 1.0	Vdc
Base – Emitter Saturation Voltage (I_C = 150 mAdc, I_B = 15 mAdc) (I_C = 500 mAdc, I_B = 50 mAdc)		V _{BE(sat)}	0.6 -	1.2 2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS					
$\label{eq:Current-Gain-Bandwidth Product} \ensuremath{\text{(I}_{\text{C}}} = 20 \text{ mAdc, V}_{\text{CE}} = 20 \text{ Vdc, f} = 100 \text{ M}$	Hz)	f _T	300	_	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)		C _{obo}	-	8.0	pF
Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz)		C _{ibo}	-	30	pF
Input Impedance (V _{CE} = 10 Vdc, I _C = 10 mAdc, f = 1.0 kHz)		h _{ie}	0.25	1.25	kΩ
Voltage Feedback Ratio (V _{CE} = 10 Vdc, I _C = 10 mAdc, f = 1.0 kHz)		h _{re}	-	4.0	X 10 ⁻²
Small – Signal Current Gain (V _{CE} = 10 Vdc, I _C = 10 mAdc, f = 1.0 kHz)		h _{fe}	75	375	-
Output Admittance (V _{CE} = 10 Vdc, I _C = 10 mAdc, f = 1.0 kHz)		h _{oe}	25	200	μmhos
Noise Figure $(V_{CE}=10~Vdc,~I_{C}=100~\mu Adc,~R_{S}=1.0~k~ohms,~f=1.0~kHz)$		NF	-	4.0	dB
SWITCHING CHARACTERISTICS					
Delay Time	$(V_{CC} = 3.0 \text{ Vdc}, V_{BE} = -0.5 \text{ Vdc},$	t _d	-	10	ne
Rise Time	$I_C = 150 \text{ mAdc}, I_{B1} = 15 \text{ mAdc})$	t _r	_	25	ns
Storage Time	$(V_{CC} = 30 \text{ Vdc}, I_{C} = 150 \text{ mAdc},$	t _s	-	225	ns
Fall Time	I _{B1} = I _{B2} = 15 mAdc)	t _f	-	60	

Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.
 Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2.0%.

SWITCHING TIME EQUIVALENT TEST CIRCUITS

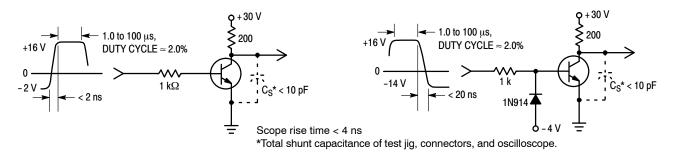


Figure 1. Turn-On Time

Figure 2. Turn-Off Time

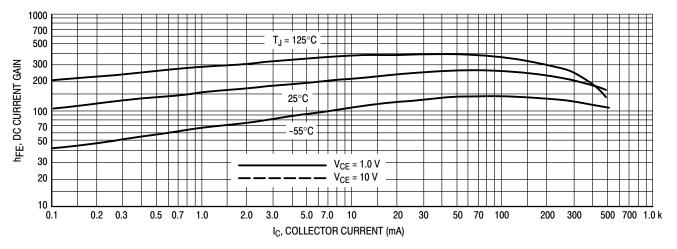


Figure 3. DC Current Gain

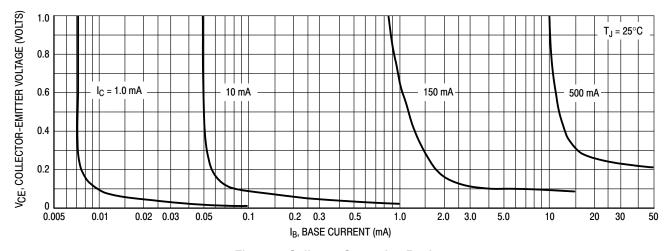


Figure 4. Collector Saturation Region

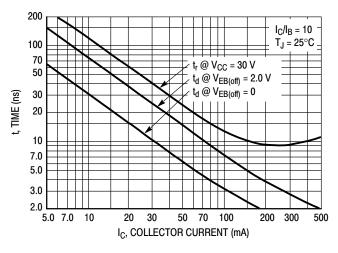


Figure 5. Turn - On Time

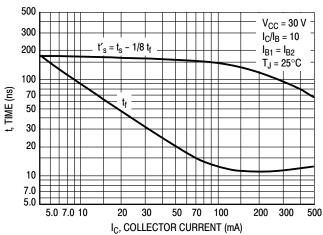


Figure 6. Turn - Off Time

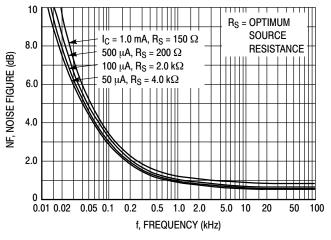


Figure 7. Frequency Effects

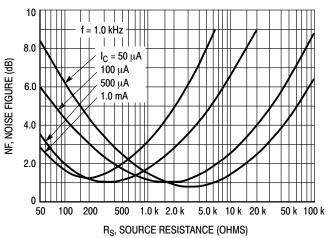


Figure 8. Source Resistance Effects

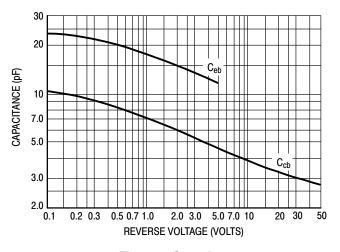


Figure 9. Capacitances

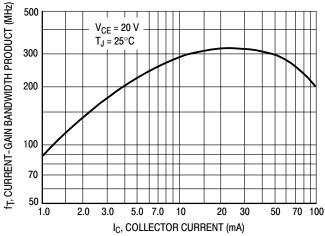


Figure 10. Current-Gain Bandwidth Product

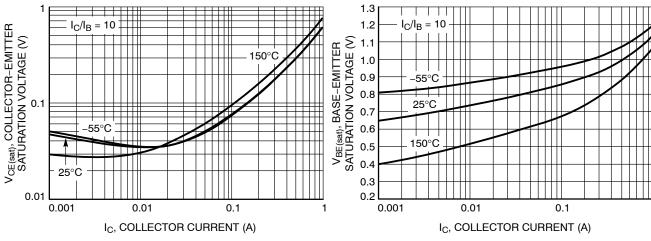
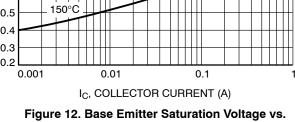


Figure 11. Collector Emitter Saturation Voltage vs. Collector Current



Collector Current

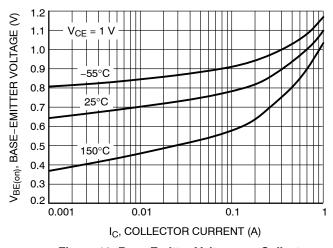


Figure 13. Base Emitter Voltage vs. Collector Current

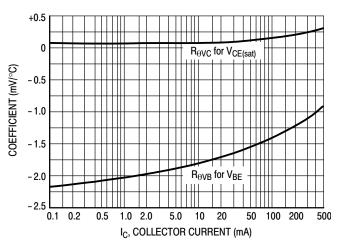


Figure 14. Temperature Coefficients

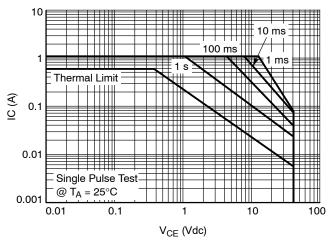


Figure 15. Safe Operating Area



SC75-3 1.60x0.80x0.80, 1.00P

CASE 463 ISSUE H

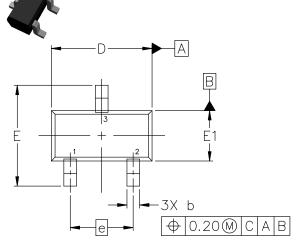
DATE 01 FEB 2024

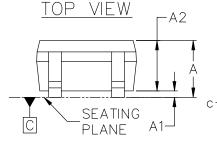
NOTES:

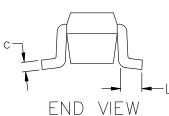
- DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
- ALL DIMENSION ARE IN MILLIMETERS.

DIM	MILLIMETERS			
DIM	MIN.	NOM.	MAX.	
А	0.70	0.80	0.90	
A1	0.00	0.05	0.10	
A2	0.80 REF.			
b	0.15	0.20	0.30	
С	0.10	0.15	0.25	
D	1.55	1.60	1.65	
Е	1.50	1.60	1.70	
E1	0.70	0.80	0.90	
е	1.00 BSC			
L	0.10	0.15	0.20	

-0.356







SIDE VIEW

GENERIC MARKING DIAGRAM*



XX = Specific Device Code

Μ = Date Code

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1:	
PIN 1. BASE	
O EMITTED	

STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE

3. COLLECTOR

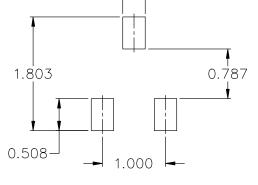
STYLE 2: PIN 1. ANODE 2. N/C 3. CATHODE

STYLE 5: PIN 1. GATE 2. SOURCE 3. DRAIN

STYLE 3: PIN 1. ANODE 2. ANODE 3 CATHODE

RECOMMENDED MOUNTING FOOTPRINT* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY

AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.



DOCUMENT NUMBER:

98ASB15184C

Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.

DESCRIPTION:

SC75-3 1.60x0.80x0.80, 1.00P

PAGE 1 OF 1

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. **onsemi** makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, Onsemi, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales