ON Semiconductor

Is Now

Onsemi

To learn more about onsemi[™], please visit our website at <u>www.onsemi.com</u>

onsemi and ONSEMI. and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product factures, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and asfety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or by customer's technical experts. onsemi products and actal performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use onsemi products for any such unintended or unauthorized application, Buyer shall indemnify and hold onsemi and its officers, employees, subsidiari

POWERTAP II Switch-mode Power Rectifier

This state-of-the-art device uses the Schottky Barrier principle with a platinum barrier metal.

Features

- Dual Diode Construction; May Be Paralleled for Higher Current Output
- Guardring for Stress Protection
- Low Forward Voltage
- 150°C Operating Junction Temperature
- Guaranteed Reverse Avalanche
- Pb-Free Package is Available*

Mechanical Characteristics

- Case: Epoxy, Molded with Metal Heatsink Base
- Weight: 80 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant
- Top Terminal Torque: 25–40 lb–in Max
- Base Plate Torques: See Procedure Given in the Package Outline Section

MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	45	ov)	
Average Rectified Forward Current (Rated V _R , T _C = 140°C) Per Leg Per Device	I _{F(AV)}	100 200	A	
$\begin{array}{llllllllllllllllllllllllllllllllllll$	IFRM	200	A	
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	1500	A	
Peak Repetitive Reverse Current (2.0 μs, 1.0 kHz) Per Leg	I _{RRM}	2.0	A	
Storage Temperature Range	T _{stg}	–55 to +150	°C	
Operating Junction Temperature	TJ	–55 to +150	°C	
Voltage Rate of Change (Rated V_R)	dv/dt	10,000	V/μs	
Stresses exceeding those listed in the Maximum Batings table may damage the				

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



ON Semiconductor®

www.onsemi.com

SCHOTTKY BARRIER RECTIFIER 200 AMPERES, 45 VOLTS

POWERTAP II CASE 357C PLASTIC

MARKING DIAGRAM



B20045T	= Specific Device Code
MCC	= Mold Compound Code
А	= Assembly Location
YY	= Year
WW	= York Week
G	= Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
MBRP20045CT	POWERTAP II	25 Units/Tray
MBRP20045CTG	POWERTAP II (Pb-Free)	25 Units/Tray

THERMAL CHARACTERISTICS (Per Leg)

Rating	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{0JC}	0.6	°C/W
ELECTRICAL CHARACTERISTICS (Per Leg)			
Instantaneous Forward Voltage (Note 1) ($i_F = 200 \text{ Amps}, T_J = 25^{\circ}C$) ($i_F = 200 \text{ Amps}, T_J = 125^{\circ}C$)	VF	0.89 0.78	V
Instantaneous Reverse Current (Note 1) (Rated dc Voltage, T _J = 125°C) (Rated dc Voltage, T _J = 25°C)	i _R	50 0.5	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width = 300 μ s, Duty Cycle \leq 2.0%.

Terminal Penetration: 0.235 max Terminal Torque: 25-40 in-lb max Mounting Torque -Outside Holes: 30-40 in-lb max Mounting Torque -Center Hole: 8-10 in-lb max Seating Plane 1 mil per in. (between mounting holes) Flatness

MAXIMUM MECHANICAL RATINGS

POWERTAP MECHANICAL DATA APPLIES OVER OPERATING TEMPERATURE

Vertical Pull 2 in. Lever Pull 250 lbs. max 50 lbs. max

e vertical an connections. Use of Note: While the POWERTAP is capable of sustaining these vertical and levered tensions, the intimate contact between POWERTAP and heat sink may be lost. This could lead to thermal runaway. The use of very flexible leads is recommended for the anode connections. Use of thermal grease is highly recommended.

> www.onsemi.com 2

MOUNTING PROCEDURE

The POWERTAP package requires special mounting considerations because of the long longitudinal axis of the copper heat sink. It is important to follow the proper tightening sequence to avoid warping the heat sink, which can reduce thermal contact between the POWERTAP and heat sink.

2-3 TURNS

2-3 TURNS

STEP 1:

Locate the POWERTAP on the heat sink and start mounting bolts into the threads by hand (2 or 3 turns).

STEP 2:

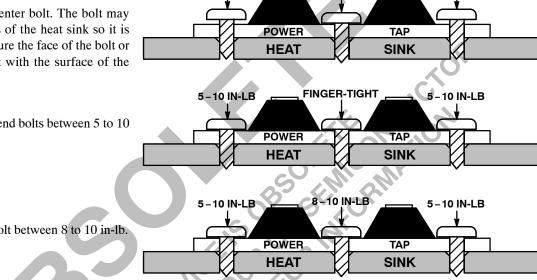
Finger tighten the center bolt. The bolt may catch on the threads of the heat sink so it is important to make sure the face of the bolt or washer is in contact with the surface of the POWERTAP.

STEP 3:

Tighten each of the end bolts between 5 to 10 in-lb.

STEP 4:

Tighten the center bolt between 8 to 10 in-lb.



POWER

HEAT

2-3 TURNS

FINGER-TIGHT

2-3 TURNS

2-3 TURNS

TAP

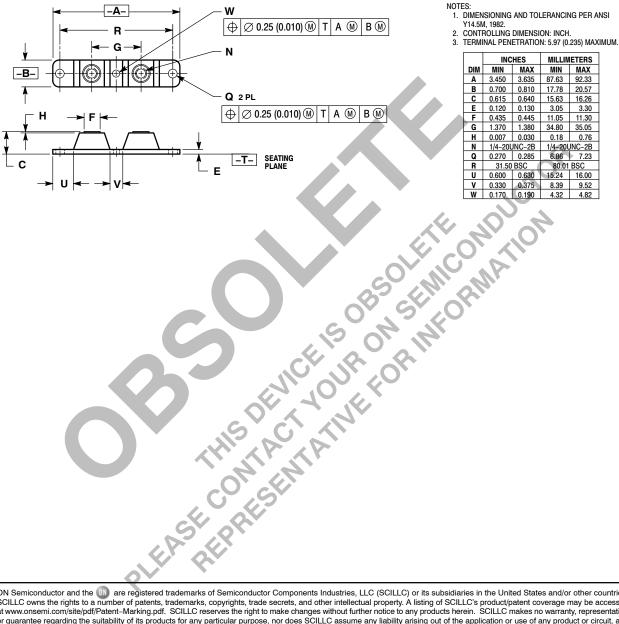
SINK

STEP 5: Finally, tighten the end bolts between 30 to 40 in-lb.	30-40 IN-LB	ТАР	
ASE PRES		SINK	
PLET PL			

PACKAGE DIMENSIONS

CASE 357C-03 POWERTAP PLASTIC PACKAGE





ON Semiconductor and the use are gistered trademarks of Semiconductor Components Industries, LLC (SCILLC) or its subsidiaries in the United States and/or other countries. SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdt/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regardin

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com N. American Technical Support: 800–282–9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support:

Phone: 421 33 790 2910 Japan Customer Focus Center Phone: 81–3–5817–1050 ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative