

Surface Mount Schottky Power Rectifier

Plastic SOD-123FL Package

MBR2H200SF

This device uses the Schottky Barrier principle with a large area metal-to-silicon power diode. Ideally suited for low voltage, high frequency rectification or as free wheeling and polarity protection diodes in surface mount applications where compact size and weight are critical to the system. Because of its small size, it is ideal for use in portable and battery powered products such as cellular and cordless phones, chargers, notebook computers, printers, PDAs and PCMCIA cards. Typical applications are AC-DC and DC-DC converters, reverse battery protection, and "Oring" of multiple supply voltages and any other application where performance and size are critical.

Features

- Guardring for Stress Protection
- Low Forward Voltage
- Epoxy Meets UL 94 V-0
- Package Designed for Optimal Automated Board Assembly
- These are Pb-Free Devices

Mechanical Characteristics

• Reel Options: MBR2H200SFT3G = 10,000 per 13 in reel/8 mm tape

• Device Marking: L2J

Polarity Designator: Cathode BandWeight: 11.7 mg (approximately)

· Case: Epoxy, Molded

• Lead Finish: 100% Matte Sn (Tin)

• Lead and Mounting Surface Temperature for Soldering Purposes:

1

260°C Max. for 10 Seconds

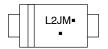
• Device Meets MSL 1 Requirements

SCHOTTKY BARRIER RECTIFIER 2.0 AMPERES 200 VOLTS



SOD-123FL CASE 498

MARKING DIAGRAM



L2J = Specific Device Code

M = Date Code ■ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MBR2H200SFT1G	SOD-123 (Pb-Free)	3000 / Tape & Reel
MBR2H200SFT3G	SOD-123 (Pb-Free)	10000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

MBR2H200SF

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	200	V
Average Rectified Forward Current (T _L = 108°C)	Io	2.0	Α
Peak Repetitive Forward Current (Rated V_R , Square Wave, 20 kHz, T_C = 105°C)	I _{FRM}	4.0	Α
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	30	Α
Storage and Operating Junction Temperature Range (Note 1)	T _{stg} , T _J	-55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Lead (Note 2)	Ψ_{JCL}	23	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ heta JA}$	85	°C/W
Thermal Resistance, Junction-to-Ambient (Note 3)	$R_{\theta JA}$	330	°C/W

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
$\label{eq:maximum Instantaneous Forward Voltage (Note 4)} \begin{tabular}{l} (I_F = 1.0 \text{ A, } T_J = 25^{\circ}\text{C}) \\ (I_F = 2.0 \text{ A, } T_J = 25^{\circ}\text{C}) \\ (I_F = 1.0 \text{ A, } T_J = 125^{\circ}\text{C}) \\ (I_F = 2.0 \text{ A, } T_J = 125^{\circ}\text{C}) \\ \end{tabular}$	V _F	0.86 0.94 0.71 0.78	V
Maximum Instantaneous Reverse Current (Note 4) (Rated dc Voltage, $T_J = 25^{\circ}C$) (Rated dc Voltage, $T_J = 125^{\circ}C$)	I _R	200 2	μA mA

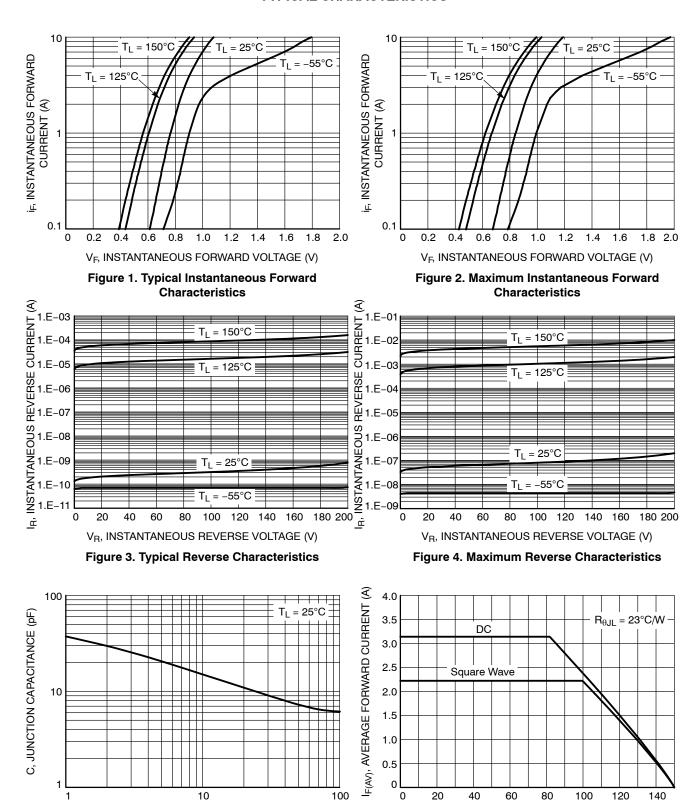
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- Mounted with 700 mm² copper pad size (Approximately 1 in²) 1 oz FR4 Board.
 Mounted with pad size approximately 20 mm² copper, 1 oz FR4 Board.
 Pulse Test: Pulse Width ≤ 380 μs, Duty Cycle ≤ 2.0%.

^{1.} The heat generated must be less than the thermal conductivity from Junction–to–Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.

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TYPICAL CHARACTERISTICS



 $\label{eq:VR} V_R,\, \text{REVERSE VOLTAGE (V)}$ Figure 5. Typical Junction Capacitance

 $\label{eq:TL} T_L\text{, LEAD TEMPERATURE (°C)}$ Figure 6. Current Derating per Diode

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TYPICAL CHARACTERISTICS

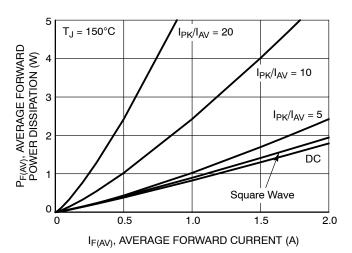


Figure 7. Forward Power Dissipation

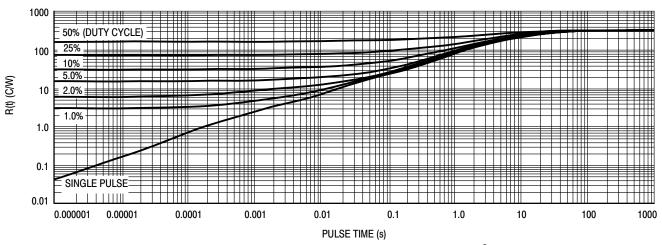


Figure 8. Thermal Response, Junction-to-Ambient (20 mm² pad)

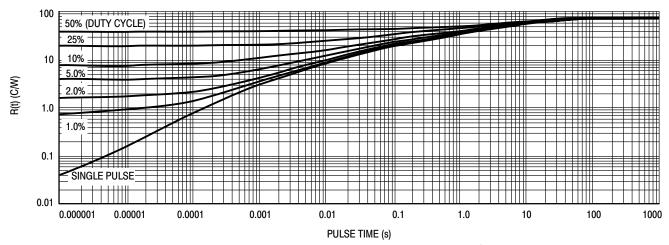


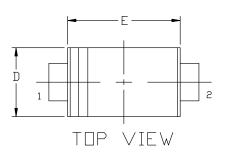
Figure 9. Thermal Response, Junction-to-Ambient (1 in² pad)

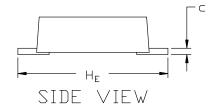


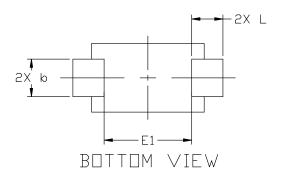


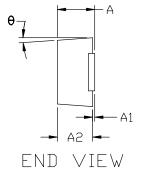
SOD-123-2 1.65x2.70x0.90 CASE 498 ISSUE E







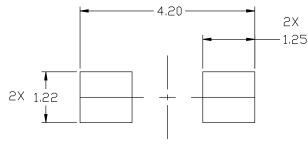




	MILLIMETERS		
DIM	MIN.	N□M.	MAX.
Α	0.90	0.95	0.98
A1	0.00	0.05	0.10
A2	0.85	0.90	0.95
b	0.70	0.90	1.10
U	0.10	0.15	0.20
D	1.50	1.65	1.80
E	2.50	2.70	2.90
E1	1.70	2.10	2.50
HE	3.40	3.60	3.80
L	0.55	0.75	0.95
θ	0°		8°

NOTES:

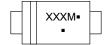
- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- DIMENSIONS 6 AND L ARE TO BE MEASURED ON A FLAT SECTION OF THE LEAD BETWEEN 0.10 AND 0.25 FROM THE LEAD TIP.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH PROTRUSIONS, OR GATE BURRS.
- 5. FLAT LEAD.



RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the IDN Semiconductor Soldering and Mounting Techniques Reference Manual, SDLDERRM/D.

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	SOD-123-2 1.65x2.70x0.90)	PAGE 1 OF 1

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