

Field Stop Trench IGBT

50 A, 650 V

FGHL50T65LQDT

Field stop 4th generation Low $V_{CE(Sat)}$ IGBT technology and Full current rated copak Diode technology.

Features

- Maximum Junction Temperature: $T_J = 175^\circ\text{C}$
- Positive Temperature Co-efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage: $V_{CE(Sat)} = 1.15\text{ V (Typ.) @ } I_C = 50\text{ A}$
- 100% of the Parts are Tested for I_{LM} (Note 2)
- Smooth and Optimized Switching
- Tight Parameter Distribution
- Co-packed with Soft and Fast Recovery Diode
- These Devices are Pb-Free and are RoHS Compliant

Typical Applications

- Solar Inverter
- UPS, ESS
- PFC, Converters

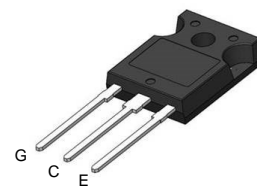
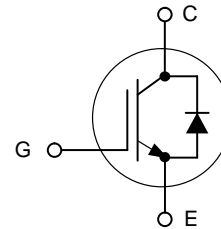
MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-to-Emitter Voltage	V_{CES}	650	V
Gate-to-Emitter Voltage Transient Gate-to-Emitter Voltage	V_{GES}	± 20 ± 30	V
Collector Current (Note 1)	I_C	80 50	A
		@ $T_C = 25^\circ\text{C}$ @ $T_C = 100^\circ\text{C}$	
Pulsed Collector Current (Note 2)	I_{LM}	200	A
Pulsed Collector Current (Note 3)	I_{CM}	200	A
Diode Forward Current	I_F	60 50	A
		@ $T_C = 25^\circ\text{C}$ @ $T_C = 100^\circ\text{C}$	
Pulsed Diode Maximum Forward Current	I_{FM}	200	A
Maximum Power Dissipation	P_D	341 170	W
		@ $T_C = 25^\circ\text{C}$ @ $T_C = 100^\circ\text{C}$	
Operating Junction and Storage Temperature Range	T_J , T_{STG}	-55 to +175	$^\circ\text{C}$
Maximum Lead Temp. for Soldering Purposes (1/8" from case for 5 s)	T_L	260	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Value limit by bond wire
2. $V_{CC} = 400\text{ V}$, $V_{GE} = 15\text{ V}$, $I_C = 200\text{ A}$, Inductive Load, 100% tested
3. Repetitive rating: pulse width limited by max. junction temperature

50 A, 650 V
 $V_{CE(Sat)} = 1.15\text{ V}$



TO-247-3L
CASE 340CX

MARKING DIAGRAM



&Y = onsemi Logo
&Z = Assembly Plant Code
&3 = 3-Digit Date Code
&K = 2-Digit Lot Traceability Code
FGHL50T65LQDT = Specific Device Code

ORDERING INFORMATION

Device	Package	Shipping
FGHL50T65LQDT	TO-247-3L	450 Units/Tube

FGHL50T65LQDT

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{\theta JC}$	0.44	$^{\circ}\text{C}/\text{W}$
Thermal resistance junction-to-case, for Diode	$R_{\theta JC}$	0.74	$^{\circ}\text{C}/\text{W}$
Thermal resistance junction-to-ambient	$R_{\theta JA}$	40	$^{\circ}\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Collector to Emitter breakdown voltage	$V_{GE} = 0\text{ V}, I_C = 1\text{ mA}$	BV_{CES}	650	–	–	V
Temperature Coefficient of Breakdown Voltage	$V_{GE} = 0\text{ V}, I_C = 1\text{ mA}$	$\frac{\Delta BV_{CES}}{\Delta T_J}$	–	0.6	–	$\text{V}/^{\circ}\text{C}$
Collector to Emitter cut-off current	$V_{GE} = 0\text{ V}, V_{CE} = 650\text{ V}$	I_{CES}	–	–	250	μA
Gate leakage current	$V_{GE} = 20\text{ V}, V_{CE} = 0\text{ V}$	I_{GES}	–	–	± 400	nA

ON CHARACTERISTICS

Gate to Emitter Threshold Voltage	$V_{GE} = V_{CE}, I_C = 50\text{ mA}$	$V_{GE(th)}$	3.0	4.5	6.0	V
Collector to Emitter Saturation Voltage	$V_{GE} = 15\text{ V}, I_C = 50\text{ A}, T_J = 25^{\circ}\text{C}$ $V_{GE} = 15\text{ V}, I_C = 50\text{ A}, T_J = 175^{\circ}\text{C}$	$V_{CE(sat)}$	–	1.15 1.22	1.35 –	V

DYNAMIC CHARACTERISTICS

Input capacitance	$V_{CE} = 30\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$	C_{ies}	–	10615	–	pF
Output capacitance		C_{oes}	–	120	–	
Reverse transfer capacitance		C_{res}	–	50	–	
Gate charge total	$V_{CE} = 400\text{ V}, I_C = 50\text{ A}, V_{GE} = 15\text{ V}$	Q_g	–	509	–	nC
Gate-to-emitter charge		Q_{ge}	–	52	–	
Gate-to-collector charge		Q_{gc}	–	167	–	

SWITCHING CHARACTERISTICS, INDUCTIVE LOAD

Turn-on delay time	$T_J = 25^{\circ}\text{C},$ $V_{CC} = 400\text{ V},$ $I_C = 25\text{ A},$ $R_G = 4.7\ \Omega,$ $V_{GE} = 15\text{ V}$	$t_{d(on)}$	–	31	–	ns	
Rise time		t_r	–	15	–		
Turn-off delay time		$t_{d(off)}$	–	408	–		
Fall time			t_f	–	164	–	mJ
Turn-on switching loss		E_{on}	–	0.51	–		
Turn-off switching loss		E_{off}	–	0.88	–		
Total switching loss		E_{ts}	–	1.39	–		
Turn-on delay time	$T_J = 25^{\circ}\text{C},$ $V_{CC} = 400\text{ V},$ $I_C = 50\text{ A},$ $R_G = 4.7\ \Omega,$ $V_{GE} = 15\text{ V}$	$t_{d(on)}$	–	33	–	ns	
Rise time		t_r	–	33	–		
Turn-off delay time		$t_{d(off)}$	–	380	–		
Fall time			t_f	–	128	–	mJ
Turn-on switching loss		E_{on}	–	1.18	–		
Turn-off switching loss		E_{off}	–	1.48	–		
Total switching loss		E_{ts}	–	2.66	–		

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Test Conditions	Symbol	Min	Typ	Max	Unit
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SWITCHING CHARACTERISTICS, INDUCTIVE LOAD

Turn-on delay time	$T_J = 175^\circ\text{C},$ $V_{CC} = 400\text{ V},$ $I_C = 25\text{ A},$ $R_G = 4.7\ \Omega,$ $V_{GE} = 15\text{ V}$	$t_{d(on)}$	–	26	–	ns
Rise time		t_r	–	18	–	
Turn-off delay time		$t_{d(off)}$	–	444	–	
Fall time		t_f	–	244	–	
Turn-on switching loss		E_{on}	–	0.94	–	mJ
Turn-off switching loss		E_{off}	–	1.26	–	
Total switching loss		E_{ts}	–	2.21	–	
Turn-on delay time	$T_J = 175^\circ\text{C},$ $V_{CC} = 400\text{ V},$ $I_C = 50\text{ A},$ $R_G = 4.7\ \Omega,$ $V_{GE} = 15\text{ V}$	$t_{d(on)}$	–	30	–	ns
Rise time		t_r	–	34	–	
Turn-off delay time		$t_{d(off)}$	–	432	–	
Fall time		t_f	–	204	–	
Turn-on switching loss		E_{on}	–	1.92	–	mJ
Turn-off switching loss		E_{off}	–	2.3	–	
Total switching loss		E_{ts}	–	4.22	–	

DIODE CHARACTERISTICS

Diode Forward Voltage	$I_F = 50\text{ A}, T_J = 25^\circ\text{C}$	V_{FM}	–	1.65	2.1	V
	$I_F = 50\text{ A}, T_J = 175^\circ\text{C}$		–	1.55	–	
Reverse Recovery Energy	$T_J = 25^\circ\text{C},$ $V_R = 400\text{ V},$ $I_F = 25\text{ A},$ $di_F/dt = 1000\text{ A}/\mu\text{s}$	E_{rec}	–	77	–	μJ
Reverse Recovery Time		T_{rr}	–	57	–	ns
Reverse Recovery Charge		Q_{rr}	–	532	–	nC
Reverse Recovery Current		I_{rr}	–	19	–	A
Reverse Recovery Energy	$T_J = 25^\circ\text{C},$ $V_R = 400\text{ V},$ $I_F = 50\text{ A},$ $di_F/dt = 1000\text{ A}/\mu\text{s}$	E_{rec}	–	127	–	μJ
Reverse Recovery Time		T_{rr}	–	75	–	ns
Reverse Recovery Charge		Q_{rr}	–	783	–	nC
Reverse Recovery Current		I_{rr}	–	21	–	A
Reverse Recovery Energy	$T_J = 175^\circ\text{C},$ $V_R = 400\text{ V},$ $I_F = 25\text{ A},$ $di_F/dt = 1000\text{ A}/\mu\text{s}$	E_{rec}	–	356	–	μJ
Reverse Recovery Time		T_{rr}	–	101	–	ns
Reverse Recovery Charge		Q_{rr}	–	1725	–	nC
Reverse Recovery Current		I_{rr}	–	35	–	A
Reverse Recovery Energy	$T_J = 175^\circ\text{C},$ $V_R = 400\text{ V},$ $I_F = 50\text{ A},$ $di_F/dt = 1000\text{ A}/\mu\text{s}$	E_{rec}	–	549	–	μJ
Reverse Recovery Time		T_{rr}	–	132	–	ns
Reverse Recovery Charge		Q_{rr}	–	2465	–	nC
Reverse Recovery Current		I_{rr}	–	38	–	A

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

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TYPICAL CHARACTERISTICS

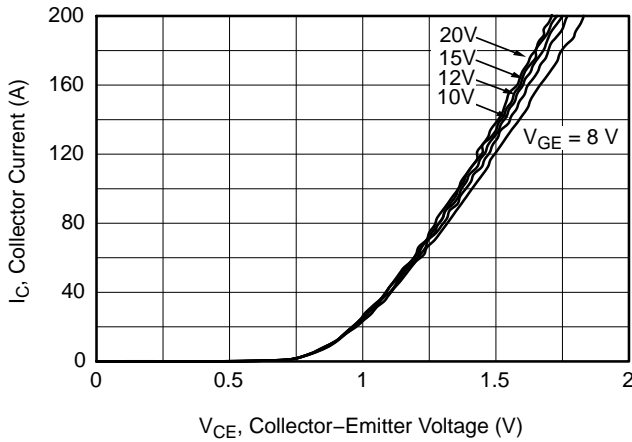


Figure 1. Typical Output Characteristics
($T_J = 25^\circ\text{C}$)

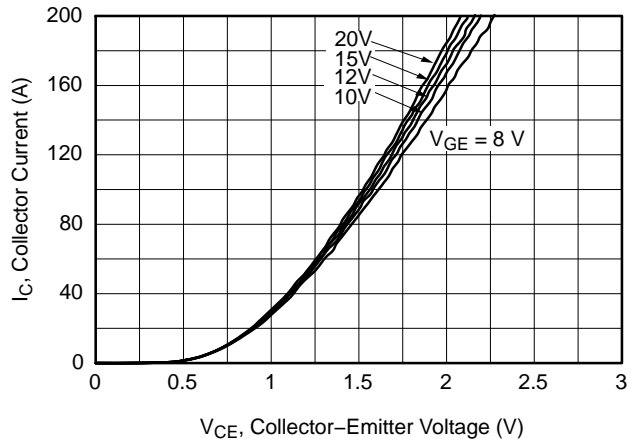


Figure 2. Typical Output Characteristics
($T_J = 175^\circ\text{C}$)

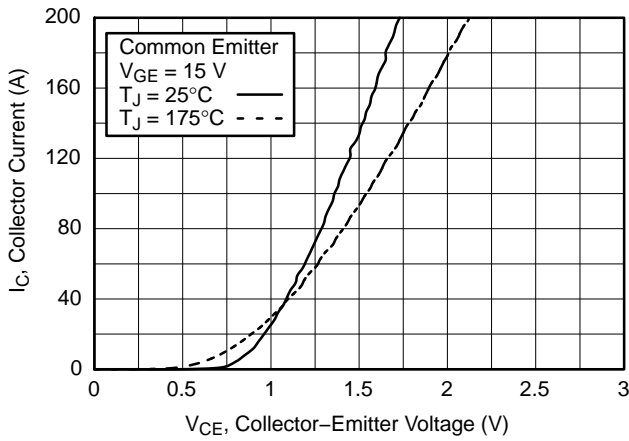


Figure 3. Typical Saturation Voltage Characteristics

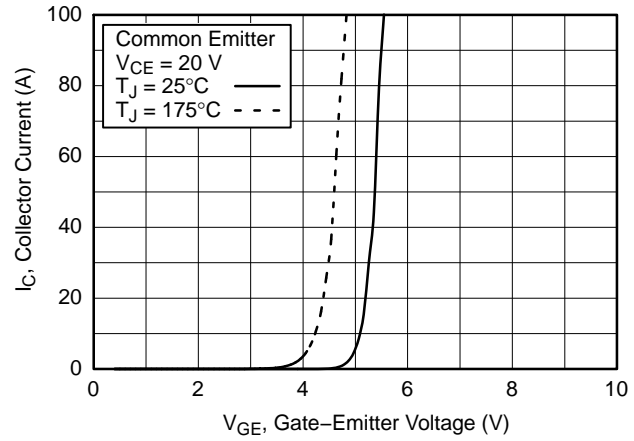


Figure 4. Typical Transfer Characteristics

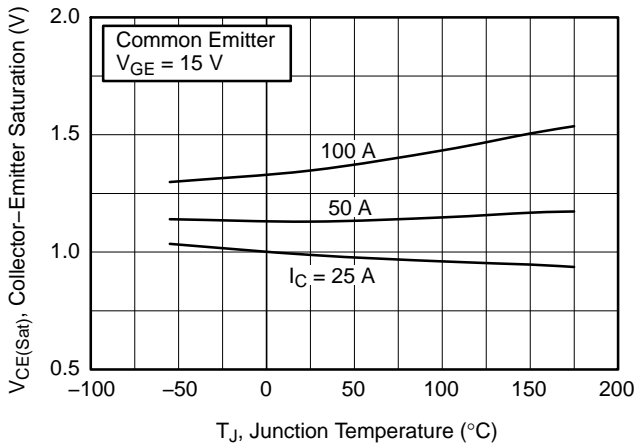


Figure 5. Saturation Voltage vs. Junction Temperature

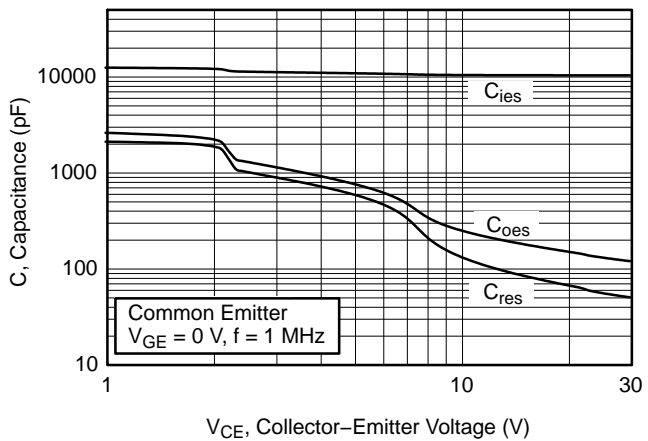


Figure 6. Capacitance Characteristics

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TYPICAL CHARACTERISTICS (continued)

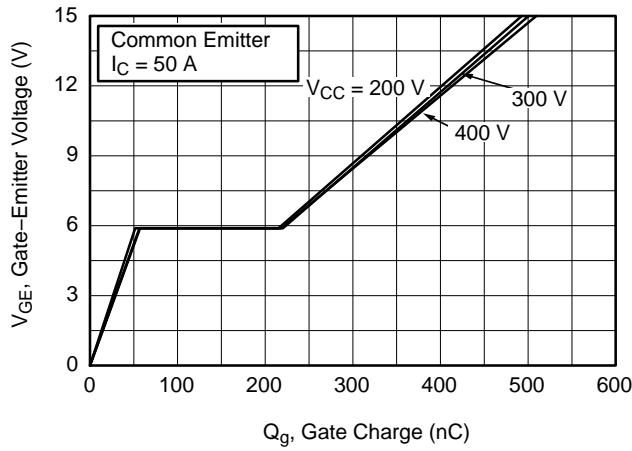


Figure 7. Gate Charge Characteristics

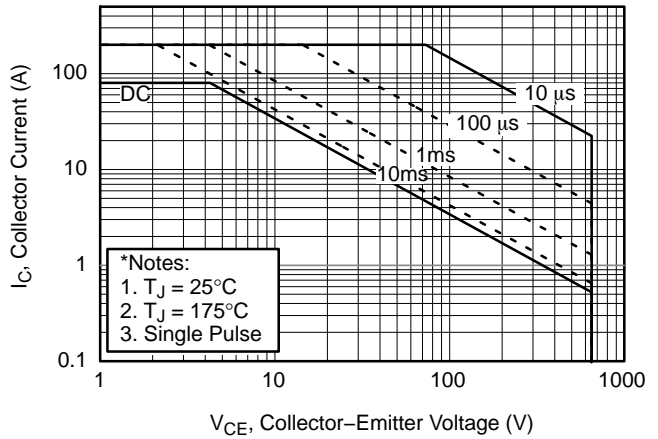


Figure 8. SOA Characteristics

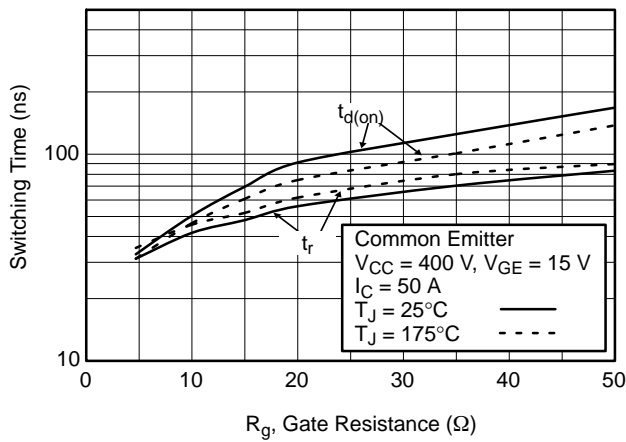


Figure 9. Turn-On Characteristics vs. Gate Resistance

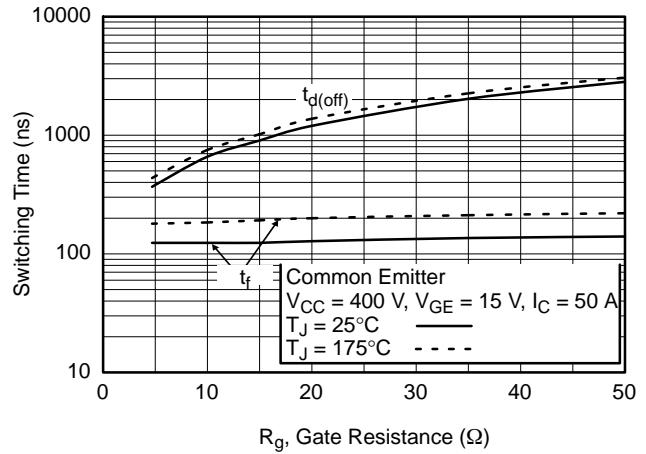


Figure 10. Turn-Off Characteristics vs. Gate Resistance

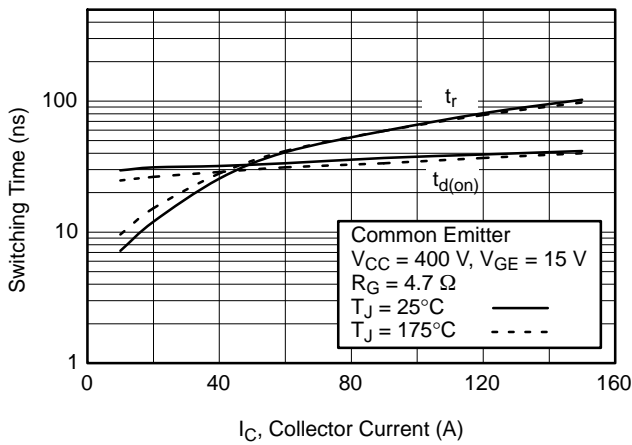


Figure 11. Turn-On Characteristics vs. Collector Current

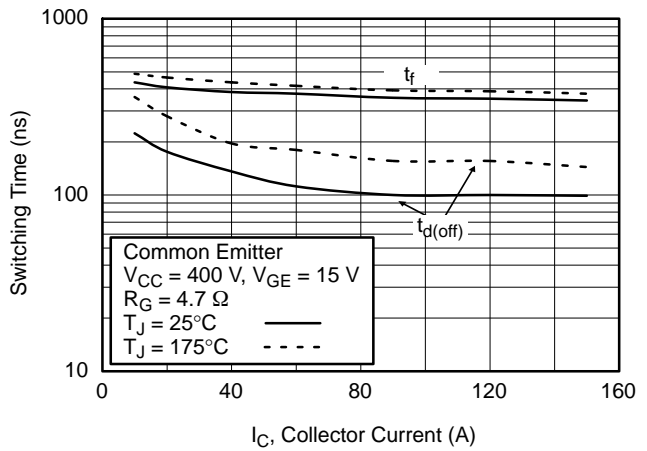


Figure 12. Turn-Off Characteristics vs. Collector Current

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TYPICAL CHARACTERISTICS (continued)

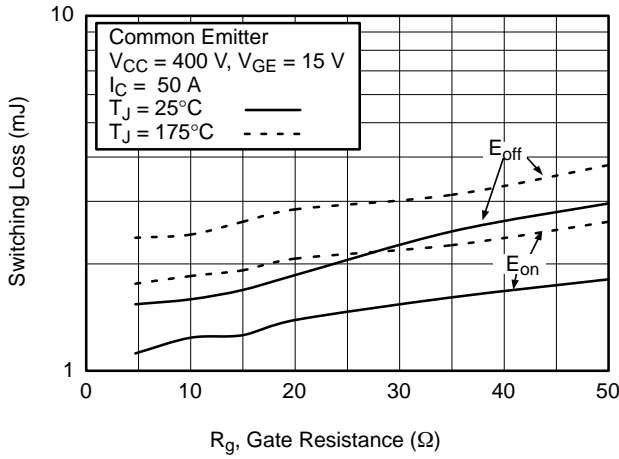


Figure 13. Switching Loss vs. Gate Resistance

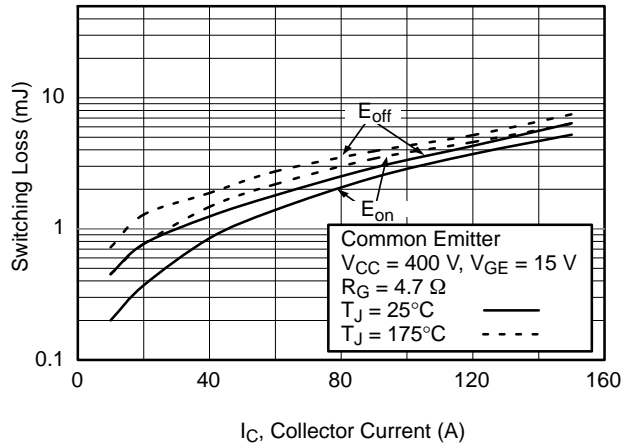


Figure 14. Switching Loss vs. Collector Current

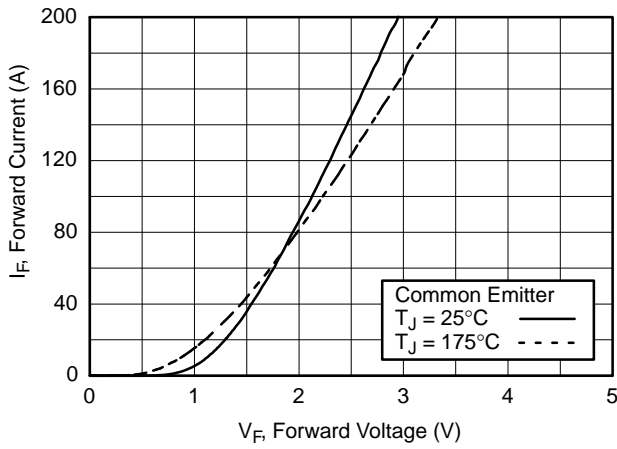


Figure 15. Forward Characteristics

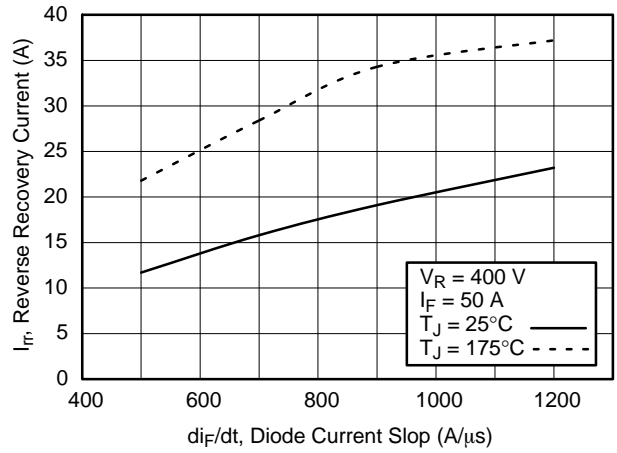


Figure 16. Reverse Recovery Current

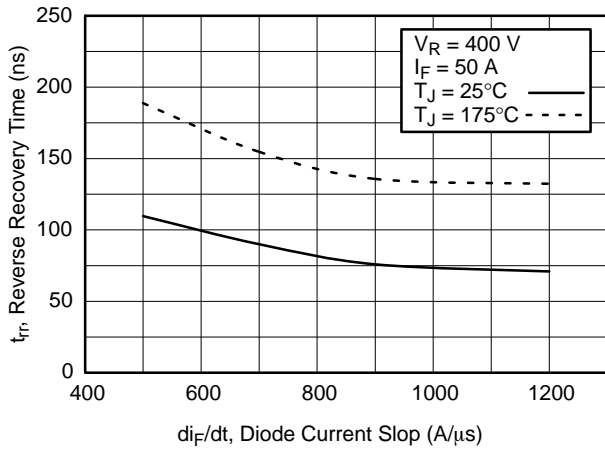


Figure 17. Reverse Recovery Time

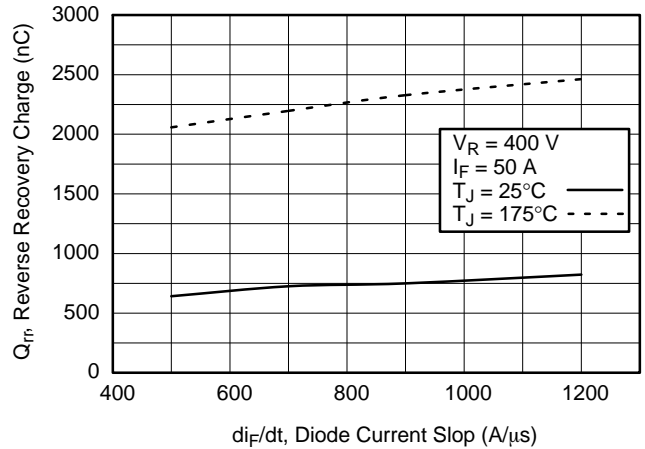


Figure 18. Stored Charge

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TYPICAL PERFORMANCE CHARACTERISTICS (continued)

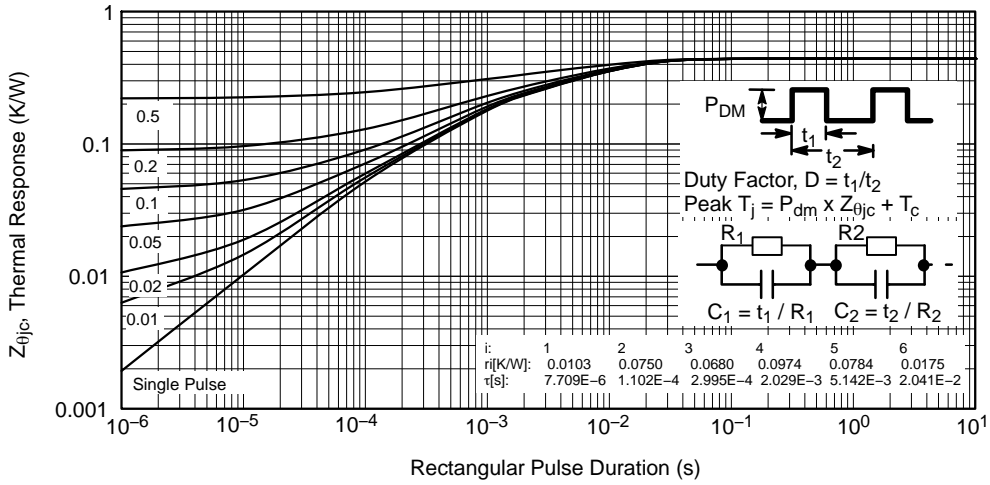


Figure 19. Transient Thermal Impedance of IGBT

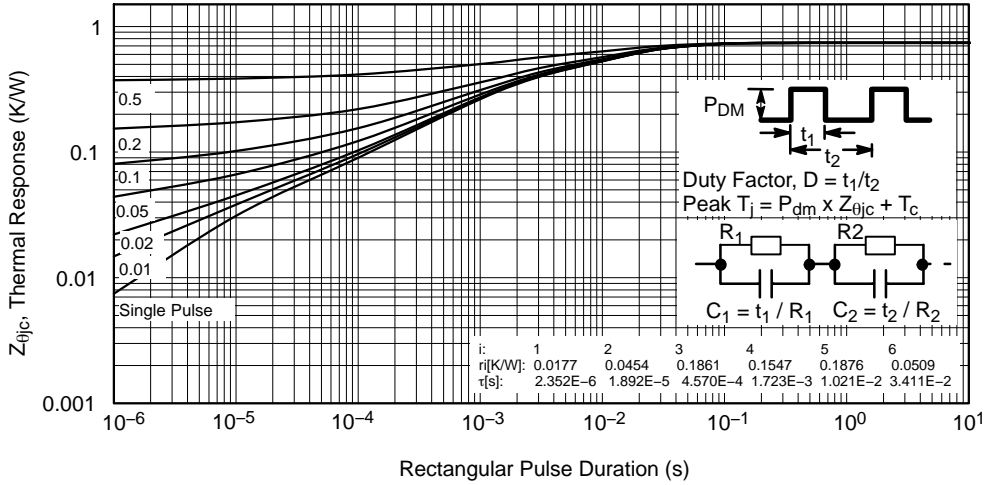
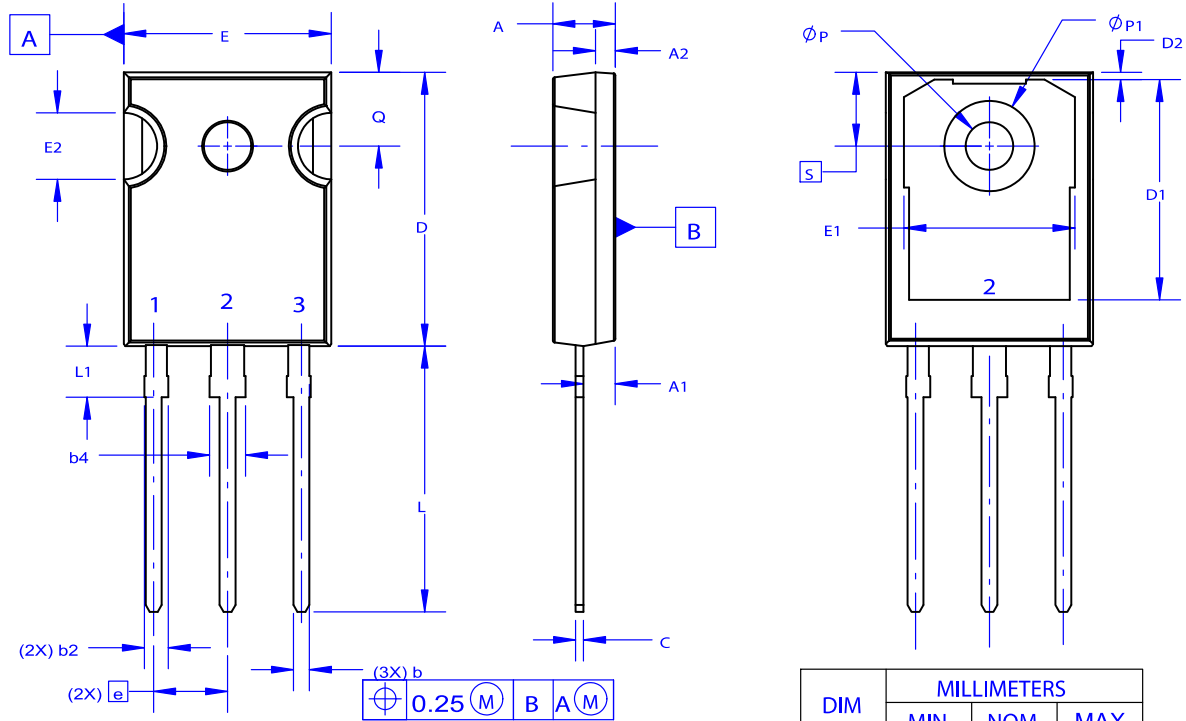


Figure 20. Transient Thermal Impedance of Diode

FGHL50T65LQDT

PACKAGE DIMENSIONS

TO-247-3LD
CASE 340CX
ISSUE A



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 - 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.58	4.70	4.82
A1	2.20	2.40	2.60
A2	1.40	1.50	1.60
D	20.32	20.57	20.82
E	15.37	15.62	15.87
E2	4.96	5.08	5.20
e	~	5.56	~
L	19.75	20.00	20.25
L1	3.69	3.81	3.93
ϕP	3.51	3.58	3.65
Q	5.34	5.46	5.58
S	5.34	5.46	5.58
b	1.17	1.26	1.35
b2	1.53	1.65	1.77
b4	2.42	2.54	2.66
c	0.51	0.61	0.71
D1	13.08	~	~
D2	0.51	0.93	1.35
E1	12.81	~	~
ϕP1	6.60	6.80	7.00

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