

Silicon Carbide (SiC) Schottky Diode – EliteSiC, 50 A, 1200 V, D1, TO-247-2L

FFSH50120A

Description

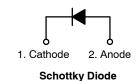
Silicon Carbide (SiC) Schottky Diodes use a completely new technology that provides superior switching performance and higher reliability compared to Silicon. No reverse recovery current, temperature independent switching characteristics, and excellent thermal performance sets Silicon Carbide as the next generation of power semiconductor. System benefits include highest efficiency, faster operating frequency, increased power density, reduced EMI, and reduced system size & cost.

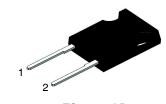
Features

- Max Junction Temperature 175°C
- Avalanche Rated 441 mJ
- High Surge Current Capacity
- Positive Temperature Coefficient
- Ease of Paralleling
- No Reverse Recovery/No Forward Recovery
- This Device is Pb–Free, Halogen Free/BFR Free and RoHS Compliant

Applications

- General Purpose
- SMPS, Solar Inverter, UPS
- Power Switching Circuits





TO-247-2LD CASE 340CL

MARKING DIAGRAM



A YWW ZZ

1

= Assembly Plant Code= Date Code (Year & Week)

ZZ FFSH50120A = Lot Code

= Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Value	Unit	
V_{RRM}	Peak Repetitive Reverse Voltage	1200	V	
E _{AS}	Single Pulse Avalanche Energy (Note 1)		441	mJ
I _F	Continuous Rectified Forward Current @ T _C < 155°C		50	Α
	Continuous Rectified Forward Current @ T _C <	Continuous Rectified Forward Current @ T _C < 135°C		
I _{F, Max}	Non-Repetitive Peak Forward Surge Current $T_C = 25^{\circ}C$, 10 μ s		1700	Α
		T _C = 150°C, 10 μs	1600	Α
I _{F,SM}	Non-Repetitive Forward Surge Current	Half-Sine Pulse, t _p = 8.3 ms	280	Α
I _{F,RM}	Repetitive Forward Surge Current	Half-Sine Pulse, t _p = 8.3 ms	85	Α
Ptot	Power Dissipation	T _C = 25°C	736	W
		T _C = 150°C	147	W
T _J , T _{STG}	Operating and Storage Temperature Range TO-247 Mounting Torque, M3 Screw		-55 to +175	°C
			60	Ncm

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. E_{AS} of 441 mJ is based on starting T_J = 25°C, L = 0.5 mH, I_{AS} = 42 A, V = 50 V.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ hetaJC}$	Thermal Resistance, Junction to Case, Max	0.17	°C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
V_{F}	Forward Voltage	I _F = 50 A, T _C = 25°C	-	1.45	1.75	V
		I _F = 50 A, T _C = 125°C	-	1.7	2.0	
		I _F = 50 A, T _C = 175°C	-	2.0	2.4	
I _R	Reverse Current	V _R = 1200 V, T _C = 25°C	-	=	200	μΑ
		V _R = 1200 V, T _C = 125°C	-	=	300	
		V _R = 1200 V, T _C = 175°C	-	=	400	
$Q_{\mathbb{C}}$	Total Capacitive Charge	V = 800 V	-	252	=	nC
С	Total Capacitance	V _R = 1 V, f = 100 kHz	-	2560	=	pF
		V _R = 400 V, f = 100 kHz	-	234	=	
		V _R = 800 V, f = 100 kHz	-	190	-	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

ORDERING INFORMATION

Part Number	Top Marking	Package	Shipping
FFSH50120A	FFSH50120A	TO-247-2LD	30 Units / Tube

TYPICAL CHARACTERISTICS

(T_J = 25°C UNLESS OTHERWISE NOTED)

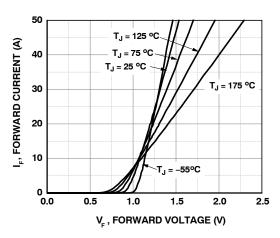


Figure 1. Forward Characteristics

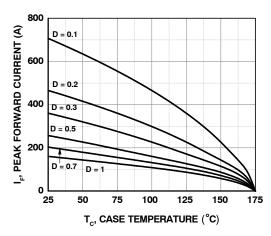


Figure 3. Current Derating

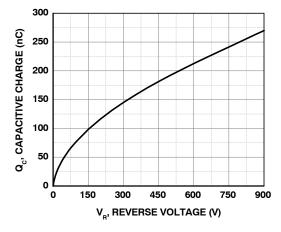


Figure 5. Capacitive Charge vs. Reverse Voltage

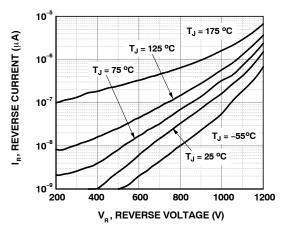


Figure 2. Reverse Characteristics

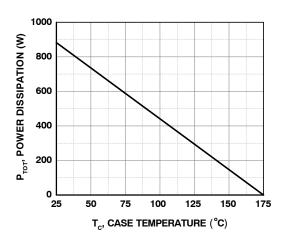


Figure 4. Power Derating

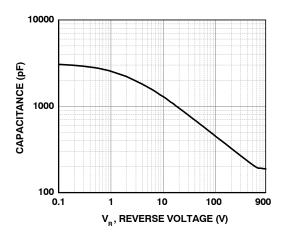


Figure 6. Capacitance vs. Reverse Voltage

TYPICAL CHARACTERISTICS

(T_J = 25°C UNLESS OTHERWISE NOTED)

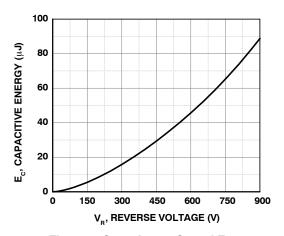


Figure 7. Capacitance Stored Energy

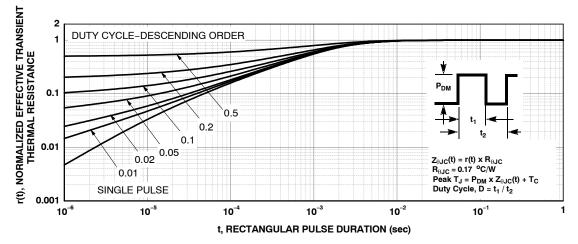


Figure 8. Junction-to-Case Transient Thermal Response Curve

TEST CIRCUIT AND WAVEFORMS

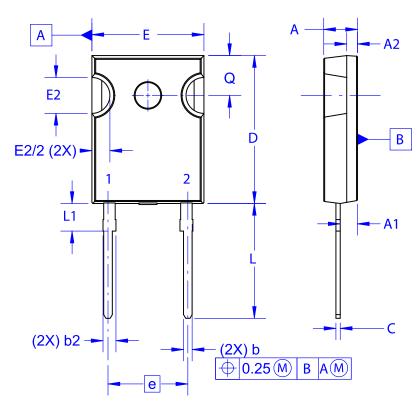
L = 0.5 mH $R < 0.1 \Omega$ $V_{DD} = 50 \text{ V}$ $EAVL = 1/2L12 \left[V_{R(AVL)} / \left(V_{R(AVL)} - V_{DD} \right) \right]$ $Q1 = IGBT \left(BV_{CES} > DUT \ V_{R(AVL)} \right)$ V_{AVL} $CURRENT V_{DD}$ $SENSE V_{DD}$ V_{DD}

Figure 9. Unclamped Inductive Switching Test Circuit & Waveform

DATE 03 DEC 2019



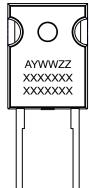






- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 2009.
 D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code

= Assembly Location

= Year

= Work Week WW

= Assembly Lot Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

Ø P —		Ø P1 D2
S E1 —		D1
		J

DIM	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	4.58	4.70	4.82	
A1	2.29	2.40	2.66	
A2	1.30	1.50	1.70	
b	1.17	1.26	1.35	
b2	1.53	1.65	1.77	
С	0.51	0.61	0.71	
D	20.32	20.57	20.82	
D1	16.37	16.57	16.77	
D2	0.51	0.93	1.35	
Е	15.37	15.62	15.87	
E1	12.81	~	~	
E2	4.96	5.08	5.20	
е	~	11.12	~	
L	15.75	16.00	16.25	
L1	3.69	3.81	3.93	
ØΡ	3.51	3.58	3.65	
Ø P 1	6.61	6.73	6.85	
Q	5.34	5.46	5.58	
S	5.34	5.46	5.58	

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DESCRIPTION:	TO-247-2LD		PAGE 1 OF 1	

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