Power MOSFET, N-Channel, Trench[®], 1.5 V Specified Thin WLCSP

20 V, 5.3 A, 39 mΩ

General Description

Designed on advanced 1.5 V PowerTrench[®] process with state of the art "fine pitch" WLCSP packaging process, the FDZ192NZ minimizes both PCB space and r_{DS(on)}. This advanced WLCSP MOSFET embodies a breakthrough in packaging technology which enables the device to combine excellent thermal transfer characteristics, ultra-low profile packaging, low gate charge, and low r_{DS(on)}.

Features

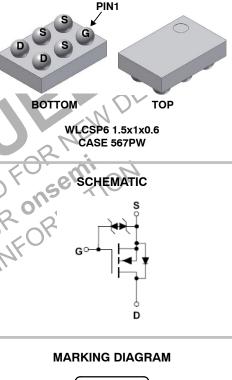
- Max $r_{DS(on)} = 39 \text{ m}\Omega$ at $V_{GS} = 4.5 \text{ V}$, $I_D = 2.0 \text{ A}$
- Max $r_{DS(on)} = 43 \text{ m}\Omega$ at $V_{GS} = 2.5 \text{ V}$, $I_D = 2.0 \text{ A}$
- Max $r_{DS(on)} = 49 \text{ m}\Omega$ at $V_{GS} = 1.8 \text{ V}$, $I_D = 1.0 \text{ A}$
- Max $r_{DS(on)} = 55 \text{ m}\Omega$ at $V_{GS} = 1.5 \text{ V}$, $I_D = 1.0 \text{ A}$
- Occupies only 1.5 mm² of PCB Area. Less than 50% of the Area of 2 x 2 BGA

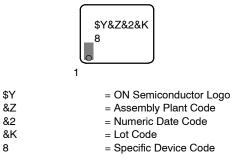
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant , wanagement • Load Switch • Battery Protection • HS REPRESENTATION • REPRESENTA



ON Semiconductor®

www.onsemi.com





ORDERING INFORMATION

See detailed ordering and shipping information on page 2 of this data sheet.

ORDERING INFORMATION

Part Number	Device Marking	Package	Shipping [†]
FDZ192NZ	8	WLCSP6 1.5x1x0.6 (Pb-Free / Halogen Free)	5000 Units / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

ABSOLUTE MAXIMUM RATINGS (T_A = 25° C unless otherwise noted)

Symbol	Parameter	Value	Unit
V _{DS}	Drain to Source Voltage	20	V
V _{GS}	Gate to Source Voltage	±8	V
۱ _D	Drain Current Continuous, T _A = 25°C (Note 1a)	5.3	А
	Drain Current Pulsed	15	
PD	Power Dissipation, $T_A = 25^{\circ}C$ (Note 1a)	1.9	N W
	Power Dissipation, $T_A = 25^{\circ}C$ (Note 1b)	0.9	2
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 to 150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Symbol	Parameter Value	Unit
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1a) 65	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient (Note 1b) 133	°C/W
	CHARACTERISTICS (T - 25°C unless otherwise noted)	

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
off Charact	eristics	DR. IE'				
BV_{DSS}	Drain to Source Breakdown Voltage	I _D = 250 μA, V _{GS} = 0 V	20			V
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \ \mu A$, referenced to 25°C		10		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 16 V, V _{GS} = 0 V			1	μA
I _{GSS}	Gate to Source Leakage Current	$V_{GS} = \pm 8 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$			±10	μΑ

On Characteristics

VGS(th)	Gate to Source Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250 \ \mu A$	0.4	0.7	1.0	V
$\Delta V_{GS(th)} \Delta T_J$	Gate to Source Threshold Voltage Temperature Coefficient	$I_D = 250 \ \mu$ A, referenced to 25°C		-3		mV/°C
r _{DS(on)}	Static Drain to Source On Resistance	$V_{GS} = 4.5 \text{ V}, I_{D} = 2.0 \text{ A}$		26	39	mΩ
		$V_{GS} = 2.5 \text{ V}, I_{D} = 2.0 \text{ A}$		29	43	
		V _{GS} = 1.8 V, I _D = 1.0 A		33	49	
		V _{GS} = 1.5 V, I _D = 1.0 A		38	55	
		$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 2.0 \text{ A}, \text{ T}_{J} = 125^{\circ}\text{C}$		31	47	
9 _{FS}	Forward Transconductance	$V_{DS} = 5 \text{ V}, \text{ I}_{D} = 5.3 \text{ A}$		36		s

Dynamic Characteristics

Ciss	Input Capacitance	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	915	1220	pF
Coss	Output Capacitance	$v_{\rm DS} = 10^{-1}$, $v_{\rm GS} = 0^{-1}$, $1 = 1^{-1}$ with 2	145	195	pF
Crss	Reverse Transfer Capacitance		100	150	pF

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted) (continued)

Symbol	Parameter	Test Cor	Test Conditions		Тур	Max	Unit
Switching C	Characteristics						
td(on)	Turn-On Delay Time				6.5	13	ns
t _r	Rise Time		V_{DD} = 10 V, I _D = 5.3 A, V _{GS} = 4.5 V, R _{GEN} = 6 Ω		4	10	ns
td(off)	Turn-Off Delay Time				50	80	ns
t _f	Fall Time				20	32	ns
Q _g	Total Gate Charge	V_{GS} = 0 V to 4.5 V	$V_{DD} = 10 V,$ $I_{D} = 5.3 A$		12	17	nC
Qgs	Gate to Source Charge				1.3		nC
Qgd	Gate to Drain "Miller" Charge				2.3		nC

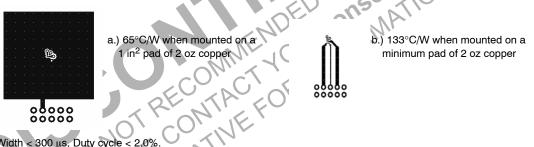
Drain-Source Diode Characteristics

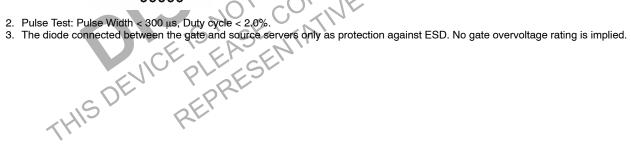
١	V _{SD}	Source to Drain Diode Forward Voltage	V _{GS} = 0 V, I _S = 1.1 A (Note 2)	0.6	1.2	V
	trr	Reverse Recovery Time	I _F = 5.3 A, di/dt = 100 A/us	18	32	ns
(Qrr	Reverse Recovery Charge		4.6	10	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

NOTES:

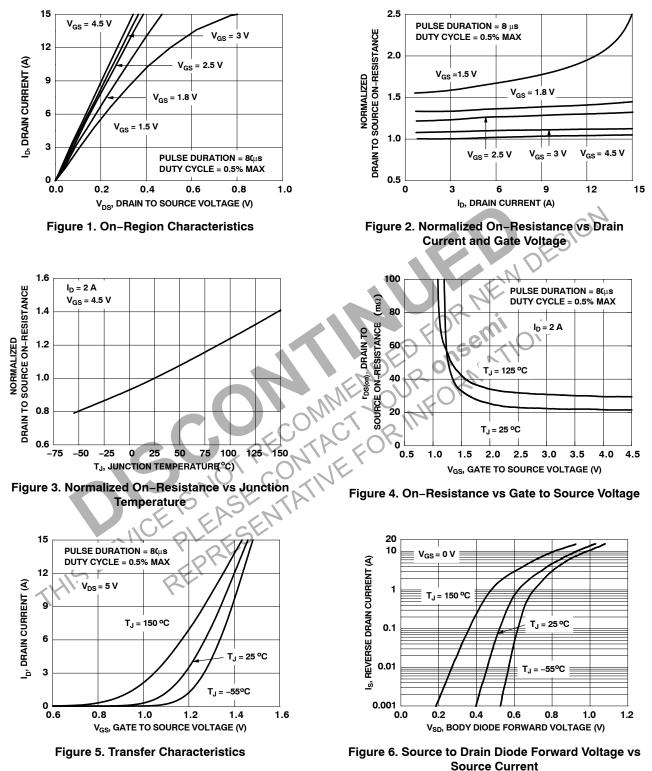
1. $R_{\theta JA}$ is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5 x 1.5 in. board of FR-4 material. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design.





TYPICAL CHARACTERISTICS

(T_J = 25°C unless otherwise noted)



TYPICAL CHARACTERISTICS

(T_J = 25°C unless otherwise noted)

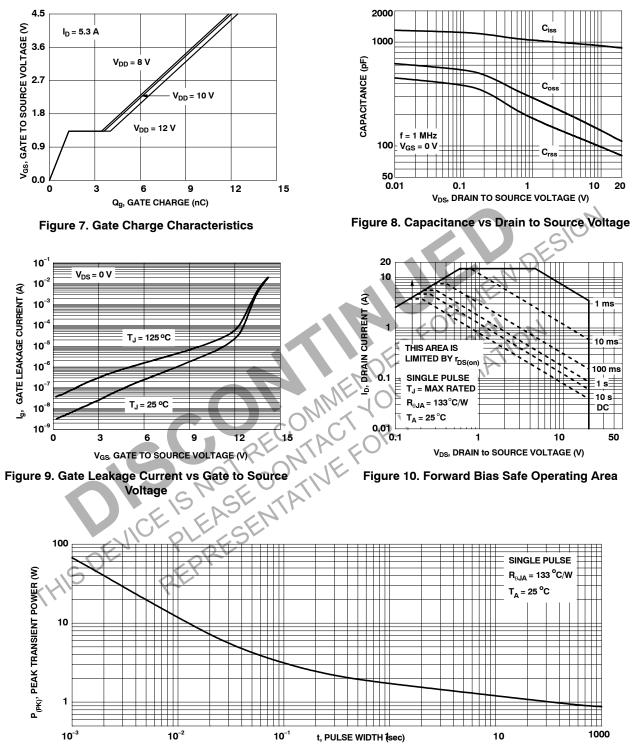
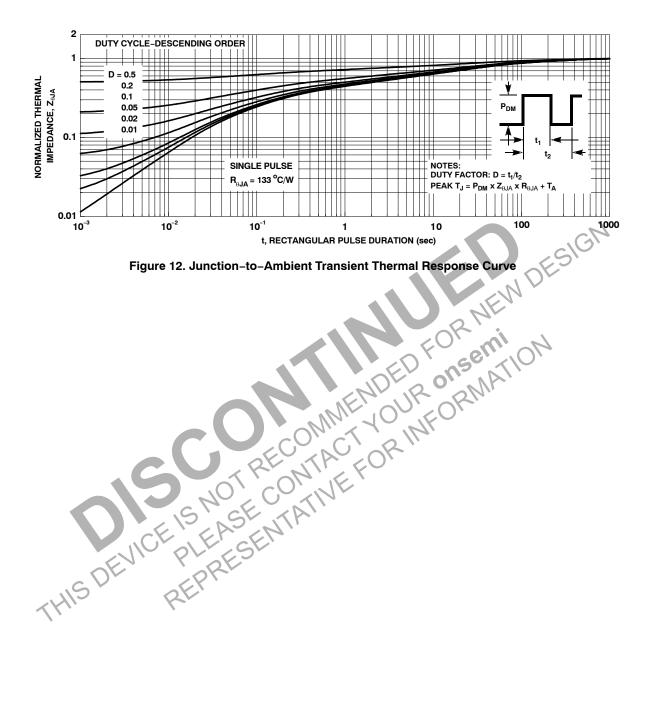


Figure 11. Single Pulse Maximum Power Dissipation

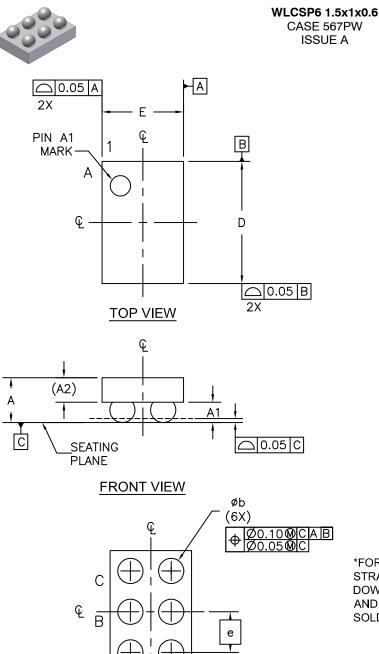
TYPICAL CHARACTERISTICS

(T_J = 25°C unless otherwise noted)



All other brand names and product names appearing in this document are registered trademarks or trademarks of their respective holders.

onsemi



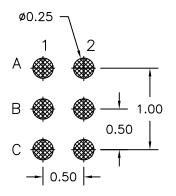
DATE 04 AUG 2021

NOTES: UNLESS OTHERWISE SPECIFIED

A) ALL DIMENSIONS ARE IN MILLIMETERS.
B) NO JEDEC REGISTRATION REFERENCE AS OF OCTOBER 2005.
C) DRAWING CONFORMS TO ASME

Y14.5M-2009

DIM	MILLIMETERS				
	MIN.	NOM.	MAX.		
Α	-	-	0.60		
A1	0.22	0.25	0.28		
A2	(0.30 REF			
b	0.24	0.31	0.39		
D	1.45	1.50	1.55		
E	0.95	1.00	1.05		
е	(0.50 BSC			



LAND PATTERN RECOMMENDATION

*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

DOCUMENT NUMBER:	98AON13306G	Electronic versions are uncontrolled except when accessed directly from the Document Reposit Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.			
DESCRIPTION:	WLCSP6 1.5x1x0.6		PAGE 1 OF 1		

onsemi and ONSEMI are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights of others.

Δ

е

BOTTOM VIEW

onsemi, ONSEMI, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at <u>www.onsemi.com/site/pdf/Patent_Marking.pdf</u>. onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or indental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification. Buyer shall indemnify and hold onsemi and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs,

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation onsemi Website: www.onsemi.com

ONLINE SUPPORT: <u>www.onsemi.com/support</u> For additional information, please contact your local Sales Representative at <u>www.onsemi.com/support/sales</u>