

# MOSFET – N-Channel, POWERTRENCH®

150 V, 15 A, 40 mΩ

## FDPF390N15A

### Description

This N-Channel MOSFET is produced using onsemi's advanced POWERTRENCH process that has been tailored to minimize the on-state resistance while maintaining superior switching performance.

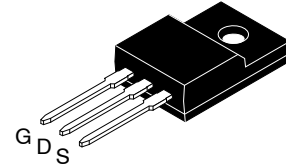
### Features

- $R_{DS(on)} = 31 \text{ m}\Omega$  (Typ.) @  $V_{GS} = 10 \text{ V}$ ,  $I_D = 15 \text{ A}$
- Fast Switching Speed
- Low Gate Charge,  $Q_G = 14.3 \text{ nC}$  (Typ.)
- High Performance Trench Technology for Extremely Low  $R_{DS(on)}$
- High Power and Current Handling Capability
- This Device is Pb-Free, Halide Free and RoHS Compliant

### Applications

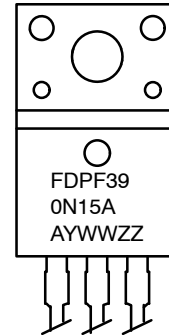
- Consumer Appliances
- LED TV
- Synchronous Rectification
- Uninterruptible Power Supply
- Motor Solar Inverter

$V_{DSS}$	$R_{DS(on)}$ MAX	$I_D$ MAX
150 V	40 mΩ @ 10 V	15 A



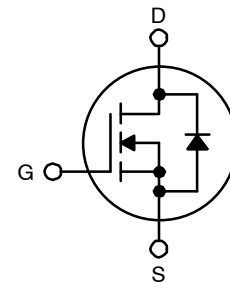
TO-220 Fullpack, 3-Lead / TO-220F-3SG  
CASE 221AT

### MARKING DIAGRAM



FDPF390N15A = Specific Device Code  
A = Assembly Location  
YWW = Date Code (Year & Week)  
ZZ = Assembly Lot

### N-CHANNEL MOSFET



### ORDERING INFORMATION

Part Number	Package	Shipping
FDPF390N15A	TO-220F	1000 Units / Tube

# FDPF390N15A

## MOSFET MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ , unless otherwise noted)

Symbol	Parameter		FDPF390N15A	Unit
$V_{DSS}$	Drain to Source Voltage		150	V
$V_{GSS}$	Gate to Source Voltage		$\pm 20$	V
$I_D$	Drain Current	– Continuous ( $T_C = 25^\circ\text{C}$ , Silicon Limited)	15	A
		– Continuous ( $T_C = 100^\circ\text{C}$ , Silicon Limited)	10	
$I_{DM}$	Drain Current	– Pulsed (Note 1)	60	A
$E_{AS}$	Single Pulsed Avalanche Energy (Note 2)		78	mJ
$dv/dt$	Peak Diode Recovery $dv/dt$ (Note 3)		6.0	V/ns
$P_D$	Power Dissipation	( $T_C = 25^\circ\text{C}$ )	22	W
		– Derate Above $25^\circ\text{C}$	0.18	W/ $^\circ\text{C}$
$T_J, T_{STG}$	Operating and Storage Temperature Range		$-55$ to $+175$	$^\circ\text{C}$
$T_L$	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Repetitive rating: pulse-width limited by maximum junction temperature.
2. Starting  $T_J = 25^\circ\text{C}$ ,  $L = 3\text{ mH}$ ,  $I_{SD} = 7.2\text{ A}$
3.  $I_{SD} \leq 15\text{ A}$ ,  $di/dt \leq 200\text{ A}/\mu\text{s}$ ,  $V_{DD} \leq BV_{DSS}$ , starting  $T_J = 25^\circ\text{C}$ .

## THERMAL CHARACTERISTICS

Symbol	Parameter	FDPF390N15A	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case, Max.	5.7	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

# FDPF390N15A

## ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
--------	-----------	-----------------	-----	-----	-----	------

### OFF CHARACTERISTICS

BV <sub>DSS</sub>	Drain to Source Breakdown Voltage	I <sub>D</sub> = 250 $\mu$ A, V <sub>GS</sub> = 0 V	150	–	–	V
$\Delta$ BV <sub>DSS</sub> / $\Delta$ T <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250 $\mu$ A, Referenced to 25°C	–	0.1	–	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 120 V, V <sub>GS</sub> = 0 V	–	–	1	$\mu$ A
		V <sub>DS</sub> = 120 V, T <sub>C</sub> = 125°C	–	–	500	
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>GS</sub> = $\pm$ 20 V, V <sub>DS</sub> = 0 V	–	–	$\pm$ 100	nA

### ON CHARACTERISTICS

V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> = V <sub>DS</sub> , I <sub>D</sub> = 250 $\mu$ A	2.0	–	4.0	V
R <sub>DS(on)</sub>	Static Drain to Source On Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A	–	31	40	m $\Omega$
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 15 A	–	32	–	S

### DYNAMIC CHARACTERISTICS

C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 75 V, V <sub>GS</sub> = 0 V, f = 1 MHz	–	965	1285	pF
C <sub>oss</sub>	Output Capacitance		–	96	130	pF
C <sub>rss</sub>	Reverse Transfer Capacitance		–	5.8	–	pF
C <sub>oss(er)</sub>	Energy Related Output Capacitance	V <sub>DS</sub> = 75 V, V <sub>GS</sub> = 0 V	–	169	–	pF
Q <sub>g(tot)</sub>	Total Gate Charge at 10 V	V <sub>DS</sub> = 75 V, I <sub>D</sub> = 27 A, V <sub>GS</sub> = 10 V (Note 4)	–	14.3	18.6	nC
Q <sub>gs</sub>	Gate to Source Gate Charge		–	5.0	–	nC
Q <sub>gs2</sub>	Gate Charge Threshold to Plateau		–	2.0	–	nC
Q <sub>gd</sub>	Gate to Drain “Miller” Charge		–	3.5	–	nC
ESR	Equivalent Series Resistance (G–S)	f = 1 MHz	–	1.4	–	$\Omega$

### SWITCHING CHARACTERISTICS

t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 75 V, I <sub>D</sub> = 27 A, V <sub>GS</sub> = 10 V, R <sub>G</sub> = 4.7 $\Omega$ (Note 4)	–	14	38	ns
t <sub>r</sub>	Turn-On Rise Time		–	10	30	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		–	20	50	ns
t <sub>f</sub>	Turn-Off Fall Time		–	5	20	ns

### DRAIN-SOURCE DIODE CHARACTERISTICS

I <sub>S</sub>	Maximum Continuous Drain to Source Diode Forward Current		–	–	15	A
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current		–	–	64	A
V <sub>SD</sub>	Drain to Source Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 15 A	–	–	1.25	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>SD</sub> = 27 A, dI <sub>F</sub> /dt = 100 A/μs	–	63	–	ns
Q <sub>rr</sub>	Reverse Recovery Charge		–	131	–	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics.

TYPICAL PERFORMANCE CHARACTERISTICS

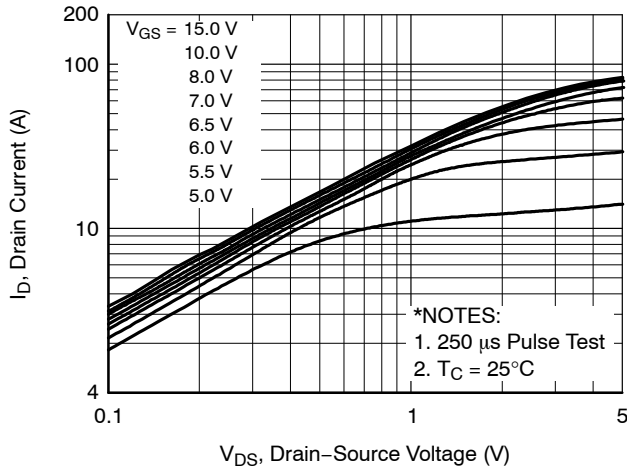


Figure 1. On-Region Characteristics

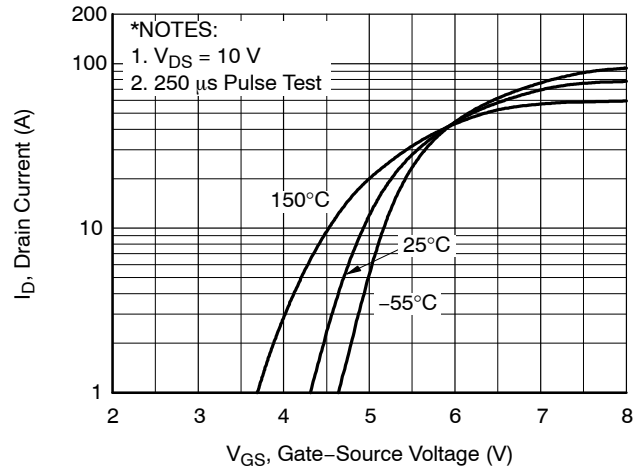


Figure 2. Transfer Characteristics

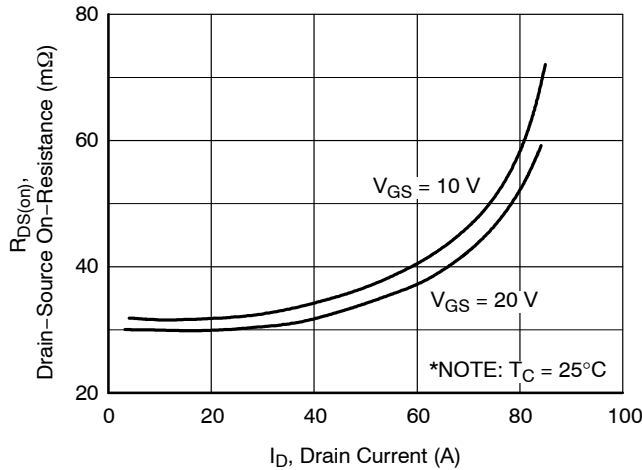


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

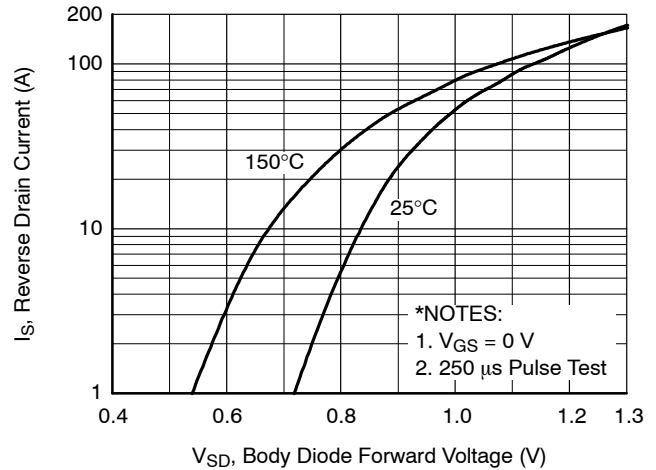


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

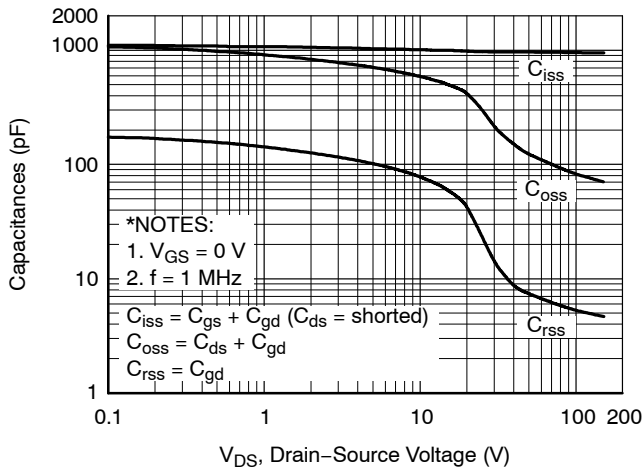


Figure 5. Capacitance Characteristics

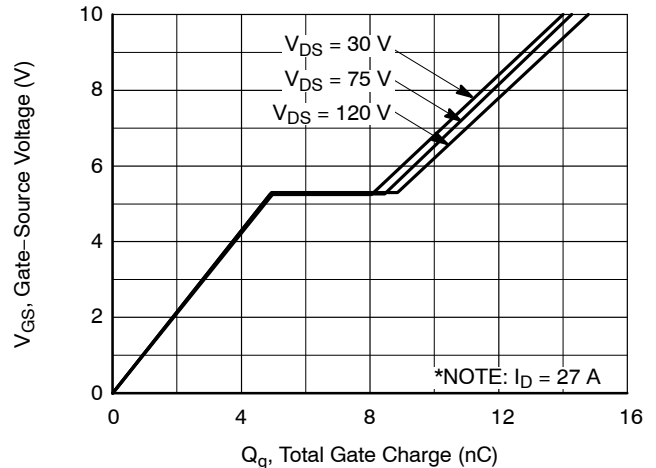
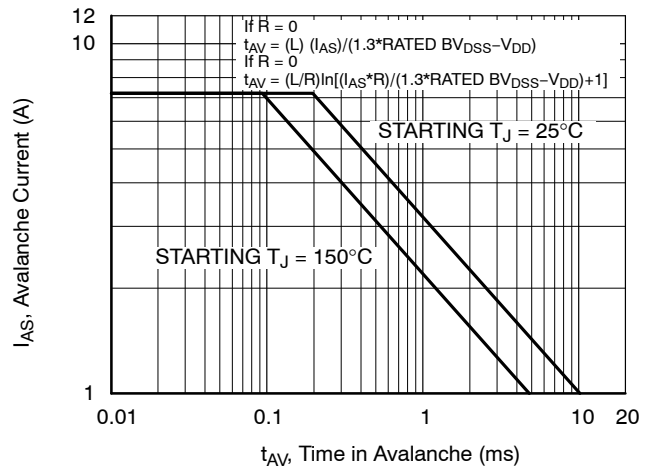
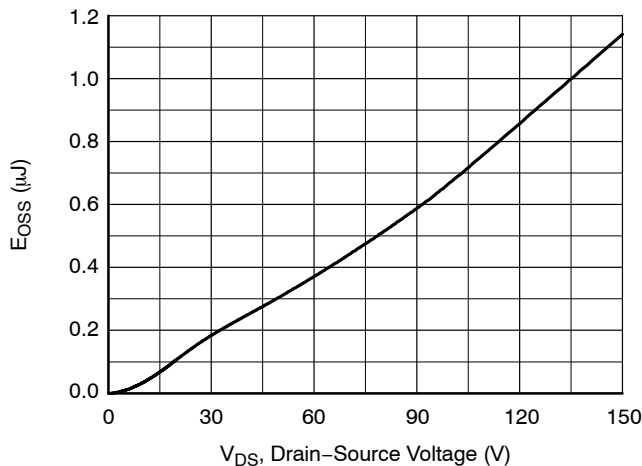
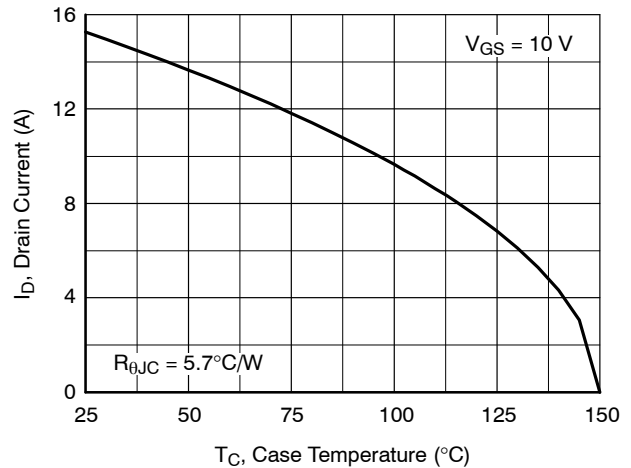
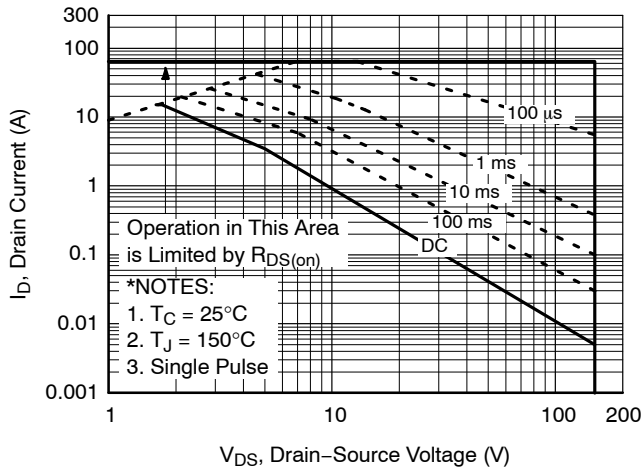
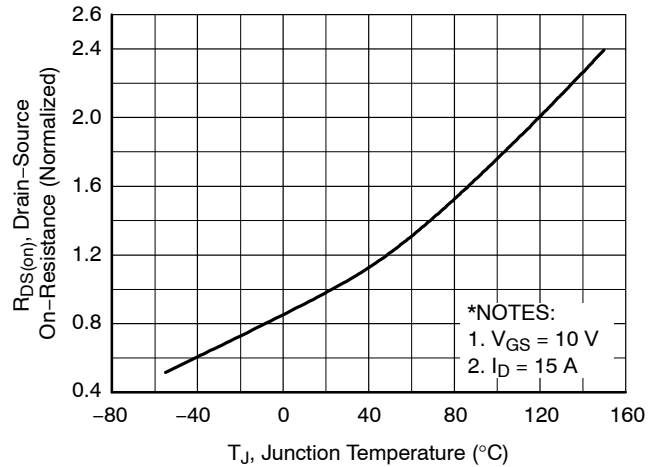
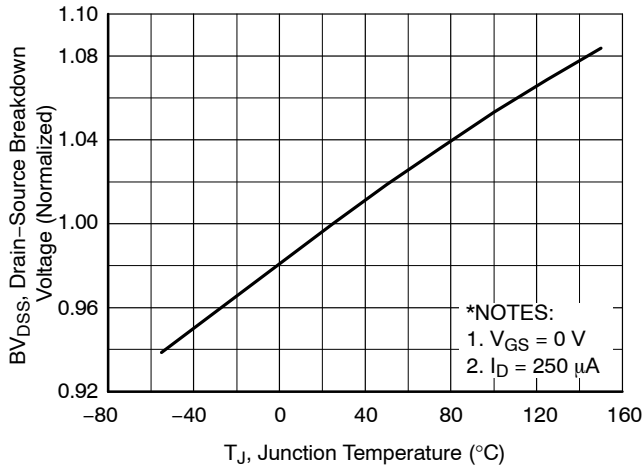


Figure 6. Gate Charge Characteristics

TYPICAL PERFORMANCE CHARACTERISTICS (continued)



TYPICAL PERFORMANCE CHARACTERISTICS (continued)

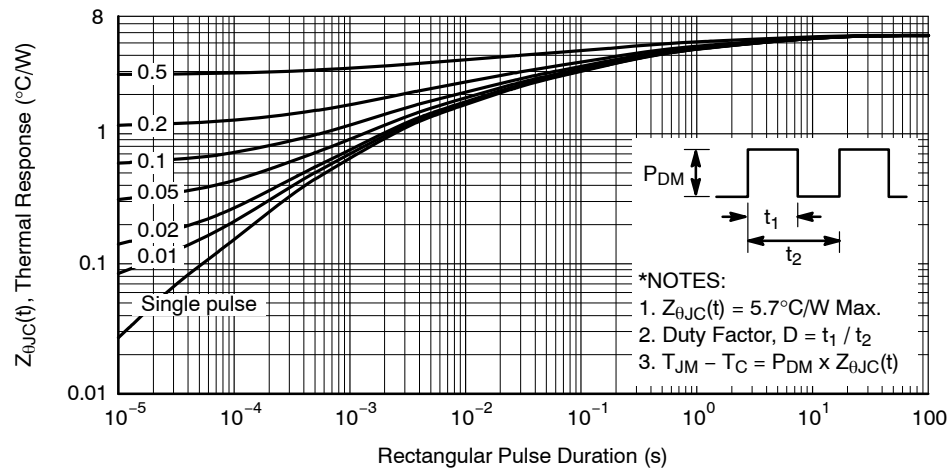


Figure 13. Transient Thermal Response Curve

# FDPF390N15A

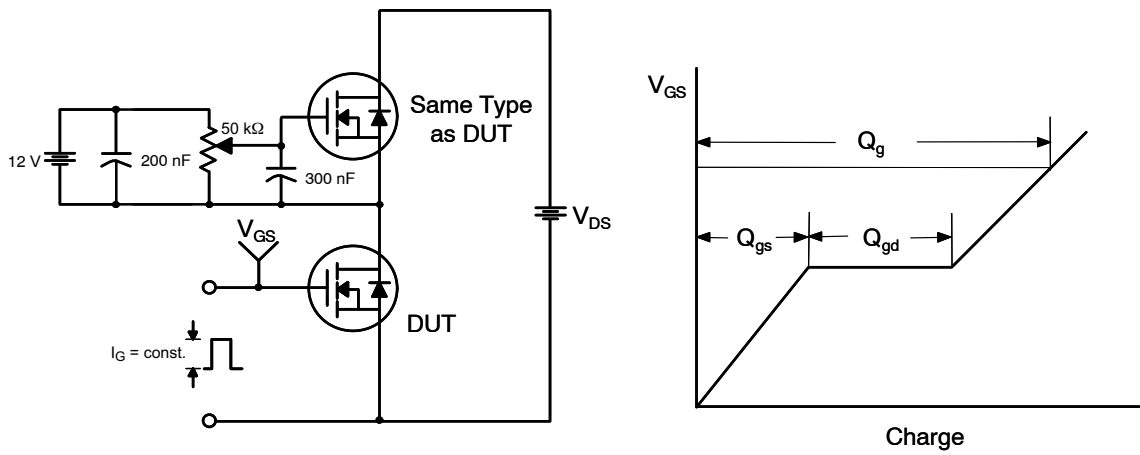


Figure 14. Gate Charge Test Circuit & Waveform

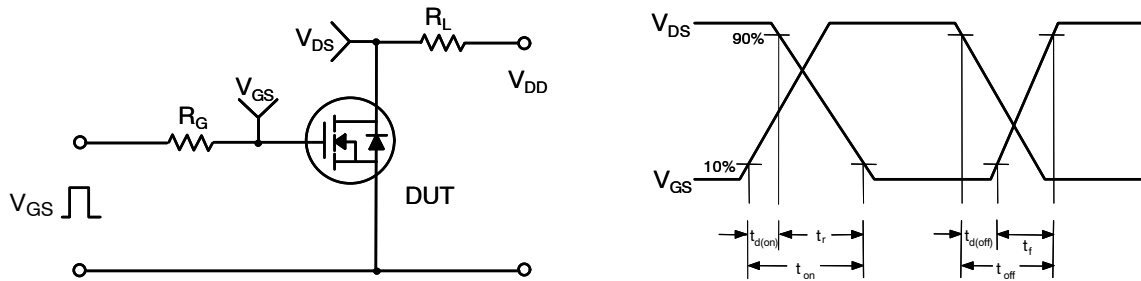


Figure 15. Resistive Switching Test Circuit & Waveforms

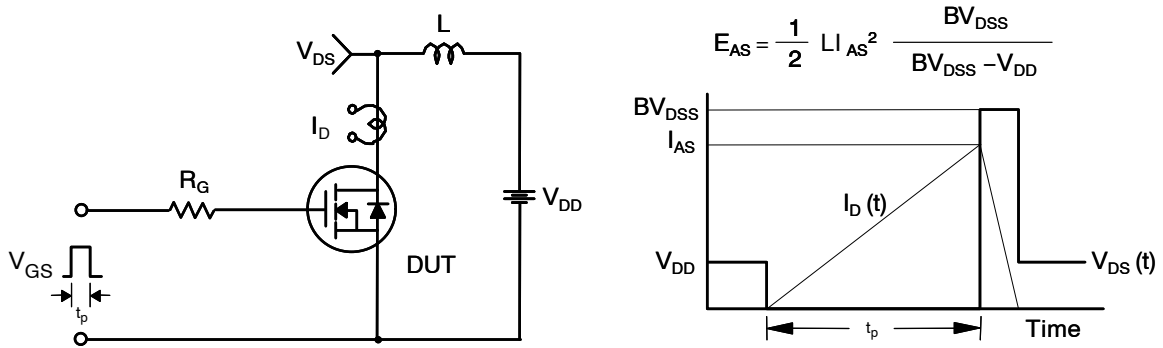
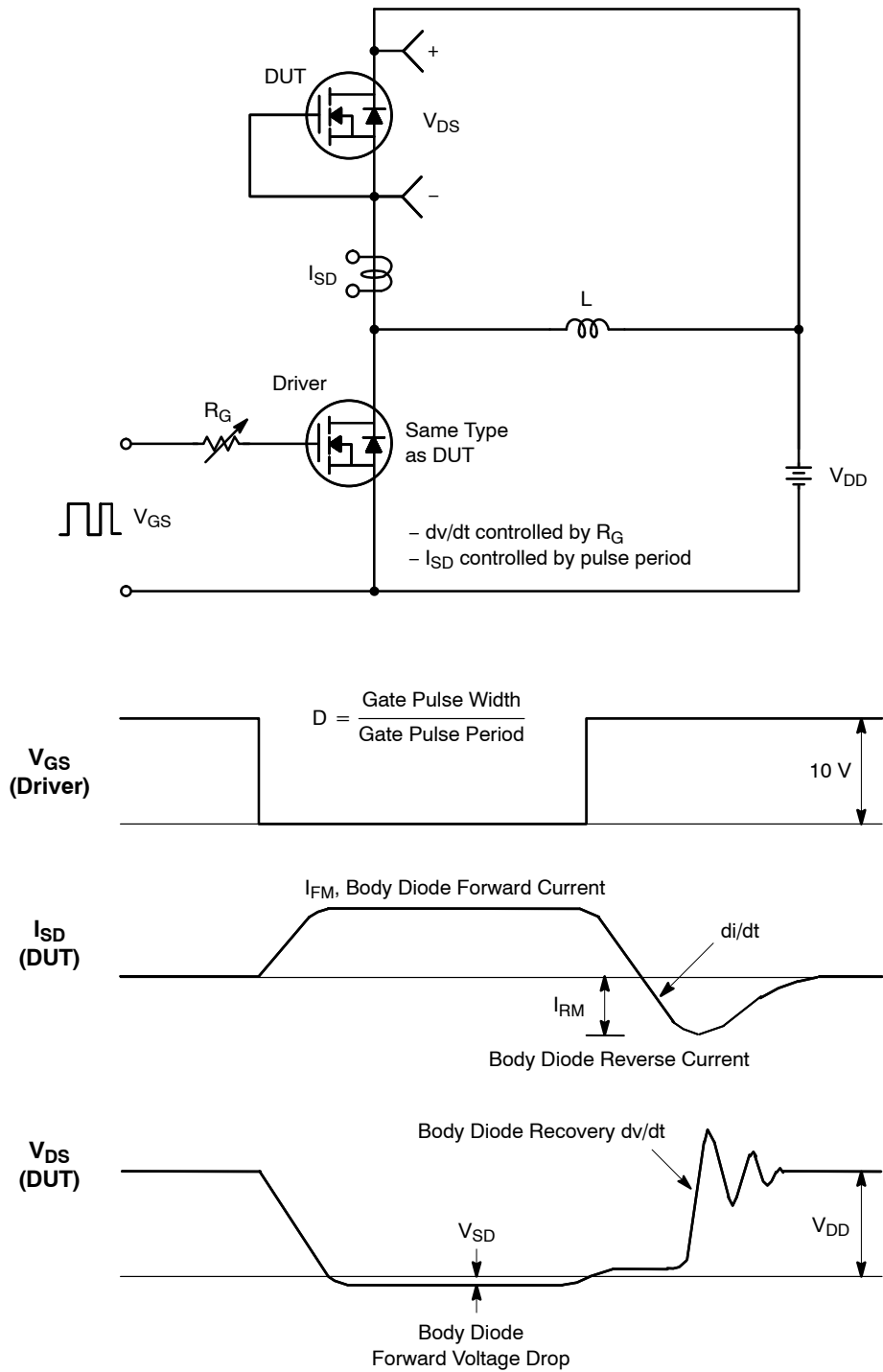


Figure 16. Unclamped Inductive Switching Test Circuit & Waveforms

## FDPF390N15A



**Figure 17. Peak Diode Recovery  $dv/dt$  Test Circuit & Waveforms**



**TO-220 Fullpack, 3-Lead / TO-220F-3SG**  
**CASE 221AT**  
**ISSUE B**

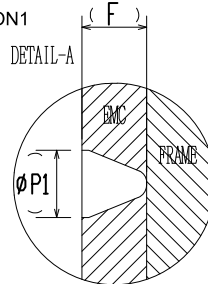
DATE 19 JAN 2021



Scale 1:1



OPTION1



DIM	MILLIMETERS		
	MIN	NOM	MAX
A	4.50	4.70	4.90
A1	2.56	2.76	2.96
A2	2.34	2.54	2.74
b	0.70	0.80	0.90
b2	~	~	1.47
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.60	15.80	16.00
E	9.96	10.16	10.36
e	2.34	2.54	2.74
F	~	0.84	~
H1	6.48	6.68	6.88
L	12.78	12.98	13.18
L1	3.03	3.23	3.43
Ø P	2.98	3.18	3.38
Ø P1	~	1.00	~
Q	3.20	3.30	3.40

**NOTES:**

A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009

B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCTIONS.

C. OPTION 1 - WITH SUPPORT PIN HOLE

OPTION 2 - NO SUPPORT PIN HOLE

<b>DOCUMENT NUMBER:</b>	<b>98AON67439E</b>	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
<b>DESCRIPTION:</b>	<b>TO-220 FULLPACK, 3-LEAD / TO-220F-3SG</b>	<b>PAGE 1 OF 1</b>

onsemi and onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at  
[www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)