onsemi

MOSFET – N-Channel, SUPERFET[®] II

600 V, 10 A, 400 m Ω

FCPF400N60

Description

SuperFET II MOSFET is **onsemi**'s brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. This technology is tailored to minimize conduction loss, provide superior switching performance, dv/dt rate and higher avalanche energy. Consequently, SuperFET II MOSFET is very suitable for the switching power applications such as PFC, server/telecom power, FPD TV power, ATX power and industrial power applications.

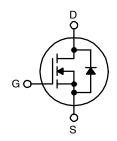
Features

- $650 \text{ V} @ \text{T}_{\text{J}} = 150^{\circ}\text{C}$
- Typ. $R_{DS(on)} = 350 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. $Q_g = 28 \text{ nC}$)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 90 pF)
- 100% Avalanche Tested
- RoHS Compliant

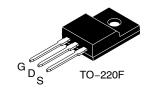
Applications

- LCD / LED / PDP TV Lighting
- Solar Inverter
- AC-DC Power Supply

V _{DSS}	R _{DS(ON)} MAX	I _D MAX	
600 V	400 m Ω @ 10 V	10 A	

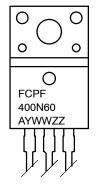


N-Channel MOSFET



TO-220 Fullpack, 3-Lead / TO-220F-3SG CASE 221AT

MARKING DIAGRAM



FCPF400N60= Specific Device Code

= Assembly Location

А

YWW	= Date Code (Year & Work Week)
ZZ	= Assembly Lot

ORDERING INFORMATION

Device	Package	Shipping
FCPF400N60	TO-220-3 (Pb-Free)	1000 Units / Tube

Symbol		FCPF400N60	Unit		
V _{DSS}	Drain to Source Voltage		600	V	
V _{GSS}	Gate to Source Voltage	– DC	±20	V	
		– AC (f > 1 Hz)	±30		
ID	Drain Current	– Continuous (T _C = 25°C)	10*	А	
		– Continuous (T _C = 100°C)	6.3*		
I _{DM}	Drain Current	- Pulsed (Note 1)	30*	Α	
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		211.6	mJ	
I _{AR}	Avalanche Current (Note 1)		2.3	А	
E _{AR}	Repetitive Avalanche Energy (Note 1)		1.06	mJ	
dv/dt	MOSFET dv/dt		100	V/ns	
	Peak Diode Recovery dv/dt (Note 3)		20	1	
PD	Power Dissipation	(T _C = 25°C)	31	W	
		– Derate above 25°C	0.25	W/°C	
T _J , T _{STG}	Operating and Storage Temperature Range		–55 to +150	°C	
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C	

ABSOLUTE MAXIMUM RATINGS (T_C = 25°C unless otherwise specified)

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected. *Drain current limited by maximum junction temperature.

1. Repetitive rating: pulse width limited by maximum junction temperature. 2. $I_{AS} = 2.3 \text{ A}, V_{DD} = 50 \text{ V}, R_G = 25 \Omega$, starting $T_J = 25^{\circ}\text{C}$ 3. $I_{SD} \le 5 \text{ A}, \text{ di/dt} \le 200\text{A}/\mu\text{s}, V_{DD} \le \text{BV}_{DSS}$, starting $T_J = 25^{\circ}\text{C}$

THERMAL CHARACTERISTICS

Symbol	Parameter	FCPF400N60	Unit
$R_{ extsf{ heta}JC}$	Thermal Resistance, Junction to Case, Max.	4.0	°C/W
$R_{ extsf{ heta}JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

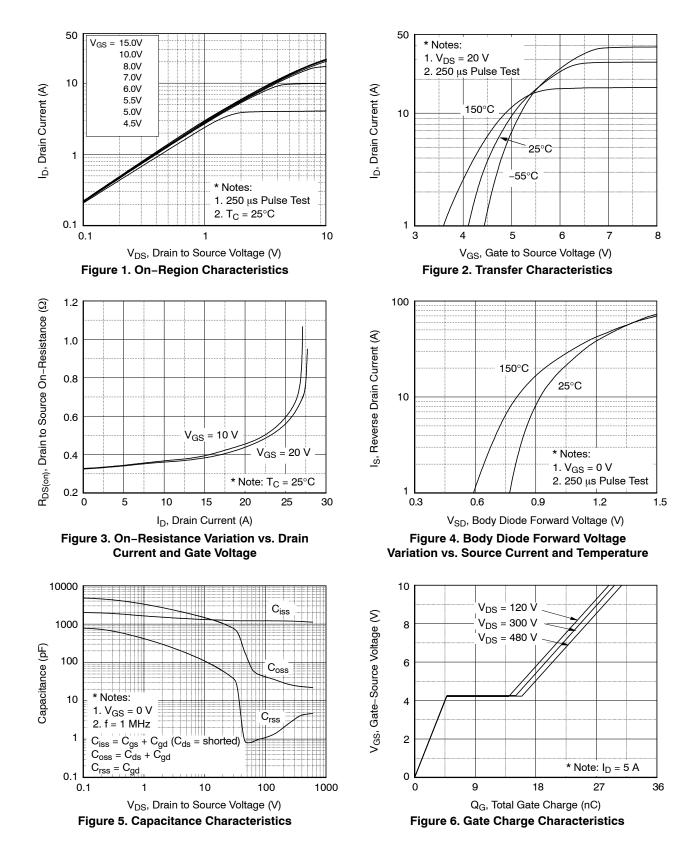
ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

DFF CHAR BV _{DSS} ΔBV _{DSS} / ΔΤ _J	ACTERISTICS					
ΔBV_{DSS}	Drain to Source Breakdown Voltage					
	Drain to Source Breakdown Voltage	V_{GS} = 0 V, I _D = 10 mA, T _J = 25°C	600	-	-	V
		V_{GS} = 0 V, I _D = 10 mA, T _J = 150°C	650	-	-	
,	Breakdown Voltage Temperature Coefficient	$I_D = 10$ mA, Referenced to 25°C	-	0.67	-	V/°C
BV _{DS}	Drain-Source Avalanche Breakdown Voltage	V _{GS} = 0 V, I _D = 10 A	-	700	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	1	μA
		V_{DS} = 480 V, T_{C} = 125°C	-	0.97	-	
I _{GSS}	Gate to Body Leakage Current	V_{GS} = ±20 V, V_{DS} = 0 V	-	-	±100	nA
ON CHARA	ACTERISTICS					
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 250 \ \mu A$	2.5	-	3.5	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 5 A	-	0.35	0.40	Ω
9 _{FS}	Forward Transconductance	$V_{DS} = 20 \text{ V}, I_{D} = 5 \text{ A}$	-	11	-	S
OYNAMIC (CHARACTERISTICS	·				
C _{iss}	Input Capacitance	V_{DS} = 25 V, V_{GS} = 0 V, f = 1 MHz	-	1180	1580	pF
C _{oss}	Output Capacitance		_	860	1144	pF
C _{rss}	Reverse Transfer Capacitance		_	43	54	pF
C _{oss}	Output Capacitance	V _{DS} = 380 V, V _{GS} = 0 V, f = 1 MHz	-	22	-	pF
C _{oss(eff.)}	Effective Output Capacitance	V_{DS} = 0 V to 480 V, V_{GS} = 0 V	-	90	-	pF
Q _{g(tot)}	Total Gate Charge at 10 V	V _{DS} = 380 V, I _D = 5 A, V _{GS} = 10 V (Note 4)	-	28	38	nC
Q _{gs}	Gate to Source Gate Charge		-	5	-	nC
Q _{gd}	Gate to Drain "Miller" Charge		-	10	-	nC
ESR	Equivalent Series Resistance	f = 1 MHz	-	1	-	Ω
SWITCHING	G CHARACTERISTICS	·				
t _{d(on)}	Turn–On Delay Time	$V_{DD} = 380 \text{ V}, \text{ I}_{D} = 5 \text{ A}, \text{ V}_{GS} = 10 \text{ V},$	-	13	37	ns
t _r	Turn-On Rise Time	R _G = 4.7 Ω (Note 4)	_	7	24	ns
t _{d(off)}	Turn-Off Delay Time		_	43	95	ns
t _f	Turn-Off Fall Time	1	-	6	21	ns
DRAIN-SO	URCE DIODE CHARACTERISTICS	•	•	•		
I _S	Maximum Continuous Drain to Source Diode Forward Current			-	10	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	-	30	Α
V _{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 5 A	-	-	1.2	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _{SD} = 5 A,	-	240	-	ns
Q _{rr}	Reverse Recovery Charge	$dI_F/dt = 100 \text{ A}/\mu \text{s}$	_	2.7	_	μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature typical characteristics

TYPICAL PERFORMANCE CHARACTERISTICS



TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

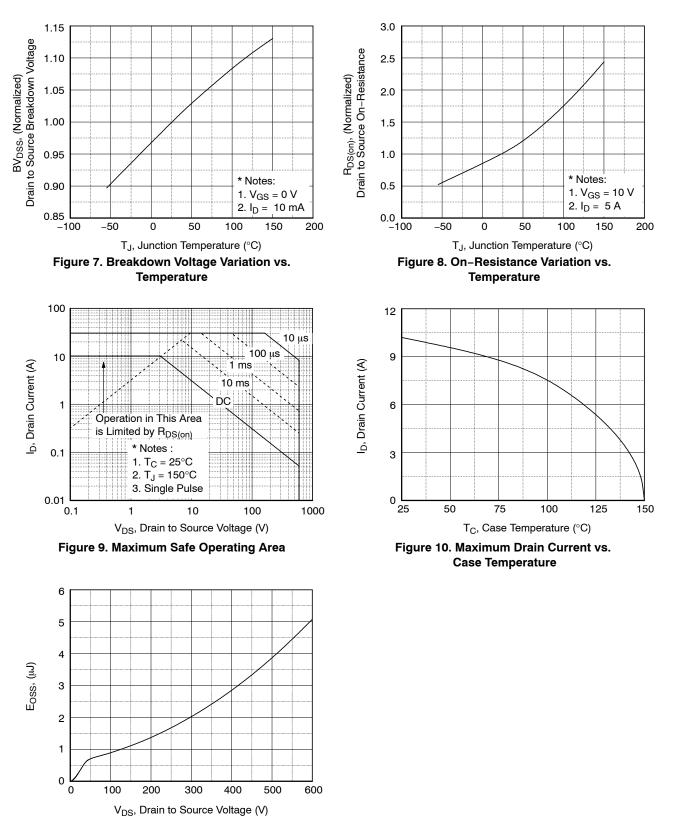


Figure 11. E_{oss} vs. Drain to Source Voltage

TYPICAL PERFORMANCE CHARACTERISTICS (Continued)

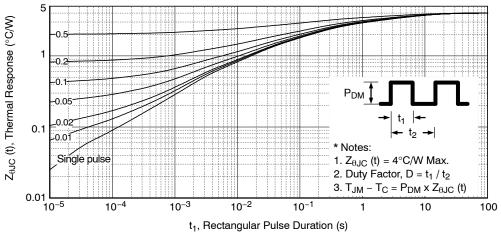
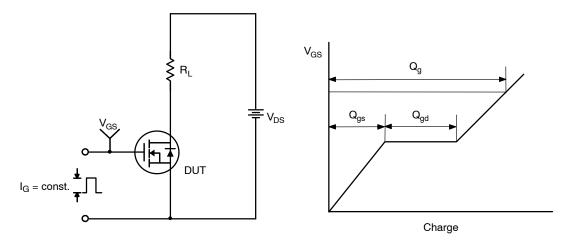


Figure 12. Transient Thermal Response Curve





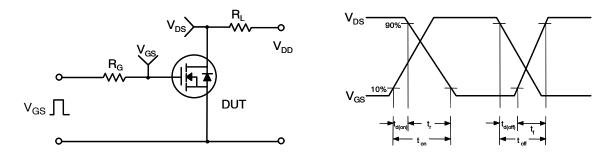


Figure 14. Resistive Switching Test Circuit & Waveforms

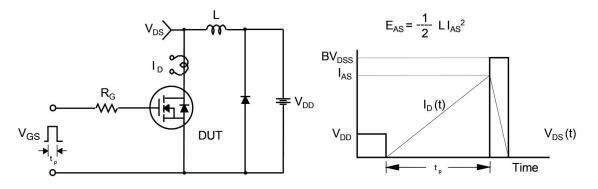


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

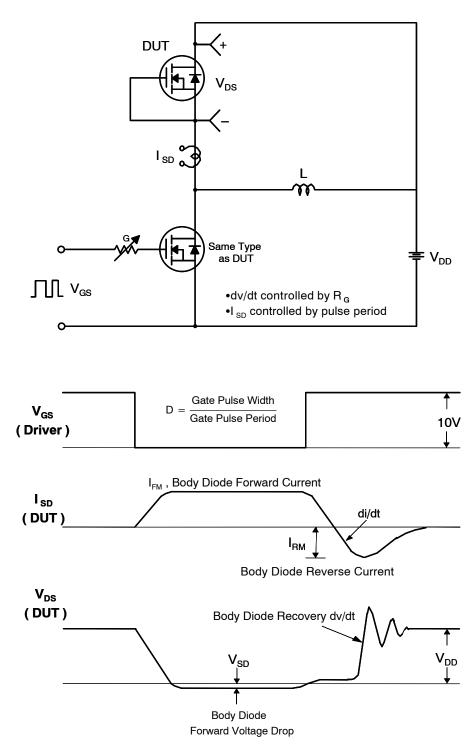
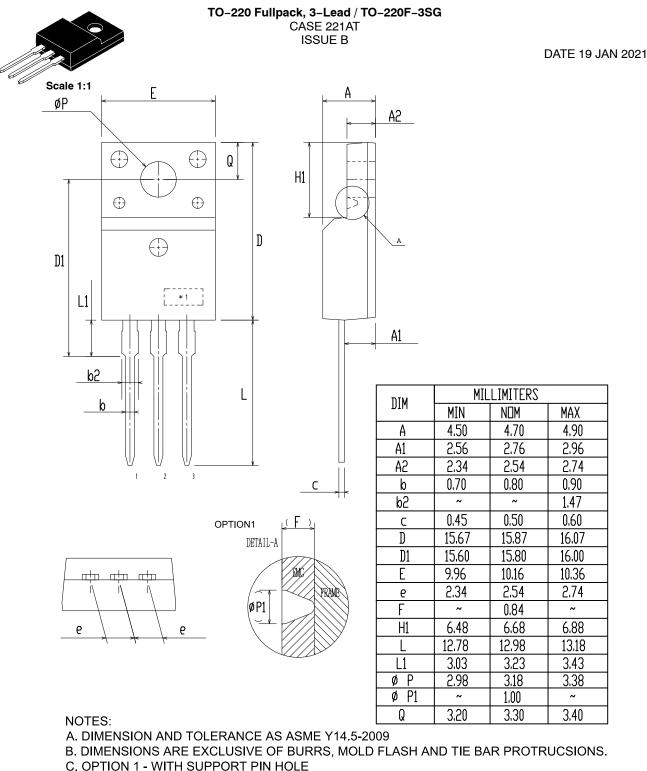


Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

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OPTION 2 - NO SUPPORT PIN HOLE

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DESCRIPTION:	TO-220 FULLPACK, 3-LEAD / TO-220F-3SG		PAGE 1 OF 1	

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