

MOSFET - N-Channel, SUPERFET® II, FRFET®

650 V, 24 A, 150 mΩ

FCPF150N65F

Description

SUPERFET II MOSFET is **onsemi**'s brand-new high voltage super-junction (SJ) MOSFET family that is utilizing charge balance technology for outstanding low on-resistance and lower gate charge performance. SUPERFET II FRFET MOSFET combines a faster and more rugged intrinsic body diode performance with fast switching, aimed at achieving better reliability and efficiency especially in resonant switching applications. SUPERFET II FRFET is very suitable for the switching power applications such as server/telecom power, Solar inverter, FPD TV power, computing power, lighting and industrial power applications.

Features

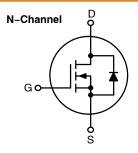
- Typ. $R_{DS(on)} = 133 \text{ m}\Omega$
- Ultra Low Gate Charge (Typ. Q_g = 72 nC)
- Low Effective Output Capacitance (Typ. Coss(eff.) = 361 pF)
- 100% Avalanche Tested
- RoHS Compliant

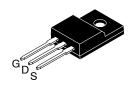
Applications

- Telecom / Server Power Supplies
- Solar Inverters
- Computing Power Supplies
- FPD TV Power / Lighting

V _{DS}	R _{DS(ON)} MAX	I _D MAX	
650 V	150 mΩ @ 10 V	24 A*	

^{*}Drain current limited by maximum junction temperature.





TO-220 Fullpack, 3-Lead / TO-220F-3SG CASE 221AT

MARKING DIAGRAM



FCPF150N65F = Device Code
A = Assembly Location
YWW = Date Code (Year & Week)
ZZ = Assembly Lot

ORDERING INFORMATION

Device	Package	Shipping
FCPF150N65F	TO-220F	1000 Units / Tube

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ABSOLUTE MAXIMUM RATINGS ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Paramete	Value	Unit	
V _{DSS}	Drain to Source Voltage		650	V
V_{GSS}	Gate to Source Voltage	-DC	±20	V
		-AC (f > 1 Hz)	±30	
I _D	Drain Current	– Continuous (T _C = 25°C)	24*	Α
		– Continuous (T _C = 100°C)	14.9*	
I _{DM}	Drain Current	- Pulsed (Note 1)	72*	Α
E _{AS}	Single Pulsed Avalanche Energy (Note 2)		663	mJ
I _{AR}	Avalanche Current (Note 1)		4.7	Α
E _{AR}	Repetitive Avalanche Energy (Note 1)		2.98	mJ
dv/dt	MOSFET dv/dt		100	V/ns
	Peak Diode Recovery dv/dt (Note 3)		50	
P_{D}	Power Dissipation	(T _C = 25°C)	39	W
		-Derate above 25°C	0.31	W/°C
T _J , T _{STG}	Operating and Storage Temperature Range		-55 to +150	°C
TL	Maximum Lead Temperature for Soldering, 1/8" from Case for 5 Seconds		300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
*Drain current limited by maximum junction temperature.

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
$R_{ heta JC}$	Thermal Resistance, Junction to Case, Max.	3.2	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, Max.	62.5	

^{1.} Repetitive rating: pulse–width limited by maximum junction temperature. 2. $I_{AS} = 4.7 \text{ A}$, $R_G = 25 \Omega$, starting $T_J = 25^{\circ}\text{C}$. 3. $I_{SD} \le 12 \text{ A}$, $\text{di/dt} \le 200 \text{ A/µs}$, $V_{DD} \le 380 \text{ V}$, starting $T_J = 25^{\circ}\text{C}$.

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
OFF CHAR	ACTERISTICS				-	
BV _{DSS}	Drain to Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 10 \text{ mA}, T_J = 25^{\circ}\text{C}$	650	_	_	V
		V _{GS} = 0 V, I _D = 10 mA, T _J = 150°C	700	_	_	
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	I _D = 10 mA, referenced to 25°C	-	0.72	-	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V	_	_	10	μΑ
200		V _{DS} = 520 V, V _{GS} = 0 V, T _C = 125°C	_	86	_	<u>'</u>
I _{GSS}	Gate to Body Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA
	CTERISTICS	. 50		1	l	
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 2.4$ mA	3	_	5	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10 V, I _D = 12 A	_	133	150	mΩ
9 _{FS}	Forward Transconductance	V _{DS} = 20 V, I _D = 12 A	-	22	_	S
DYNAMIC (CHARACTERISTICS			1		<u>. </u>
C _{iss}	Input Capacitance	V _{DS} = 100 V, V _{GS} = 0 V, f = 1 MHz	-	2810	3737	pF
C _{oss}	Output Capacitance		-	91	121	pF
C _{rss}	Reverse Transfer Capacitance	1	-	0.77	_	pF
C _{oss}	Output Capacitance	V _{DS} = 380 V, V _{GS} = 0 V, f = 1 MHz	-	54	_	pF
C _{oss(eff.)}	Effective Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V	-	361	_	pF
Q _{g(tot)}	Total Gate Charge at 10 V	V _{DS} = 380 V, I _D = 12 A, V _{GS} = 10 V	-	72	94	nC
Q _{gs}	Gate to Source Gate Charge	(Note 4)	-	15	_	nC
Q _{gd}	Gate to Drain "Miller" Charge	1	-	31	_	nC
ESR	Equivalent Series Resistance	f = 1 MHz	i	0.69	_	Ω
SWITCHING	G CHARACTERISTICS					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 380 \text{ V}, I_D = 12 \text{ A}, V_{GS} = 10 \text{ V},$	-	28	66	ns
t _r	Turn-On Rise Time	$R_G = 4.7 \Omega$ (Note 4)	-	15	40	ns
t _{d(off)}	Turn-Off Delay Time	1	-	73	156	ns
t _f	Turn-Off Fall Time	1	-	6	22	ns
DRAIN-SO	URCE DIODE CHARACTERISTICS					
IS	Maximum Continuous Drain to Source Diode Forward Current		-	_	24	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		-	_	72	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0 V, I _{SD} = 12 A	-	_	1.4	V
t _{rr}	Reverse Recovery Time	$V_{GS} = 0 \text{ V}, I_{SD} = 12 \text{ A}, dI_F/dt = 100 \text{ A}/\mu\text{s}$	-	123	_	ns
Q _{rr}	Reverse Recovery Charge	1	_	597	_	nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Essentially independent of operating temperature.

TYPICAL PERFORMANCE CHARACTERISTICS

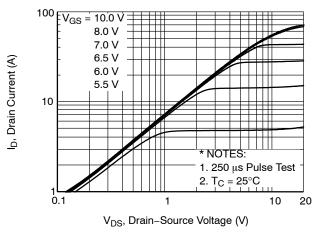


Figure 1. On-Region Characteristics

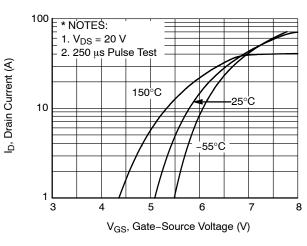


Figure 2. Transfer Characteristics

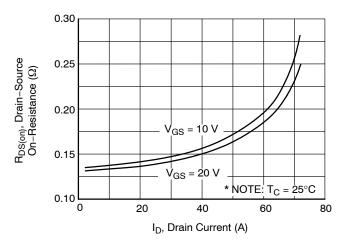


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

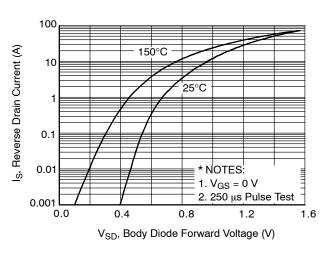


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

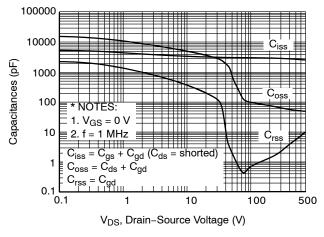


Figure 5. Capacitance Characteristics

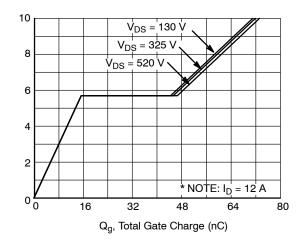


Figure 6. Gate Charge Characteristics

V_{GS}, Gate-Source Voltage (V)

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

R_{DS(on)}, (Normalized) Drain-Source On-Resistance

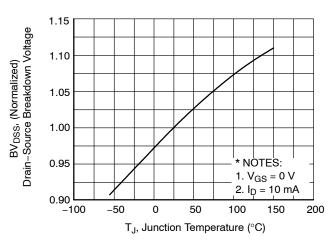


Figure 7. Breakdown Voltage Variation vs. Temperature

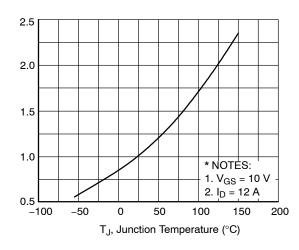


Figure 8. On-Resistance Variation vs. Temperature

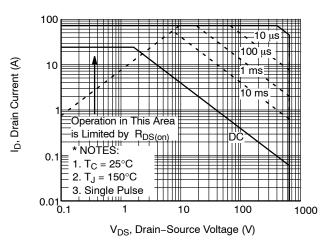


Figure 9. Maximum Safe Operating Area

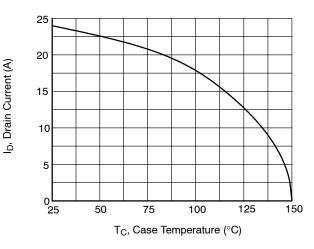


Figure 10. Maximum Drain Current vs. Case Temperature

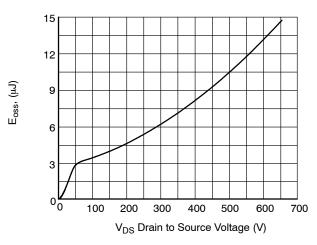


Figure 11. E_{OSS} vs. Drain to Source Voltage

TYPICAL PERFORMANCE CHARACTERISTICS (continued)

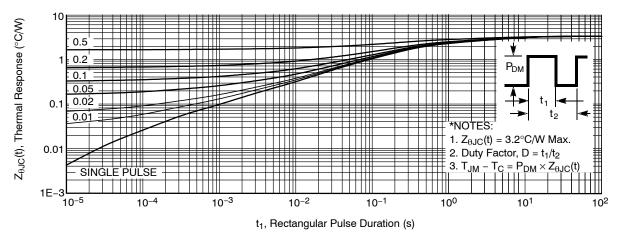


Figure 12. Transient Thermal Response Curve

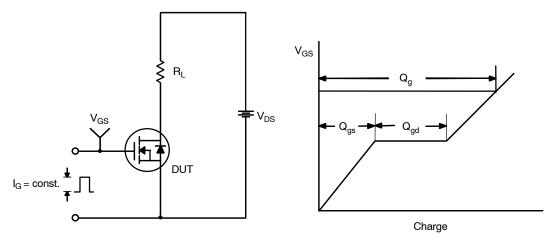


Figure 13. Gate Charge Test Circuit & Waveform

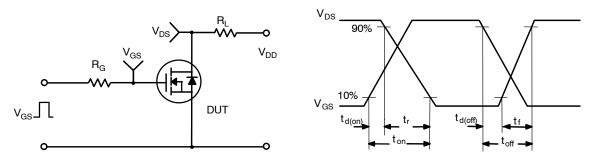


Figure 14. Resistive Switching Test Circuit & Waveforms

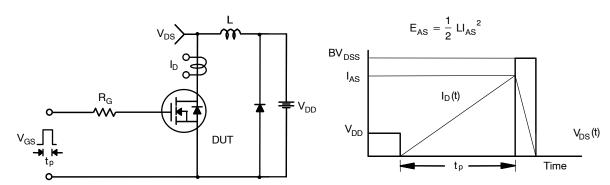


Figure 15. Unclamped Inductive Switching Test Circuit & Waveforms

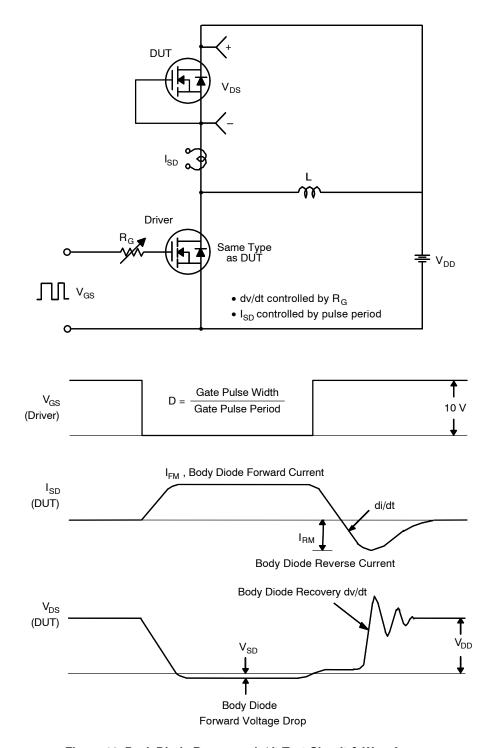
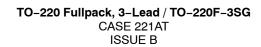


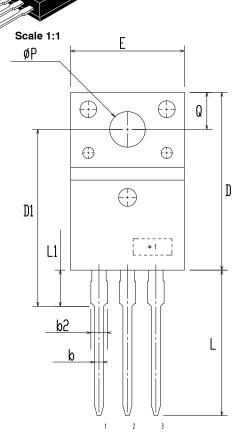
Figure 16. Peak Diode Recovery dv/dt Test Circuit & Waveforms

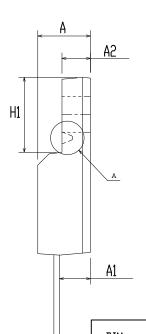
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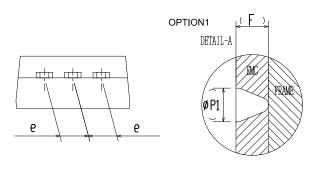




DATE 19 JAN 2021







DIM	HILLIHITENS			
ויונע	MIN	NDM	MAX	
Α	4.50	4.70	4.90	
A1	2.56	2.76	2.96	
A2	2.34	2.54	2.74	
b	0.70	0.80	0.90	
b2	~	2	1.47	
С	0.45	0.50	0.60	
D	15.67	15.87	16.07	
D1	15.60	15.80	16.00	
E	9.96	10.16	10.36	
е	2.34	2.54	2.74	
F	~	0.84	~	
H1	6.48	6.68	6.88	
L	12.78	12.98	13.18	
L1	3.03	3.23	3.43	
øΡ	2.98	3.18	3.38	
ø P1	~	1.00	~	
Q	3.20	3.30	3.40	

MILL IMITERS

NOTES:

- A. DIMENSION AND TOLERANCE AS ASME Y14.5-2009
- B. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUCSIONS.

C

C. OPTION 1 - WITH SUPPORT PIN HOLE OPTION 2 - NO SUPPORT PIN HOLE

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