ESD Protection Diodes

Micro-packaged Diodes for ESD Protection

The ESD5102 is designed to protect voltage sensitive components from ESD. Excellent clamping capability, low leakage, and fast response time provide best in class protection on designs that are exposed to ESD. Because of its small size, it is suited for use in smartphone, smart-watch, or many other portable / wearable applications where board space comes at a premium.

Features

- Bi-directional Dual Line ESD Protection
- Low Capacitance (5 pF Max, I/O to GND)
- Small Body Outline Dimensions: 0.705 x 0.23 mm
- Protection for the Following IEC Standards: IEC 61000-4-2 (Level 4)
- Low ESD Clamping Voltage
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

| Rating | Symbol | Value | Unit |
|--|------------------|-------------|----------|
| Operating Junction Temperature Range | Tj | -55 to +125 | °C |
| Storage Temperature Range | T _{stg} | -55 to +150 | °C |
| Lead Solder Temperature - Maximum (10 Seconds) | E | 260 | °C |
| IEC 61000-4-2 Contact (ESD) IEC 61000-4-2 Air (ESD) | ESD ESD | ±15 ±15 | kV kV |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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MARKING DIAGRAM



PIN CONFIGURATION AND SCHEMATIC

ORDERING INFORMATION

| Device | Package | Shipping [†] |
|--------------|-------------------|-------------------------|
| ESD5102FCT5G | DSN3 (Pb-Free) | 10,000 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

See Application Note AND8308/D for further description of survivability specs.

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ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise specified)

| Parameter | Symbol | Conditions | Min | Тур | Max | Unit |
|----------------------------------|------------------|---|------|-----|-----|------|
| Reverse Working Voltage | V _{RWM} | I/O Pin to GND | | | 3.3 | V |
| Breakdown Voltage | V _{BR} | I _T = 1 mA, I/O Pin to GND | 3.68 | 5 | 6.5 | V |
| Reverse Leakage Current | ۱ _R | V _{RWM} = 3.3 V, I/O Pin to GND | | | 0.1 | μA |
| Clamping Voltage TLP (Note 1) | V _C | $I_{PP} = 8 A $ Bec 61000-4-2 Level 2 equivalent (±4 kV Contact, ±4 kV Air) | | 5.4 | | V |
| | | $I_{PP} = 16 A \\ \left\{ \begin{array}{l} \text{IEC } 61000 - 4 - 2 \text{ Level } 2 \text{ equivalent} \\ (\pm 8 \text{ kV Contact}, \pm 15 \text{ kV Air}) \end{array} \right\}$ | | 6.5 | | |
| Junction Capacitance | CJ | $V_{R} = 0 V$, f = 1 MHz | | | 5.5 | pF |

1. ANSI/ESD STM5.5.1 – Electrostatic Discharge Sensitivity Testing using Transmission Line Pulse (TLP) Model. TLP conditions: $Z_0 = 50 \ \Omega$, $t_p = 100 \ ns$, $t_r = 4 \ ns$, averaging window; $t_1 = 30 \ ns$ to $t_2 = 60 \ ns$.



ESD5102

TYPICAL CHARACTERISTICS





IEC 61000-4-2 Spec.

| Level | Test Volt- age (kV) | First Peak Current (A) | Current at 30 ns (A) | Current at 60 ns (A) |
|-------|------------------------|------------------------------|-------------------------|-------------------------|
| 1 | 2 | 7.5 | 4 | 2 |
| 2 | 4 | 15 | 8 | 4 |
| 3 | 6 | 22.5 | 12 | 6 |
| 4 | 8 | 30 | 16 | 8 |



Figure 8. IEC61000-4-2 Spec

Transmission Line Pulse (TLP) Measurement

Transmission Line Pulse (TLP) provides current versus voltage (I–V) curves in which each data point is obtained from a 100 ns long rectangular pulse from a charged transmission line. A simplified schematic of a typical TLP system is shown in Figure 9. TLP I–V curves of ESD protection devices accurately demonstrate the product's ESD capability because the 10s of amps current levels and under 100 ns time scale match those of an ESD event. This is illustrated in Figure 10 where an 8 kV IEC 61000–4–2 current waveform is compared with TLP current pulses at 8 A and 16 A. A TLP I–V curve shows the voltage at which the device turns on as well as how well the device clamps voltage over a range of current levels.





Figure 10. Comparison Between 8 kV IEC 61000-4-2 and 8 A and 16 A TLP Waveforms





Techniques Reference Manual, <u>SOLDERRM/D</u>.

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