

MOSFET – Power, P-Channel, Dual ECH8

-20 V, -5 A, 38 m Ω

ECH8654

Features

- Low ON-resistance
- 1.8 V Drive
- Halogen Free Compliance
- Protection Diode in

ABSOLUTE MAXIMUM RATINGS (at Ta = 25°C)

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DSS}		-20	V
Gate-to-Source Voltage	V _{GSS}		±10	٧
Drain Current (DC)	I _D		-5	Α
Drain Current (Pulse)	I _{DP}	PW ≤ 10 μs, duty cycle ≤ 1%	-40	Α
Allowable Power Dissipation	P _D	When mounted on ceramic substrate (900 mm ² × 0.8 mm) 1 unit	1.3	W
Total Power Dissipation	P _T	When mounted on ceramic substrate (900 mm² × 0.8 mm)	1.5	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		–55 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1

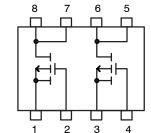


SOT-28FL / ECH8 CASE 318BF

MARKING DIAGRAM



ELECTRICAL CONNECTION



- 1: Source 1
- 2: Gate 1
- 3: Source 2
- 4: Gate 2
- 5: Drain 2
- 6: Drain 2
- 7: Drain 1 8: Drain 1
- ORDERING INFORMATION

Device	Package Shippi	
ECH8654-TL-H	SOT-28FL / ECH8 (Pb-Free and Halide Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ECH8654

ELECTRICAL CHARACTERISTICS (at Ta = 25°C)

			Ratings			
Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$I_D = -1 \text{ mA}, V_{GS} = 0 \text{ V}$	-20	-	_	V
Zero-Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	-1	μΑ
Gate-to-Source Leakage Current	I _{GSS}	$V_{GS} = \pm 8 \text{ V}, V_{DS} = 0 \text{ V}$	-	-	±10	μΑ
Cutoff Voltage	V _{GS} (off)	V _{DS} = -10 V, I _D = -1 mA	-0.4	-	-1.3	V
Forward Transfer Admittance	yfs	$V_{DS} = -10 \text{ V}, I_D = -3 \text{ A}$	4.9	8.3	-	S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	$I_D = -3 \text{ A}, V_{GS} = -4.5 \text{ V}$	-	29	38	mΩ
	R _{DS} (on)2	$I_D = -1.5 \text{ A}, V_{GS} = -2.5 \text{ V}$	-	41	58	mΩ
	R _{DS} (on)3	$I_D = -0.5 \text{ A}, V_{GS} = -1.8 \text{ V}$	-	64	98	mΩ
Input Capacitance	Ciss	V _{DS} = -10 V, f = 1 MHz	-	960	-	pF
Output Capacitance	Coss		-	180	-	pF
Reverse Transfer Capacitance	Crss	1	-	140	-	pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.	-	14	-	ns
Rise Time	t _r		-	55	-	ns
Turn-OFF Delay Time	t _d (off)		-	92	-	ns
Fall Time	t _f	1	-	68	-	ns
Total Gate Charge	Qg	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V},$ $I_{D} = -5 \text{ A}$	-	11	_	nC
Gate-to-Source Charge	Qgs		-	2.0	-	nC
Gate-to-Drain "Miller" Charge	Qgd	1	-	2.8	-	nC
Diode Forward Voltage	V _{SD}	I _S = -5 A, V _{GS} = 0 V	_	-0.82	-1.2	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Switching Time Test Circuit

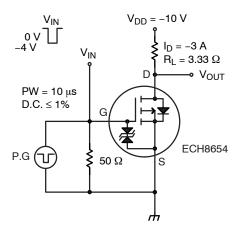
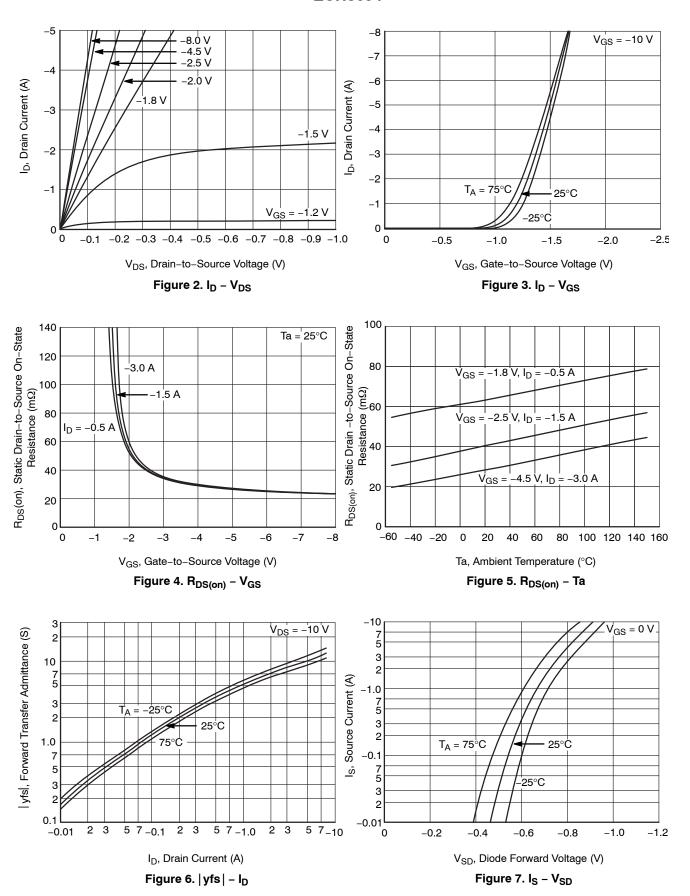


Figure 1. Switching Time Test Circuit

ECH8654



ECH8654

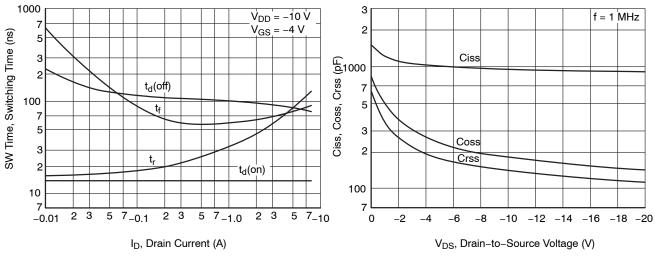


Figure 8. SW Time - I_D

Figure 9. Ciss, Coss, Crss - V_{DS}

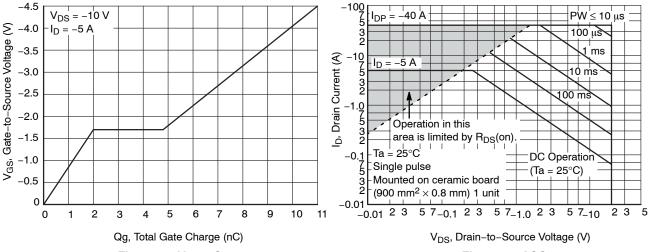


Figure 10. V_{GS} – Qg

Figure 11. ASO

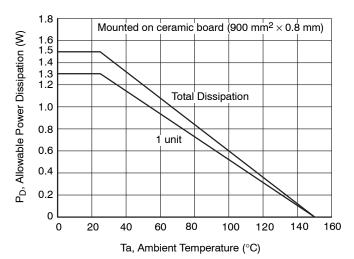
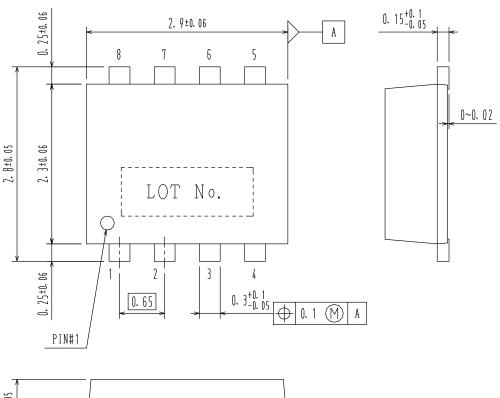
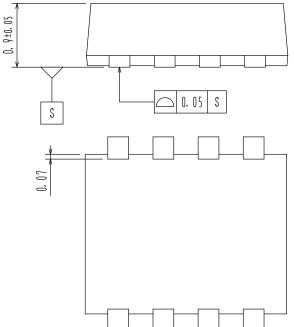


Figure 12. P_D - Ta

SOT-28FL / ECH8 CASE 318BF ISSUE O

DATE 31 MAR 2012





DOCUMENT NUMBER:	98AON78700E	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.		
DESCRIPTION:	SOT-28FL / ECH8		PAGE 1 OF 1	

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.

onsemi, Onsemi, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries. onsemi owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of onsemi's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. Onsemi reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and onsemi makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using onsemi products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by onsemi. "Typical" parameters which may be provided in onsemi data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. onsemi does not convey any license under any of its intellectual property rights nor the rights of others. onsemi products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA class 3 medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

 $\textbf{Technical Library:} \ \underline{www.onsemi.com/design/resources/technical-documentation}$

onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at

www.onsemi.com/support/sales