

Dual Switching Diode BAW56TT1G, SBAW56TT1G

Features

- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_A = 25°C)

Rating	Symbol	Max	Unit
Reverse Voltage	V _R	70	Vdc
Forward Current	I _F	200	mAdc
Peak Forward Surge Current	I _{FM(surge)}	500	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation, FR-4 Board (Note 1), T _A = 25°C Derated above 25°C	P _D	225 1.8	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	555	°C/W
Total Device Dissipation, FR-4 Board (Note 2), T _A = 25°C Derated above 25°C	P _D	360 2.9	mW mW/°C
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{\theta JA}$	345	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	-55 to +150	°C

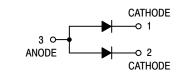
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1

- 1. FR-4 @ Minimum Pad
- 2. FR-4 @ 1.0 × 1.0 Inch Pad



CASE 463 SC-75/SOT-416 STYLE 4



MARKING DIAGRAM



A1 = Specific Device Code
M = Date Code*

= Date Code*= Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation may vary depending upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
BAW56TT1G	SC-75/SOT-416 (Pb-Free)	3,000 / Tape & Reel
SBAW56TT1G	SC-75/SOT-416 (Pb-Free)	3,000 / Tape & Reel

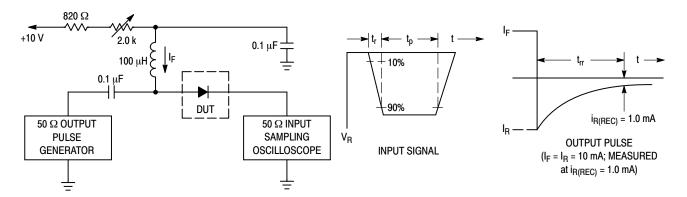
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

BAW56TT1G, SBAW56TT1G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				•
Reverse Breakdown Voltage (I _(BR) = 100 μAdc)	V _(BR)	70	-	Vdc
Reverse Voltage Leakage Current $(V_R = 25 \text{ Vdc}, T_J = 150^{\circ}\text{C})$ $(V_R = 70 \text{ Vdc})$ $(V_R = 70 \text{ Vdc}, T_J = 150^{\circ}\text{C})$	I _R	- - -	30 2.5 50	μAdc
Diode Capacitance (V _R = 0, f = 1.0 MHz)	C _D	_	2.0	pF
Forward Voltage (I _F = 1.0 mAdc) (I _F = 10 mAdc) (I _F = 50 mAdc) (I _F = 150 mAdc)	V _F	- - - -	715 855 1000 1250	mVdc
Reverse Recovery Time (I _F = I _R = 10 mAdc, R _L = 100 Ω , I _{R(REC)} = 1.0 mAdc) (Figure 1)	t _{rr}	-	6.0	ns

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



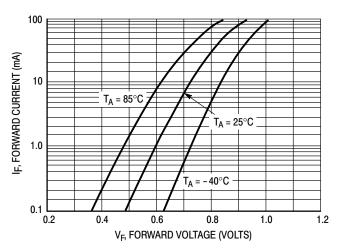
Notes: 1. A 2.0 k Ω variable resistor adjusted for a Forward Current (I_F) of 10 mA.

- 2. Input pulse is adjusted so $I_{R(peak)}$ is equal to 10 mA.
- 3. t_p » t_{rr}

Figure 1. Recovery Time Equivalent Test Circuit

BAW56TT1G, SBAW56TT1G

TYPICAL CHARACTERISTICS



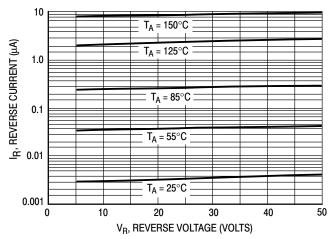


Figure 2. Forward Voltage

Figure 3. Leakage Current

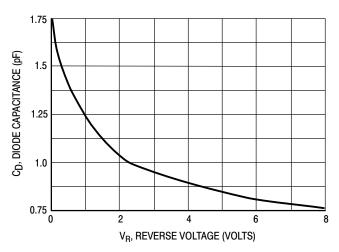


Figure 4. Capacitance

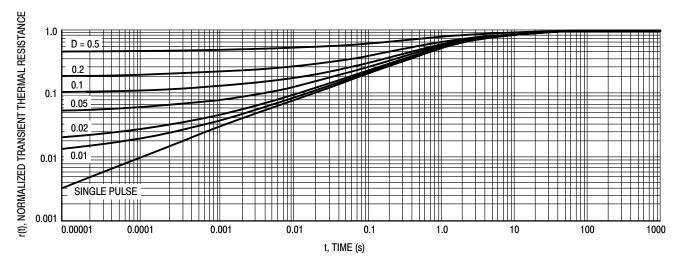


Figure 5. Normalized Thermal Response



SC75-3 1.60x0.80x0.80, 1.00P

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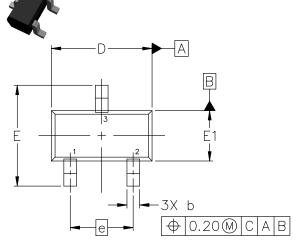
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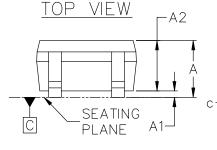
NOTES:

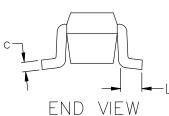
- DIMENSIONING AND TOLERANCING CONFORM TO ASME Y14.5-2018.
- ALL DIMENSION ARE IN MILLIMETERS.

DIM	MILLIMETERS			
	MIN.	NOM.	MAX.	
А	0.70	0.80	0.90	
A1	0.00	0.05	0.10	
A2	0.80 REF.			
b	0.15	0.20	0.30	
С	0.10	0.15	0.25	
D	1.55	1.60	1.65	
Е	1.50	1.60	1.70	
E1	0.70	0.80	0.90	
е	1.00 BSC			
L	0.10	0.15	0.20	

-0.356







SIDE VIEW

GENERIC MARKING DIAGRAM*



XX = Specific Device Code

Μ = Date Code

= Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

STYLE 1:	
PIN 1. BASE	
O EMITTED	

STYLE 4: PIN 1. CATHODE 2. CATHODE 3. ANODE

3. COLLECTOR

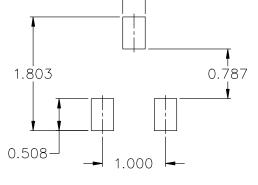
STYLE 2: PIN 1. ANODE 2. N/C 3. CATHODE

STYLE 5: PIN 1. GATE 2. SOURCE 3. DRAIN

STYLE 3: PIN 1. ANODE 2. ANODE 3 CATHODE

RECOMMENDED MOUNTING FOOTPRINT* FOR ADDITIONAL INFORMATION ON OUR Pb-FREE STRATEGY

AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.



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DESCRIPTION:

SC75-3 1.60x0.80x0.80, 1.00P

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