

MOSFET – Power, P-Channel, Single ATPAK

-60 V, -35 A, 29.5 mΩ

ATP113

Features

- ON-Resistance $R_{DS(on)}$ 1 = 22.5 mΩ (typ)
- 4 V Drive
- Protection Diode in
- Input Capacitance C_{iss} = 2400 pF (typ)
- This Device is a Pb-Free and Halogen Free

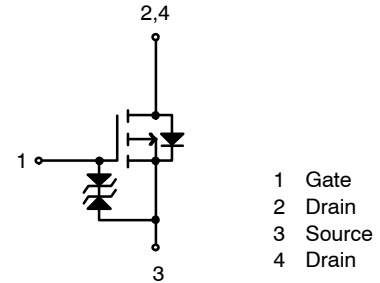
ABSOLUTE MAXIMUM RATINGS (Ta = 25°C) (Note 1)

Parameter	Symbol	Conditions	Value	Unit
Drain-to-Source Voltage	V_{DSS}		-60	V
Gate-to-Source Voltage	V_{GSS}		±20	V
Drain Current (DC)	I_D		-35	A
Drain Current (PW ≤ 10 μs)	I_{DP}	PW ≤ 10 μs, duty cycle ≤ 1%	-105	A
Allowable Power Dissipation	P_D	Tc = 25°C	50	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C
Avalanche Energy (Single Pulse) (Note 1)	E_{AS}		95	mJ
Avalanche Current (Note 2)	I_{AV}		-18	A

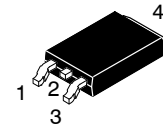
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. $V_{DD} = -10$ V, $L = 500$ μH, $I_{AV} = -18$ A

2. $L \leq 500$ μH, Single pulse

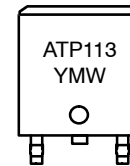


ELECTRICAL CONNECTION



**DPAK (Single Gauge) / ATPAK
CASE 369AM**

MARKING DIAGRAM



ATP113 = Specific Device Code
Y = Year of Production
M = Assembly Operation Month
W = Work Week in the Month

ORDERING INFORMATION

Device	Package	Shipping†
ATP113-TL-H	DPAK / ATPAK (Pb-Free and Halide Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, [BRD8011/D](http://www.onsemi.com/BRD8011/D).

ATP113

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Symbol	Test Condition	Value			Unit
			Min	Typ	Max	
Drain to Source Breakdown Voltage	V _{(BR)DSS}	I _D = -1 mA, V _{GS} = 0 V	-60	-	-	V
Zero-Gate Voltage Drain Current	I _{DSS}	V _{DS} = -60 V, V _{GS} = 0 V	-	-	-1	μA
Gate to Source Leakage Current	I _{GSS}	V _{GS} = +16 V, V _{DS} = 0 V	-	-	+10	μA
Cutoff Voltage	V _{GS(off)}	V _{DS} = -10 V, I _D = -1 mA	-1.2	-	-2.6	V
Forward Transfer Admittance	y _{fs}	V _{DS} = -10 V, I _D = -18 A	-	37	-	S
Static Drain to Source On-State Resistance	R _{DS(on)1}	I _D = -18 A, V _{GS} = -10 V	-	22.5	29.5	mΩ
	R _{DS(on)2}	I _D = -9 A, V _{GS} = -4.5 V	-	27	38	mΩ
	R _{DS(on)3}	I _D = -5 A, V _{GS} = -4 V	-	29	44	mΩ
Input Capacitance	C _{iss}	V _{DS} = -20 V, f = 1 MHz	-	2400	-	pF
Output Capacitance	C _{oss}		-	250	-	pF
Reverse Transfer Capacitance	C _{rss}		-	195	-	pF
Turn-ON Delay Time	t _{d(on)}	See specified Test Circuit.	-	15	-	ns
Rise Time	t _r		-	125	-	ns
Turn-OFF Delay Time	t _{d(off)}		-	250	-	ns
Fall Time	t _f		-	200	-	ns
Total Gate Charge	Q _g	V _{DS} = -30 V, V _{GS} = -10 V, I _D = -35 A	-	55	-	nC
Gate to Source Charge	Q _{gs}		-	7.5	-	nC
Gate to Drain "Miller" Charge	Q _{gd}		-	12	-	nC
Diode Forward Voltage	V _{SD}	I _S = -35 A, V _{GS} = 0 V	-	-0.98	-1.5	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

Switching Time Test Circuit

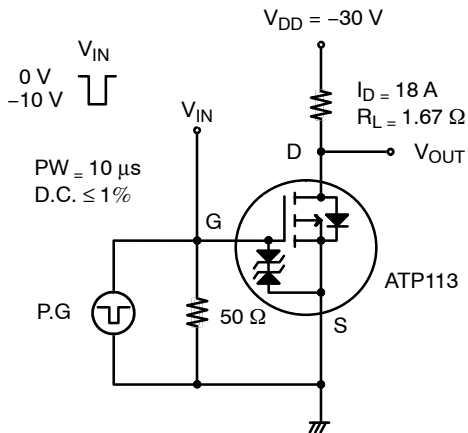


Figure 1. Switching Time Test Circuit

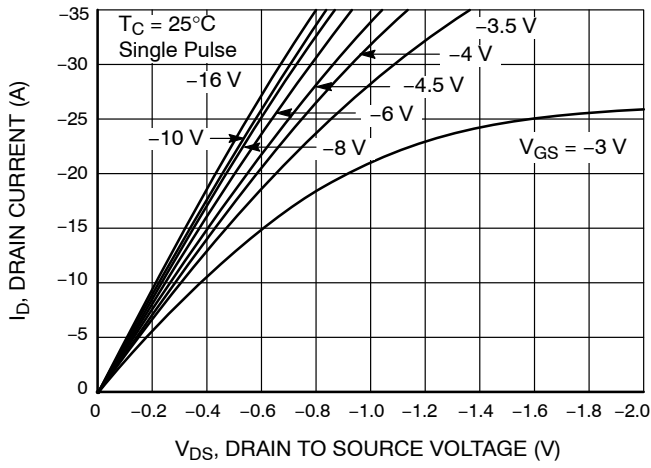


Figure 2. $I_D - V_{DS}$

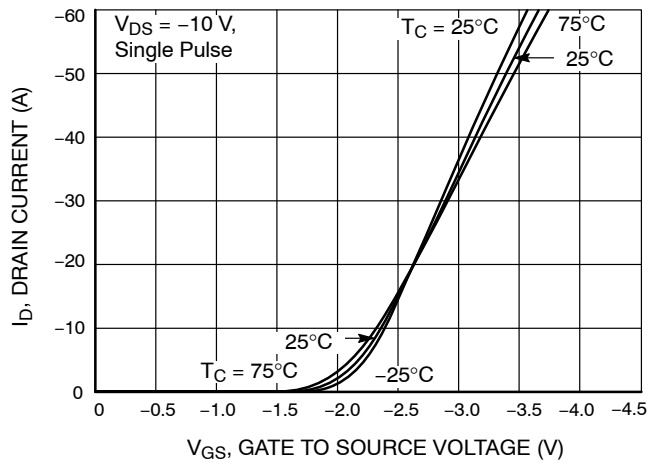


Figure 3. $I_D - V_{GS}$

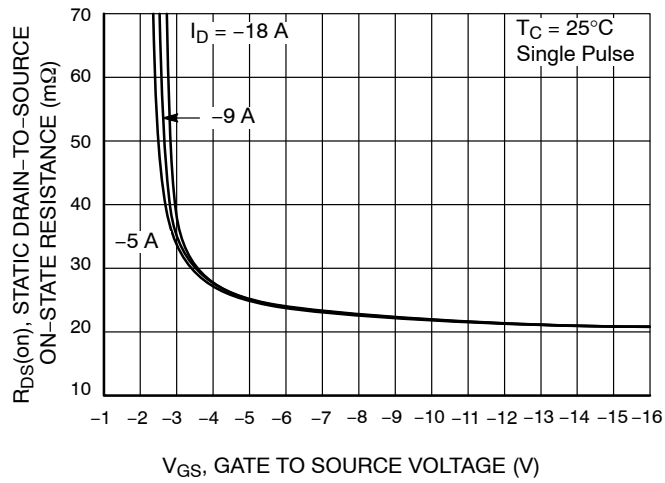


Figure 4. $R_{DS(on)} - V_{GS}$

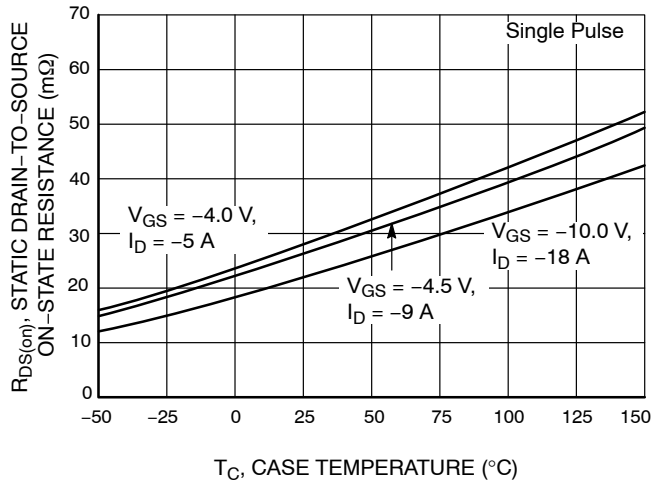


Figure 5. $R_{DS(on)} - T_C$

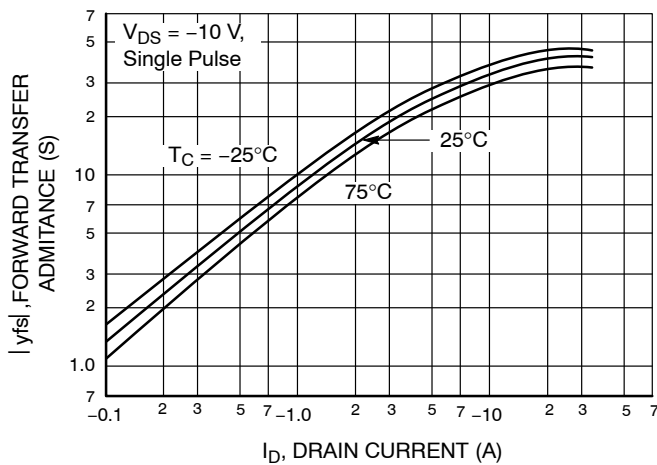


Figure 6. $|y_{fs}| - I_D$

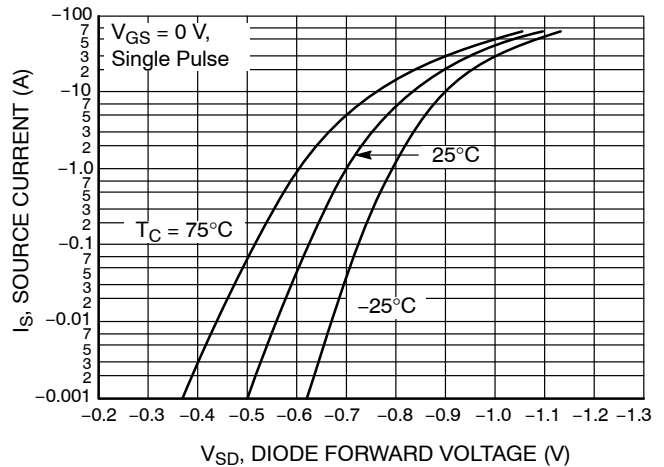


Figure 7. $I_S - V_{SD}$

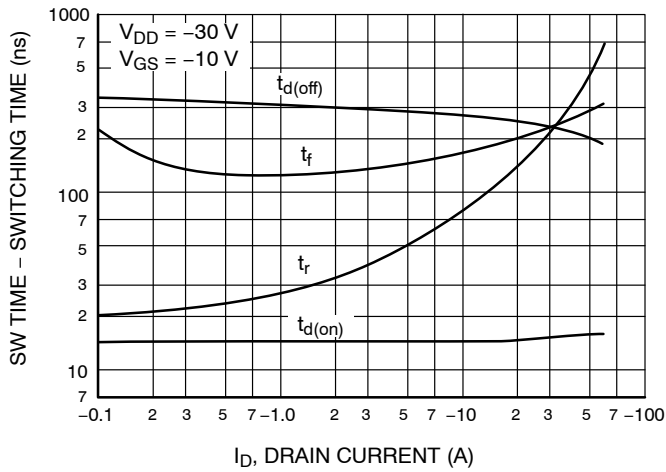


Figure 8. SW Time - I_D

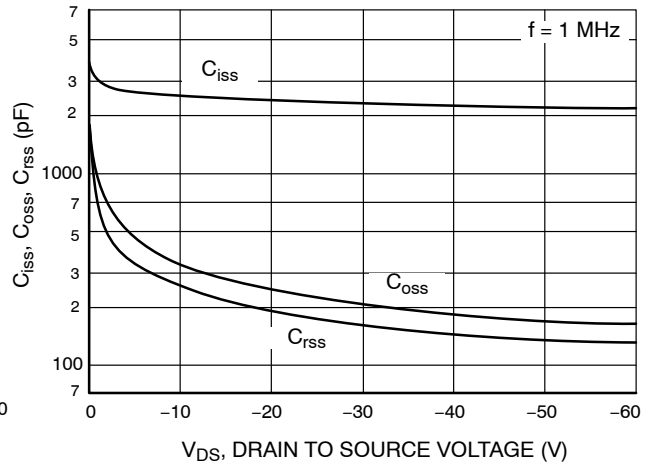


Figure 9. C_{iss} , C_{oss} , C_{rss} - V_{DS}

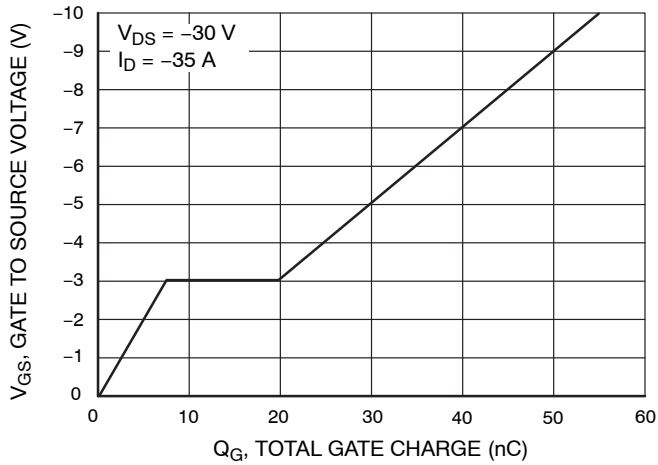


Figure 10. V_{GS} - Q_g

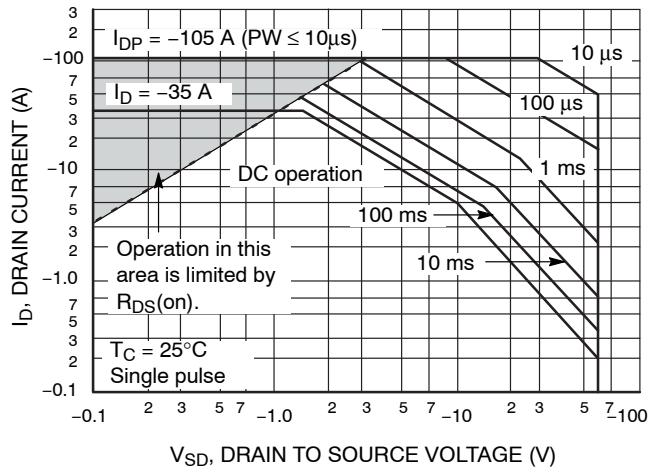


Figure 11. ASO

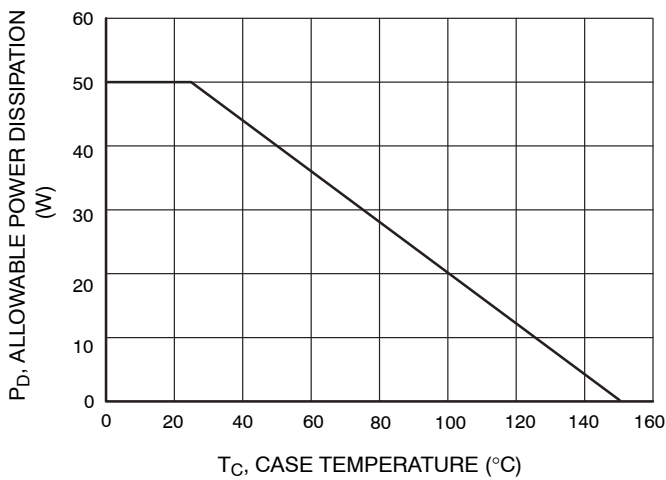


Figure 12. P_D - T_C

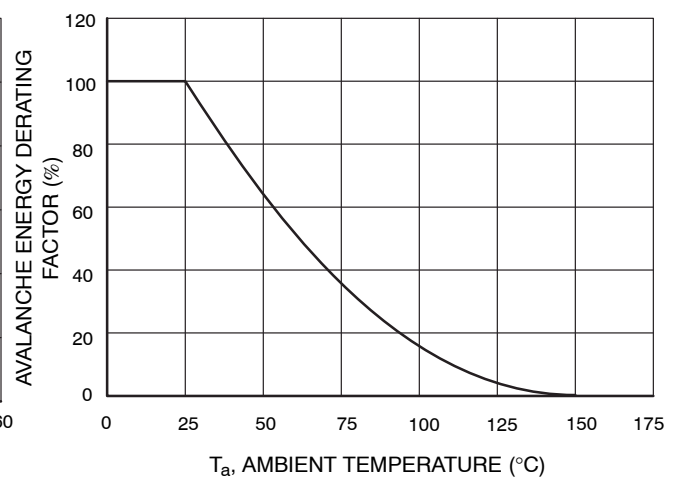


Figure 13. E_{AS} - T_a

ATP113

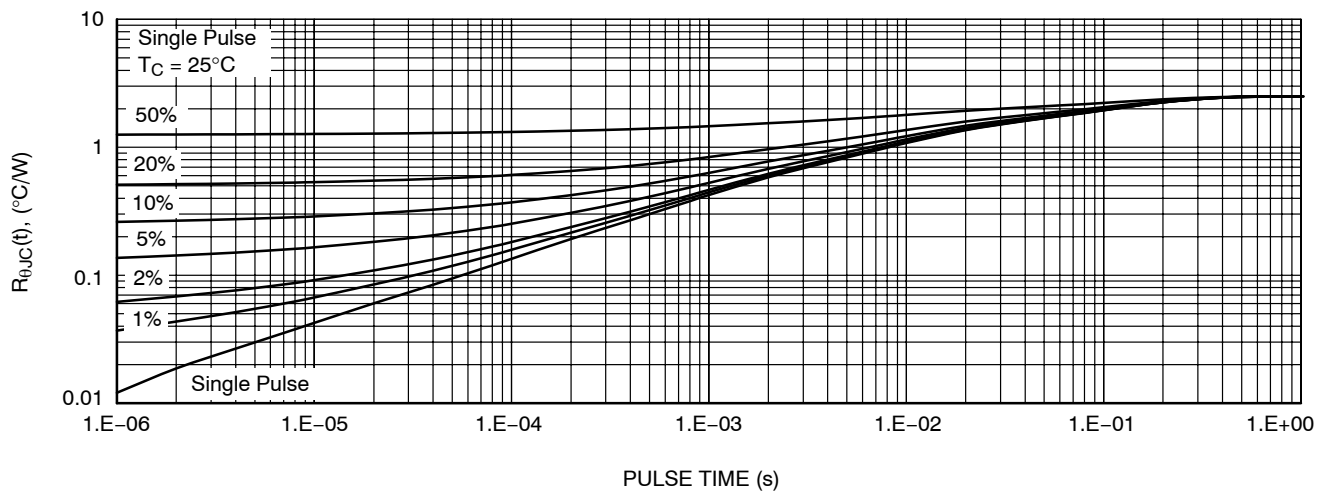
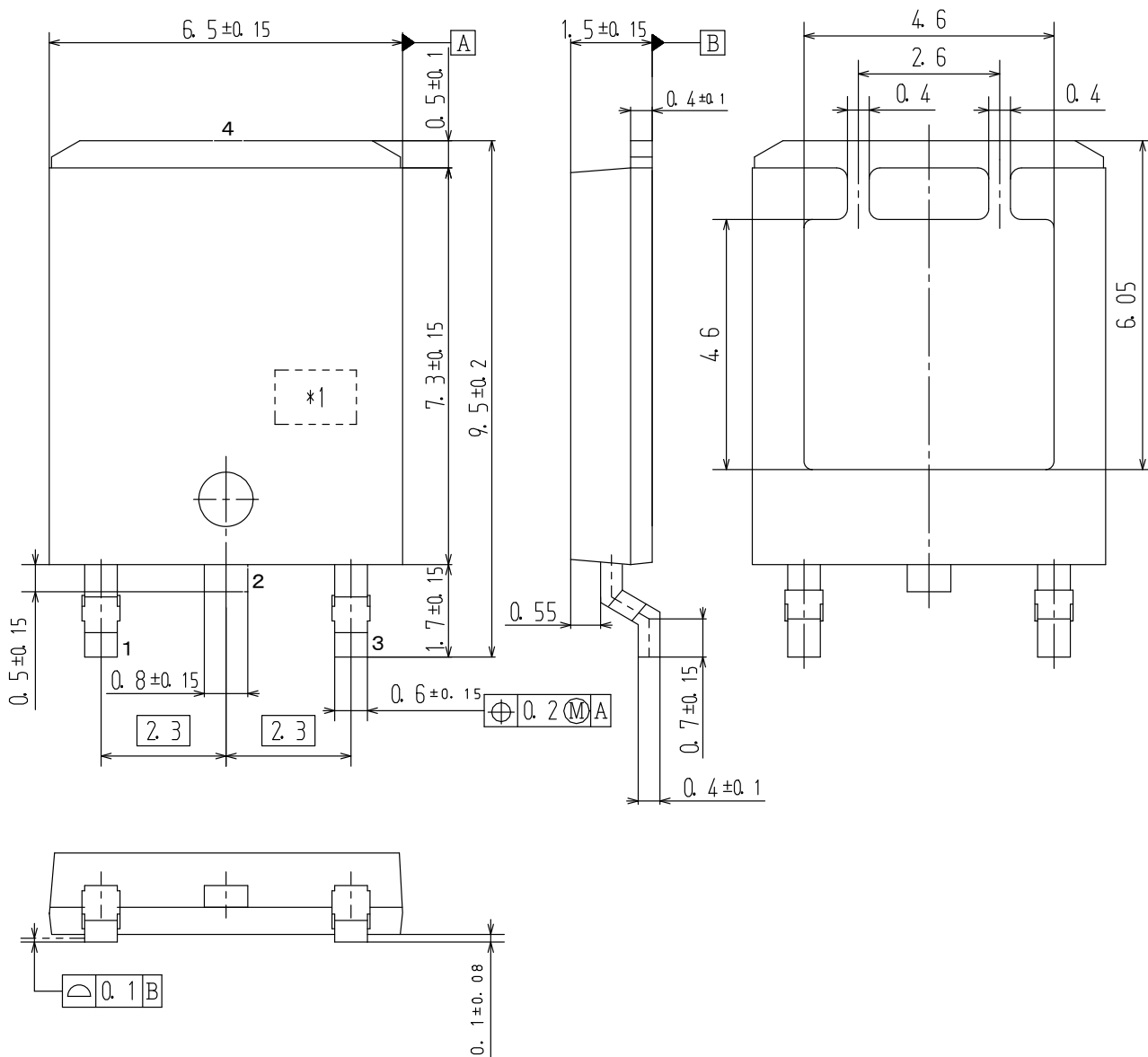


Figure 14. Thermal Response

DPAK (Single Gauge) / ATPAK
CASE 369AM
ISSUE O

DATE 29 FEB 2012



Pin2 is idle pin with electrical
designation only carried

DOCUMENT NUMBER:	98AON67243E	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	DPAK (SINGLE GAUGE) / ATPAK	PAGE 1 OF 1

onsemi and Onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation
onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at
www.onsemi.com/support/sales